

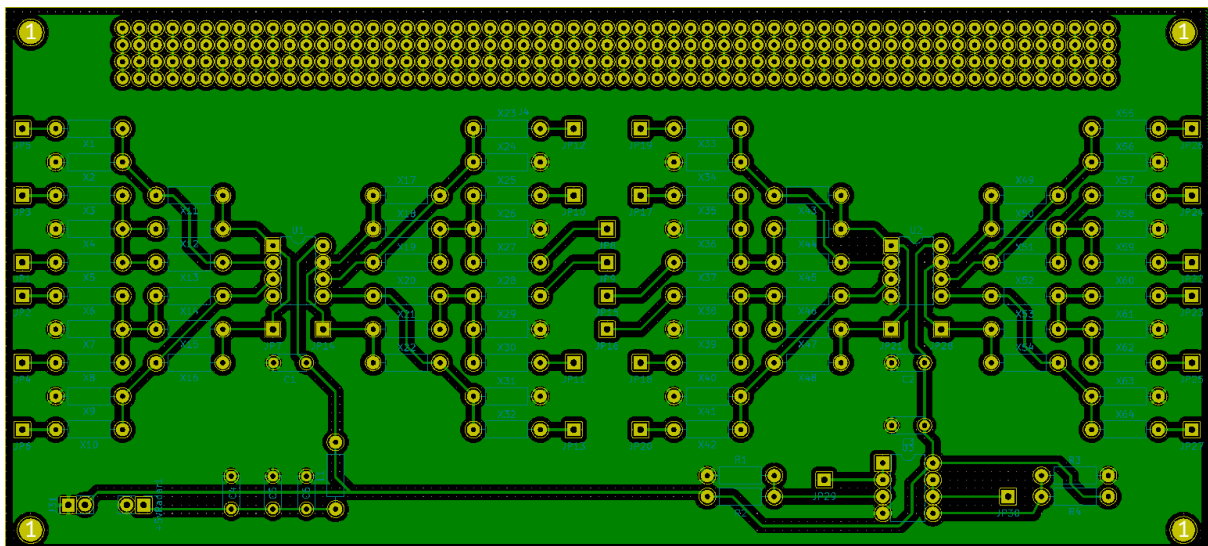
Kicad Keep Out Areas

The Keep Out areas function is used in Kicad to defined area(s) in which a certain feature is avoided in that defined area. It is generally defined before signal path is made.

A proper use of keep out area will help during designing the pcb. A high voltage is usually used in keep out areas where no tracks and copper pour generated in that area. This helps in providing better clearance for the high voltage signal.

How To Add Keep Out Area

In this example, Radar Doppler Template is used with Copper Pour Area is generated in it. The steps are the same as on how to generate a Copper Pour Function.



In Pcbnew, the icon for Keep Out Area is located at the right hand side of the window.

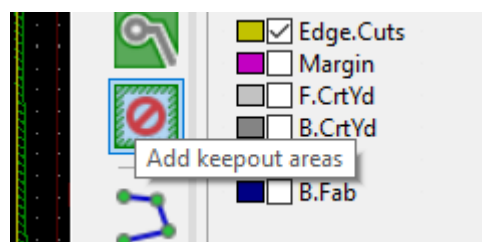


Figure 1 Keep Out Area Icon

Once clicked, a crosshair will appear and click on where you want to start to defined the keep out area. A window will pop out to define the settings.

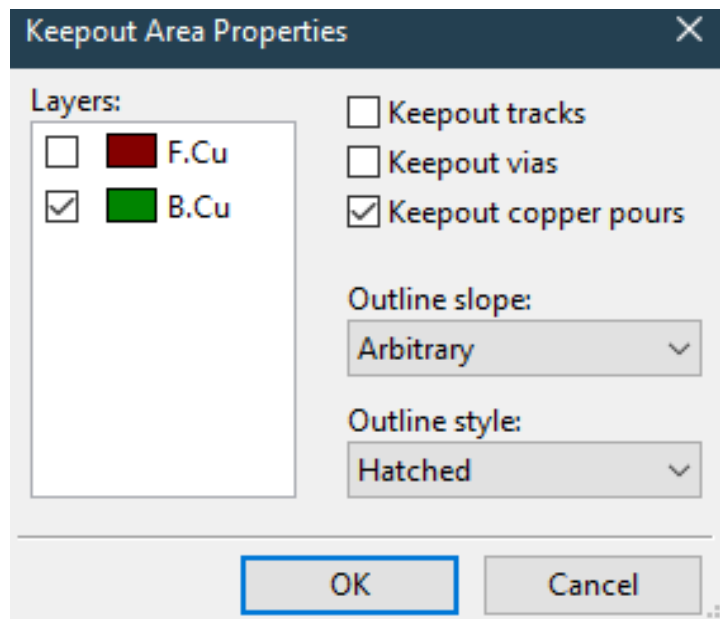


Figure 2 Keep Out Areas Settings

For this example, a keep out area will be generated at a certain place inside the copper pour area. This serve no function in electrical functionality except to provide example for this tutorial.

Click OK and start draw where the area is. Press 'B' to build it and it should be generated.

