

DAY 7-111 DAYS VERIFICATION CHALLENGE

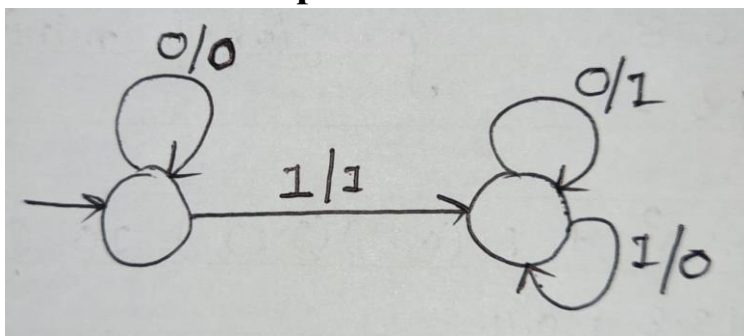
Topic: Sequence detectors

Skill: Digital Electronics

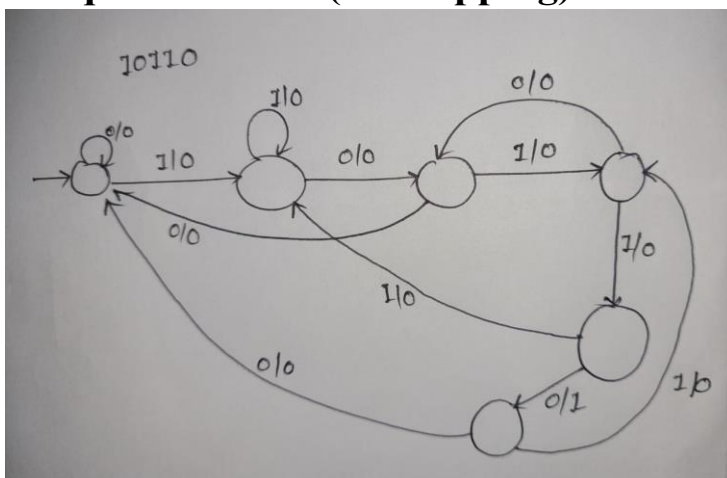
DAY 7 CHALLENGE:

1. Design a Finite state machine for detecting following sequence:

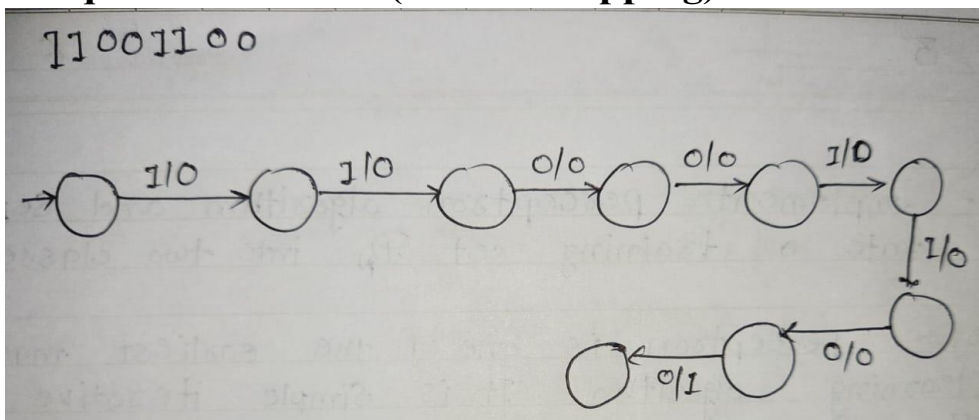
I. serial two's complement block



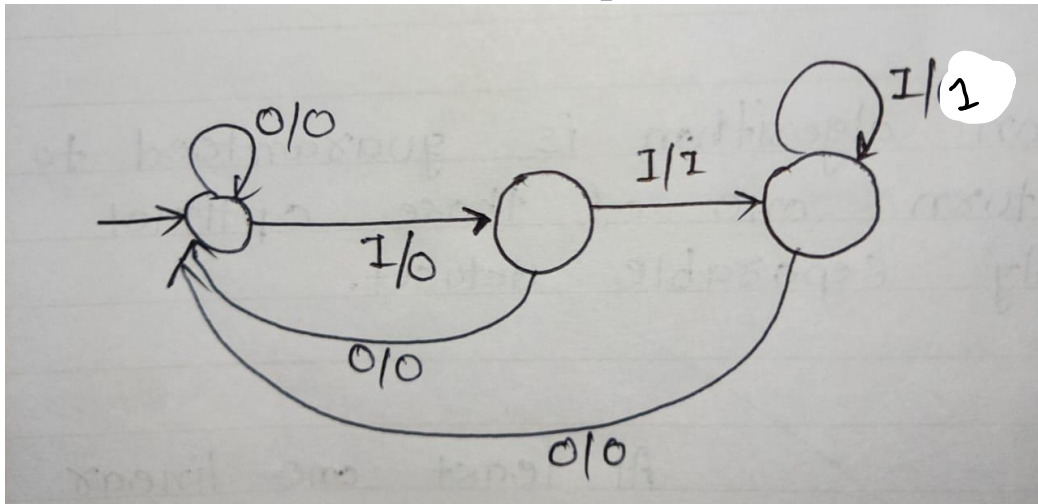
II. a sequence 10110 (overlapping)



III. a sequence 11001100 (non-overlapping)



IV. more than one "1"s in last 3 samples.



V. consequent 4 "0" s (overlapping)

