## **UTTARA UNIVERSITY**

## **Semester Final Exam Schedule (Revised)**

## **Department of Computer Science & Engineering**

**Semester: Spring - 2017** 

	Mon	day 17/0	04/2017 Monday 1	Monday 17/04/2017			Monday 17/04/2017				
Day	Time	Course	Course Title	Batch	Teacher	Room	Column No	Student ID	Invigilator		
		Code				No					
Monday	11:00 AM	CSEC 314	Object Oriented Programming Sessional	BSc 36A Reg	MHZ	LAB 1	ALL	ALL	MHZ+Galib		
17/04/2017	_										
, - ,		CSEC 314	Object Oriented Programming Sessional	BSc 36B+37 Reg	SHS	LAB 2	ALL	ALL	SHS+Perveg		
	01:00 PM										
		CSEC 429	Computer Graphics Sessional	BSc 34 Reg	MSH	LAB 7	ALL	ALL	MSH+Rubel		
		CSEC 214	Electrical and Electronics Technology Sessional	BSc 38A Reg	MHS	LAB 5	ALL	ALL	MHS+Shamim		
		PHY 111	Modern Physics Sessional	BSc 39 Reg	MNH	LAB 4	ALL	ALL	MNH+Perveg		
		CSEC 218	Digital Logic Design Sessional	BSc 38B Reg	TYR	LAB 3	ALL	ALL	TYR+Kaisar		

02:00 PM	CSEC 218	Digital Logic Design Sessional	BSc 38C Reg	TYR	LAB 3	ALL	ALL	TYR+ Kaisar
-	EENG-122	Electrical Circuit Sessional	BSc 40 Reg	MNH	LAB 5	ALL	ALL	MNH+
04:00 PM								Shamim
	CSEC -322	Database Management System Sessional	BSc 35A Reg	UKD	LAB 1	ALL	ALL	UKD+ Galib
	CSEC -322	Database Management System Sessional	BSc 35B Reg	SRS	LAB 2	ALL	ALL	SRS+ Perveg
	CSEC 326	Compiler Construction Sessional.	BSc 33 Reg	CSI	LAB 7	ALL	ALL	CSI+ Rubel

	Tues	day 18/0	04/2017 Tuesday 1	Tuesday 18/04/2017		Tuesday 18/04/2017				
Day	Time	Course Code	Course Title	Batch	Teacher	Room No	Column No	Student ID	Invigilator	
Tuesday	11:00 AM	CSEC -112	Computer System & Application Sessional	BSc 41A Reg	MST	LAB 1	ALL	ALL	MST+ Galib	
18/04/2017	- 01:00 PM	CSEC -112	Computer System & Application Sessional	BSc 41B Reg	SSY	LAB 2	ALL	ALL	SSY+ Perveg	
	02.001	CSEC -112	Computer System & Application Sessional	BSc 41C Reg	SAC	LAB 7	ALL	ALL	SAC+SHS+ Rubel	
	01:00 AM	CSEC -212	Programming Language & Application-II Sessional	BSc 39 Reg	MTR	LAB 1	ALL	ALL	MTR+ Kaisar	

	03:00 PM	CSEC -122	Programming Language & Application-I Sessional	BSc 40 Reg	ALD	LAB2	ALL	ALL	ALD+ Kaisar
		CSEC -112	Computer System & Application Sessional	BSc 41D Reg	TZW	LAB 7	ALL	ALL	TZW+ Shamim
	03:00 PM	CSEC 424	Artificial Intelligence and Expert Systems &	BSc 32 Reg	TSI	LAB 1	ALL	ALL	TSI+ Galib
	05:00 PM		Sessional(Sessional Part)						
		CSEC -317	System Analysis and Design Sessional	BSc 35A Reg	AIA	LAB 2	ALL	ALL	AIA+ Perveg
		CSEC -317	System Analysis and Design Sessional	BSc 35B Reg	MMR	LAB 7	ALL	ALL	MMR+ Rubel
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VV	Carresa	ay 19/02	veunesuay	19/04/201	/	wea	nesday 1	19/04/2	01/
Day	Time	Course Code	Course Title	19/04/201	Teacher	Room No	Column No	Student ID	U1 / Invigilator
Day Wednesday	1	Course	•			Room	-		1
Day	Time	Course Code CSEC -	Course Title  Computer Peripheral, Interfacing &	Batch	Teacher	Room No	Column No	Student ID	Invigilator

BSc 38B Reg

BSc 38A Reg

LAB 5

LAB 3

ALL

ALL

ALL

ALL

MMI

SHC

MMI+ Shamim

SHC+ Kaisar

Electrical and Electronics Technology

Digital Logic Design Sessional

Sessional

CSEC 214

CSEC 218

02:00 PM	CSEC 214	Electrical and Electronics Technology	BSc 38C Reg	SHC	LAB 5	ALL	ALL	SHC+ Shamim
04:00 PM		Sessional						

	Thurs	day 20/	04/2017 Thursday 2	20/04/201	7	Thurs	day 20/0	04/2017	,
Day	Time	Course Code	Course Title	Batch	Teacher	Room No	Column No	Student ID	Invigilator
Thursday	11:00 AM	CSEC -111	Computer System & Application	BSc 41A Reg	MST	301	1,3,5	1-20	SSY+ARM
20/04/2017	-					304	1,3,5	21-45	MTR+TYR
	01:00 PM	CSEC -121	Programming Language & Application-I	BSc 40 Reg	ALD	301	2,4,6	1-18	
						304	2,4,6	19-33	
		CSEC -111	Computer System & Application	BSc 41B Reg	SSY	302	2,4,6	46-70	MST+MNH
						303	2,4,6	71-90	SHS+MHS
		CSEC -211	Programming Language & Application-II	BSc 39 Reg	MTR	302	1,3,5	1-23	
						303	1,3,5	24-49	
		CSEC -111	Computer System & Application	BSc 41C Reg	SAC	601	1,3,5	91-109	TZW+SRS
						602	1,3,5	110-131	MMR+FBA
		CSEC -316	System Analysis and Design	BSc 35A Reg	AIA	601	2,4,6	1-31	

				602	2,4,6	32-50	
CSEC -111	Computer System & Application	BSc 41D Reg	TZW	603	1,3,5	132-138	AIA+IWS
				604	1,3,5	139-143	SHC+MHZ
CSEC -316	System Analysis and Design	BSc 35B Reg	MMR	603	2,4,6	51-80	
				604	2,4,6	81-100	
CSEC 216	Data Structure Sessional	BSc 38B Reg	МАН	LAB 1	ALL	ALL	MAH+Galib
CSEC 228	Operating System Design Sessional	BSc 36A Reg	ALY	LAB 2	ALL	ALL	ALY+Perveg
CSEC 228	Operating System Design Sessional	BSc 36B+37 Reg	SMR	Lab 7	ALL	ALL	SMR+Rubel
CSEC -317	System Analysis and Design Sessional	BSc 34 Reg	МКН	503	ALL	ALL	MKH+Shamim

	Saturday 22/04/2017			Saturday 22/04/2017		S				
Day	Time	Course Code	Course Title		Batch	Teacher	Room No	Column No	Student ID	Invigilator
Saturday	02:30 AM	MATH-110	Differential & Integ	gral Calculus	BSc 41A Reg	МВН	301	1,3,5	1-20	ALY+MTR
22/04/2017	-						304	1,3,5	21-45	SMR+FBA
	04:30 PM	MATH-129	Complex Variable &	Laplace Transforms	BSc 40 Reg	AMU	301	2,4,6	1-18	
							304	2,4,6	19-33	

MATH-110	Differential & Integral Calculus	BSc 41B Reg	МВН	302	2,4,6	46-70	MKH+MHS
				303	2,4,6	71-90	MMR+SRS
ECON 229	Economics	BSc 39 Reg	МНК	302	1,3,5	1-23	
				303	1,3,5	24-49	
MATH-110	Differential & Integral Calculus	BSc 41C Reg	AMU	601	1,3,5	91-109	MBH+CSI
				602	1,3,5	110-131	TSI+UKD
CSEC 324	Measurement & Instrumentation	BSc 35A Reg	SHK	601	2,4,6	1-31	
				602	2,4,6	32-50	
MATH-110	Differential & Integral Calculus	BSc 41D Reg	МВН	603	1,3,5	132-138	SHC+SHS
				604	1,3,5	139-143	TYR+MNH
CSEC 324	Measurement & Instrumentation	BSc 35B Reg	SHK	603	2,4,6	51-80	
				604	2,4,6	81-100	
CSEC 216	Data Structure Sessional	BSc 38A Reg	МАН	Lab 1	ALL	ALL	MAH+Galib
CSEC 216	Data Structure Sessional	BSc 38C Reg	ARM	LAB 2	ALL	ALL	ARM+Perveg
CSEC 433	Computer Communication & Networks Sessional.	BSc 33 Reg	SSY	Lab 7	ALL	ALL	SSY+Rubel

	Mond	lay 24/0	4/2017 Monday	Monday 24/04/2017					
Day	Time	Course Code	Course Title	Batch	Teacher	Room No	Column No	Student ID	Invigilator
Monday	11:00 AM	CSEC 213	Basic Electronics	BSc 38A Reg	MHS	301	1,3,5	1-24	MAH+ARM
24/04/2017	-					304	1,3,5	25-40	SMR+SSY
	01:00 PM	CSEC -316	System Analysis and Design	BSc 34 Reg	МКН	301	2,4,6	1-31	
						304	2,4,6	32-74	
		CSEC 213	Basic Electronics	BSc 38B Reg	MMI	302	2,4,6	41-62	MST+AIA
						303	2,4,6	63-80	IWS+MHZ
		CSEC 325	Compiler Construction	BSc 33 Reg	CSI	302	1,3,5	1-35	
						303	1,3,5	36-62	
		CSEC 213	Basic Electronics	BSc 38C Reg	SHC	601	1,3,5	81-101	TSI+ALY
						602	1,3,5	102-127	мкн+мвн
		CSEC 227	Operating System Design	BSc 36A Reg	ALY	601	2,4,6	1-17	_

			602	2,4,6	18-35	
Operating System Design	BSc 36B+37 Reg	SMR	603	1,3,5	36B batch	SHS+MHS
			604	1,3,5	37 batch	SHC+TYR
igital Signal Processing.	BSc 32 Reg	DMR	603	2,4,6	1-27	
			604	2,4,6	28-56	
				pperating System Design  BSc 36B+37 Reg  603  604  igital Signal Processing.  BSc 32 Reg  DMR  603	Operating System Design         BSc 36B+37 Reg         SMR         603         1,3,5           604         1,3,5           igital Signal Processing.         BSc 32 Reg         DMR         603         2,4,6	Operating System Design         BSc 36B+37 Reg         SMR         603         1,3,5         36B batch           604         1,3,5         37 batch           igital Signal Processing.         BSc 32 Reg         DMR         603         2,4,6         1-27

	Tuesc	lay <b>2</b> 5/0	4/2017 T	Tuesday 25/04/2017	Tuesday 25/04/2017				
Day	Time	Course	Course Title	Batch	Teacher	Room	Column No	Student ID	Invigilator
		Code				No			
Tuesday	11:00 AM	ENG -100	Communication Skill	BSc 41A Reg	IWS	301	1,3,5	1-20	IWS+ARM
25/04/2017 -	-					304	1,3,5	21-45	SMR+TYR
	01:00 PM	MATH-123	Discrete Mathematics	BSc 40 Reg	TZW	301	2,4,6	1-18	
						304	2,4,6	19-33	
		ENG -100	Communication Skill	BSc 41B Reg	IWS	302	2,4,6	46-70	TZW+MAH
						303	2,4,6	71-90	SSY+MST
		ACTG 121	Accounting	BSc 39 Reg	ABQ	302	1,3,5	1-23	
						303	1,3,5	24-49	

ENG -100	Communication Skill	BSc 41C Reg	IWS	601	1,3,5	91-109	SRS+SHC
							AIA+MMR
				602	1,3,5	110-131	
CSEC -321	Database Management System	BSc 35A Reg	UKD	601	2,4,6	1-31	
				602	2,4,6	32-50	
ENG -100	Communication Skill	BSc 41D Reg	IWS	603	1,3,5	132-138	UKD+FBA
				604	1,3,5	139-143	MHZ+SHS
CSEC -321	Database Management System	BSc 35B Reg	SRS	603	2,4,6	51-80	
				604	2,4,6	81-100	_
CSEC 499	Project II	BSc 30 Reg	CSI,MKH	LAB 1	ALL	ALL	CSI,MKH

Wednesday 26/04/2017			Wednesday 26/04/2017			Wednesday 26/04/2017				
Day	Time	Course Code	Course Title	E	Batch	Teacher	Room No	Column No	Student ID	Invigilator
Wednesday	11:00 AM	CSEC 215	Data Structure	E	BSc 38A Reg	МАН	301	1,3,5	1-24	CSI+MTR
26/04/2017	-						304	1,3,5	25-40	SSY+ ARM
	01:00 PM	CSEC 428	Computer Graphics	E	BSc 34 Reg	MSH	301	2,4,6	1-31	

				304	2,4,6	32-74	
CSEC 2	5 Data Structure	BSc 38B Reg	ARM	302	2,4,6	41-62	MAH+SMR
				303	2,4,6	63-80	MST+TSI
CSEC -	Computer Peripheral, Interfacing &	BSc 33 Reg	DMR	302	1,3,5	1-35	_
318	Maintenance			303	1,3,5	36-62	
CSEC 2	5 Data Structure	BSc 38C Reg	ARM	601	1,3,5	81-101	ALY+MNH
				602	1,3,5	102-127	MKH+TYR
CSEC -	Computer Peripheral, Interfacing &	BSc 36A Reg	DMR	601	2,4,6	1-17	
318	Maintenance			602	2,4,6	18-35	
CSEC -	Computer Peripheral, Interfacing &	BSc 36B+37 Reg	MST	603	1,3,5	36B batch	FBA+MHZ
318	Maintenance			604	1,3,5	37 batch	SHS+MHS
CSEC 412	Management Information Systems	BSc 32 Reg	SHK	603	2,4,6	1-27	
				604	2,4,6	28-56	

Thursday 27/04/2017			Thursday 27/04/2017			Thursday 27/04/2017				
Day	Time	Course	Course Title		Batch	Teacher	Room	Column No	Student ID	Invigilator
		Code					No			

Thursday	11:00 AM	STAT -121	Statistics & Probability	BSc 41A Reg	FBA	301	1,3,5	1-20	MHS+FBA
27/04/2017	-					304	1,3,5	21-45	SSY+SMR
	01:00 PM	EENG-121	Electrical Circuit	BSc 40 Reg	MNH	301	2,4,6	1-18	
						304	2,4,6	19-33	
		STAT -121	Statistics & Probability	BSc 41B Reg	MHZ	302	2,4,6	46-70	MHZ+TSI
						303	2,4,6	71-90	MMR+MAH
		PHY 110	Modern Physics	BSc 39 Reg	MNH	302	1,3,5	1-23	
						303	1,3,5	24-49	
		STAT -121	Statistics & Probability	BSc 41C Reg	MMR	601	1,3,5	91-109	SRS+AIA
						602	1,3,5	110-131	MNH+TYR
		CSEC 323	Data Communication	BSc 35A Reg	SRS	601	2,4,6	1-31	
						602	2,4,6	32-50	
		STAT -121	Statistics & Probability	BSc 41D Reg	FBA	603	1,3,5	132-138	MST+ALY
						604	1,3,5	139-143	MKH+MTR
		CSEC 323	Data Communication	BSc 35B Reg	ALY	603	2,4,6	51-80	
						604	2,4,6	81-100	

9	Saturda	y 29/04,	/2017 Saturday 29/	04/2017	Sat				
Day	Time	Course Code	Course Title	Batch	Teacher	Room No	Column No	Student ID	Invigilator
Saturday	02:30 AM	CSEC 217	Digital Logic Design	BSc 38A Reg	SHC	301	1,3,5	1-24	CSI+MBH
29/04/2017	-					304	1,3,5	25-40	
	04:30 PM	CSEC 325	Compiler Construction	BSc 34 Reg	VMA	301	2,4,6	1-31	
						304	2,4,6	32-74	ARM+MAH
		CSEC 217	Digital Logic Design	BSc 38B Reg	TRY	302	2,4,6	41-62	MST+TSI
						303	2,4,6	63-80	
		CSEC 432	Computer Communication & Networks	BSc 33 Reg	SSY	302	1,3,5	1-35	
						303	1,3,5	36-62	UKD+MTR
		CSEC 217	Digital Logic Design	BSc 38C Reg	TYR	601	1,3,5	81-101	TZW+MKH
						602	1,3,5	102-127	
		CSEC 313	Object Oriented Programming	BSc 36A Reg	MHZ	601	2,4,6	1-17	
						602	2,4,6	18-35	MNH+ALY

CSEC 313	Object Oriented Programming	BSc 36B+37 Reg	SHS	603	1,3,5	36B batch	MMR+SRS
				604	1,3,5	37 batch	
CSEC 424	Artificial Intelligence and Expert Systems & Sessional(Theory Part)	BSc 32 Reg	TSI	603	2,4,6	1-27	AIA+IWS
	Sessional(Theory Farty			604	2,4,6	28-56	
CSEC 399	Project I	BSc 31 Reg	ALD,SHS	LAB 1	ALL	ALL	ALD,SHS