

# Arduino and Digital to Analog Converter (DAC)

Author: Nakarin Jayjong

In this tutorial, the demonstration of using a Arduino and Digital to Analog Converter (DAC) as a analog signal generator is presented. The AD5308 DAC and Arduino Nano are used to demonstrated.

## Arduino

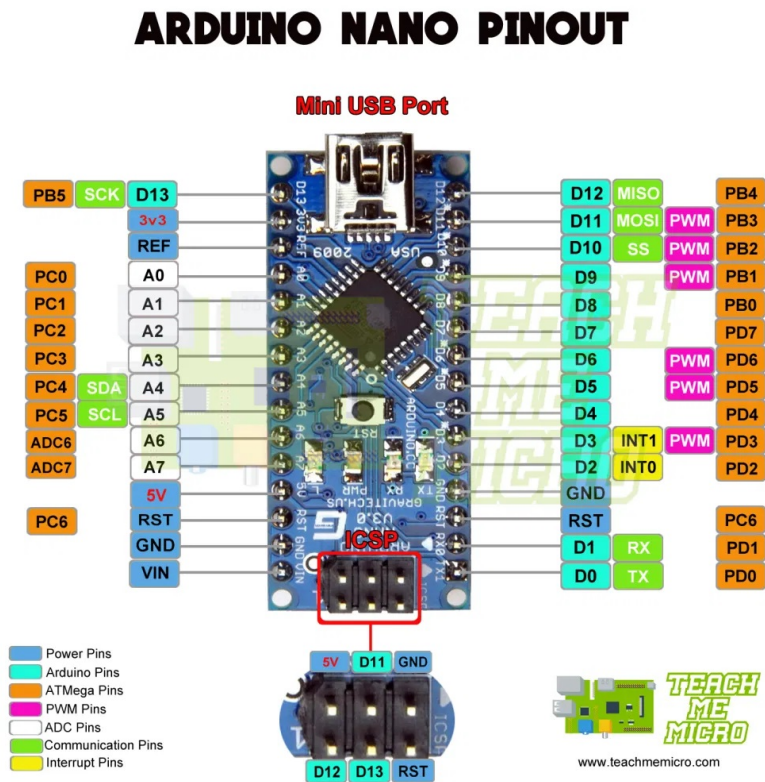


Figure 1. Arduino NANO pinout diagram

The important Arduino pins for this application (SPI communication) are the following:

- D10 This is just a normal digital pin of Arduino. This pin will be used to trigger the  $\overline{\text{SYNC}}$  of DAC for the frame synchronization signal when this pin is LOW. Alternatively, this pin can be replaced by other digital pin of Arduino
- D11 (MOSI) This is the "Master Out Slave In" pin.
- D13 (SCK) This pin is serial clock. This pin is connected with a built-in LED. When pin 13 is HIGH, the LED is on and when pin 13 is LOW, it is off. This LED can be used to check if the clock is ticking.

## Digital to Analog Converter (DAC)

Digital to Analog Converter (DAC) is a device that allows to translate numeric values into analog signals, so that it can have output voltages variable from 0 to the reference voltage.

The important mnemonics and pins the following:

- $\overline{\text{LDAC}}$  This pin is used to allows any or all DAC registers to be updated when pulse this pin low. Alternatively, this pin can be tied permanently low (tied to GND).
- $\overline{\text{SYNC}}$  This is the frame synchronization signal for the input data. When  $\overline{\text{SYNC}}$  goes low, it powers on the SCLK and DIN buffers and enables the input shift register
- SCLK Serial clock input. Data is clocked into the input shift register on the falling edge of the serial clock input.
- DIN Serial Data Input. This device has a 16-bit shift register. Data is clocked into the register on the falling edge of the serial clock input.
- $V_{\text{REF}}$  Reference input pin for DACs
- $V_{\text{OUT}}$  Buffered analog output voltage

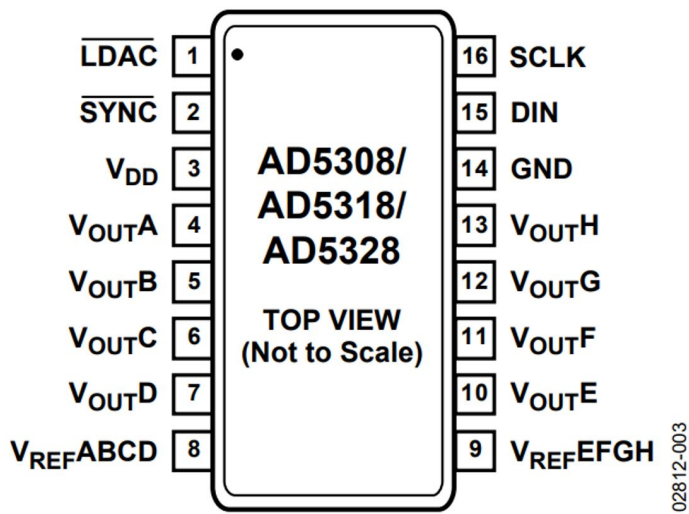


Figure 2. DAC (AD5308) pinout diagram

### Serial Interface

In the case of a DAC write, the MSB is a 0. The Address Bits for the AD5308 can be defined by using three binary numbers ranging from 000 to 111 as shown in the Figure 3. The data bits can be represented using eight binary numbers, and LSB should be set to 0 (0000)

A2 (Bit 14)	A1 (Bit 13)	A0 (Bit 12)	DAC Addressed
0	0	0	DAC A
0	0	1	DAC B
0	1	0	DAC C
0	1	1	DAC D
1	0	0	DAC E
1	0	1	DAC F
1	1	0	DAC G
1	1	1	DAC H

Figure 3. Address Bits for the AD5308

**Example:** In this example, the output of DAC is chosen to be  $(175/255) \times V_{ref} = 0.69V_{ref}$  on the pin  $V_{OUTC}$ , therefore the Address Bits is "010" and the Data Bits is "1010 1111" where MSB is 0 and LSB is "0000". The binary number that will be sent to DAC is "0 010 1010 1111 0000" which can be written in hex number as "0x2AF0" as shown in Figure 4

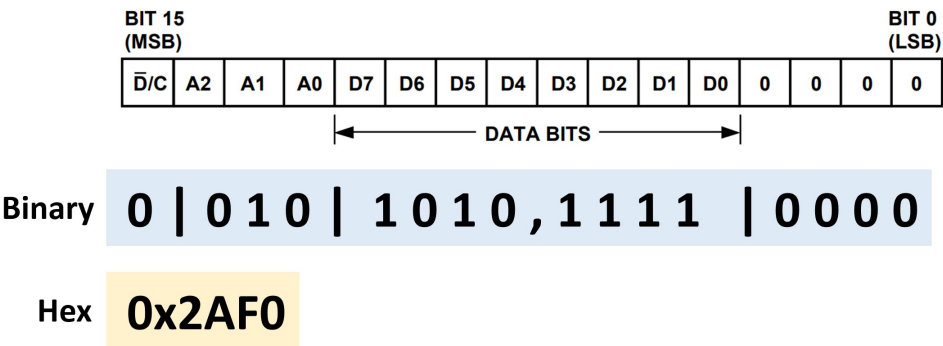


Figure 4. Serial Interface of AD5308

# Serial Interface Timing

The serial interface timing for DAC are the following:

- The  $\overline{\text{SYNC}}$  pin need to trigger by a pulse to activate the DAC. The minimum high time for  $\overline{\text{SYNC}}$  is 50 ns for the AD5308 as shown on CH1 in Figure 5
- The clock will start to tick after falling of the  $\overline{\text{SYNC}}$  pulse as shown on CH2 in Figure 5
- After the clock is ticking, the Data Bits will be sent to the DAC as shown on CH3 in Figure 5

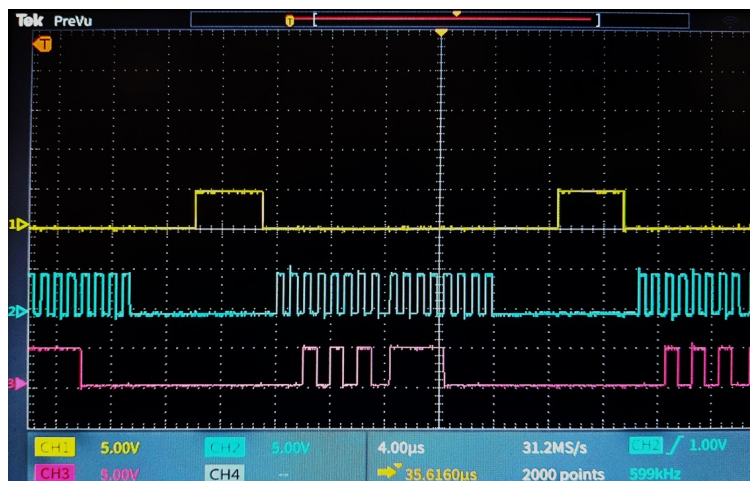
## Arduino script

This is a Arduino script for varying the analog output of DAC.

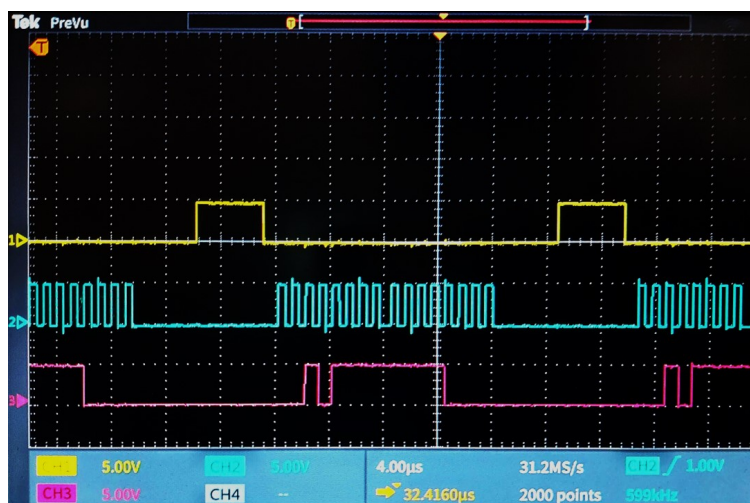
```

1  #include <SPI.h>
2
3  void setup() {
4      SPI.begin();
5      SPI.beginTransaction(SPISettings(16000000, MSBFIRST, SPI_MODE1));
6      pinMode(10, OUTPUT);
7  }
8  void loop() {
9      digitalWrite(10, LOW);
10     SPI.transfer16(0x2AF0);
11     digitalWrite(10, HIGH);
12 }

```



**Figure 5.** CH1 is  $\overline{\text{SYNC}}$ , CH2 is SCLK pin, and CH3 is DIN. The Data Bits which is sent to DAC is "0x2AF0" where Address Bits is "010" = "2" and the Data Bits = "1010111" = "AF".



**Figure 6.** CH1 is  $\overline{\text{SYNC}}$ , CH2 is SCLK pin, and CH3 is DIN. The Data Bits which is sent to DAC is "0x2AF0" where Address Bits is "010" = "2" and the Data Bits = "1111111" = "FF".

```

1  #include <SPI.h>
2
3  void setup() {
4      SPI.begin();
5      SPI.beginTransaction(SPISettings(16000000, MSBFIRST, SPI_MODE1));
6      pinMode(10, OUTPUT);
7  }
8
9  void update_dac(uint16_t ch, uint16_t num) {
10     // ch is the Channel of the output, num is the DAC number ranging from 0 to 255 (00 to FF)
11     digitalWrite(10, LOW);
12     SPI.transfer16((ch << 12) | (num << 4));
13     digitalWrite(10, HIGH);
14 }
15
16 void loop() {
17     update_dac(2, 175);
18 }

```

## Reduction of the $\overline{\text{SYNC}}$ pulse

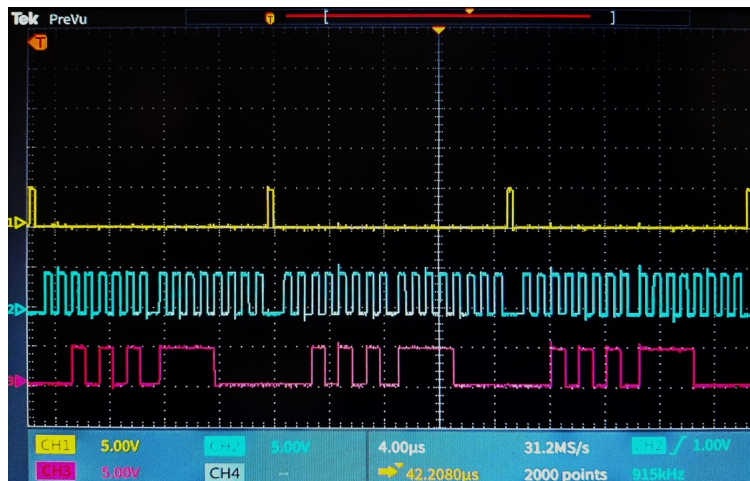
In last section the  $\overline{\text{SYNC}}$  pulse is generated by the "digitalWrite" function of Arduino. The Arduino, however, need some time to communicate to the chip (ATMega) on the Arduino board. For this reason, changing the PORT register directly can reduce the pulse delay which is generated.

In this example we use pin 10 of Arduino which is equivalent to PB2 of ATMega pin and we can set the pin to LOW and HIGH using the Port Register Control trick as shown in Figure . For more details see [1].

```

1  #include <SPI.h>
2
3  void setup() {
4
5      SPI.begin();
6
7      SPI.beginTransaction(SPISettings(16000000, MSBFIRST, SPI_MODE1));
8      pinMode(10, OUTPUT);
9  }
10
11
12 void update_dac(uint16_t Port_ATMega, uint16_t ch, uint16_t num) {
13     // Port_ATMega is a ATMega pin of the Arduino which is used to SYNC, ch is the Channel of the
14     // DAC output, num is the DAC number ranging from 0 to 255 (00 to FF)
15     // In this case pin 10 of Arduino is used and this corresponds to PORTB 2 (PB2) on ATMega pin
16     PORTB&= ~(1<<Port_ATMega); // Turn off the PB2
17     SPI.transfer16((ch << 12) | (num << 4)); // Transfer Address Bits and Data Bits
18     PORTB|= (1<<Port_ATMega); // Turn on the PB2
19 }
20
21 void loop() {
22     update_dac(2,2,175);
23 }
24

```



**Figure 7.** CH1 is  $\overline{\text{SYNC}}$ , CH2 is SCLK pin, and CH3 is DIN. The Data Bits which is sent to DAC is "0x2AF0" where Address Bits is "010" = "2" and the Data Bits = "1010111" = "AF".

```
PORTB&= ~(1<<Port_ATMega); // Turn off the PB2 Turn off
SPI.transfer16((ch << 12) | (num << 4)); // Transfer Address Bits and Data Bits
PORTB|= (1<<Port_ATMega); // Turn on the PB2 Turn on
```

## Turn off

PB2 = 0100

Shift bit 1 by 2 (1<<2) → 0001 to 0100. We need to shift by 2 because we use PB2.

~ (1<<2) → ~(0100) = 1011

**PORTB&= ~(1<<Port\_ATMega)** → (0100 & 1011) = (0000) so the pin PORTB is off.

## Turn on

PB2 now is = 0000 (turn off)

Shift bit 1 by 2 (1<<2) → 0001 to 0100. We need to shift by 2 because we use PB2.

**PORTB|= (1<<Port\_ATMega)** → (0000 | 0100) = (0100) so the pin PORTB is on.

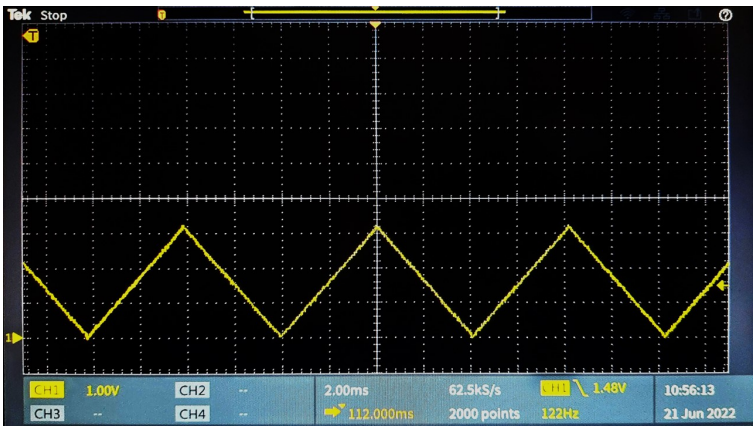
Note: The underline denote the target bit

**Figure 8.** Port Register Control for increasing speed of writing of digital pin of Arduino

## Ramping the DAC output

The following script is a Arduino script for ramping the DAC output

```
1  #include <SPI.h>
2  int t_ramping = 4; // Ramping time in milisecond
3  int step_t = 80; // The total step in ramping
4
5
6  void setup() {
7      SPI.begin();
8      SPI.beginTransaction(SPISettings(16000000, MSBFIRST, SPI_MODE1));
9      pinMode(10, OUTPUT);
10     // Increase the total step to 150 when the t_ramping > 10 milisecond to smooth the output
11     // For t_ramping < 10 milisecond the step_t need to be smaller due to the fact that the
12     // time delay in each step will never smaller than 2 microseconds
13     if (t_ramping > 10){
14         int step_t = 150; // The total step in ramping
15     }
16 }
17 void update_dac(uint16_t Port_ATMega, uint16_t ch, uint16_t num) {
18     // Port_ATMega is a ATMega pin of the Arduino which is used to SYNC, ch is the Channel of
19     // the DAC output, num is the DAC number rangibg from 0 to 255 (00 to FF)
20     // In this case pin 10 of Arduino is used and this corresponds to PORTB 2 (PB2) on ATMega
21     PORTB&= ~(1<<Port_ATMega);
22     SPI.transfer16((ch << 12) | (num << 4));
23     PORTB|= (1<<Port_ATMega);
24 }
25 void dac_ramp( uint16_t dac_start, uint16_t dac_end){
26     int step_run = 0;
27     int dac_run = dac_start; // DAC number which is ranging from dac_start to dac_end
28     int tmp;
29     // Each DAC update need 2 microseconds
30     int time_step = ((float)t_ramping/(int)step_t)*1000-2; // convert into microseconds
31     if(dac_start > dac_end)
32         tmp = -((int)(dac_start-dac_end)/step_t);
33     else
34         tmp = (dac_end-dac_start)/step_t;
35
36     while(step_run<step_t){
37         step_run+= 1;
38         dac_run += tmp;
39         update_dac(2, 2,dac_run);
40         delayMicroseconds(time_step);
41     }
42 }
43 void loop() {
44     // Ramp up from 0 to 250
45     dac_ramp(250, 0);
46     // Ramp down from 250 to 0
47     dac_ramp( 0, 250);
48 }
```



**Figure 9.** The analog output generated from the ramping code.

**References**

[1] Electronoobs, “Port register control | increase speed of read/write - arduino101.” <https://youtu.be/UhTRrjYXqPU>, 2020.