

TMS9900 IS BIG ENDIAN  
ALL OTHER CHIPS SETUP AS LITTLE ENDIAN

X'0000' TO X'0002' = RESET VECTOR  
X'0004' TO X'003E' = INTERRUPT VECTORS  
X'0040' TO X'007E' = TRAP VECTORS  
X'0080' TO X'FFFA' = GENERAL MEMORY  
    X'0080' TO X'0FFE" = CONSOLE ROM (2K WORDS / 4K BYTES)  
    X'1000' TO X'1FFE" = LOCAL REGISTER RAM (2K WORDS / 4K BYTES)  
    X'2000' TO X'FEFE" = GENERAL RAM (28K WORDS / 56K BYTES)  
    X'FF00' TO X'FFFA" = MMIO ADDRESS SPACE (126 WORDS / 252 BYTES)  
X'FFFC' TO X'FFFE' = LOAD SIGNAL VECTOR

MMIO MAP  
X'FF00' = Output Rx Data onto Data Bus  
X'FF02' = Reset Rx Data Ready Line  
X'FF04' = Put THE onto Data Bus bit 0  
X'FF08' = Set THL to load value on data bus for Tx

