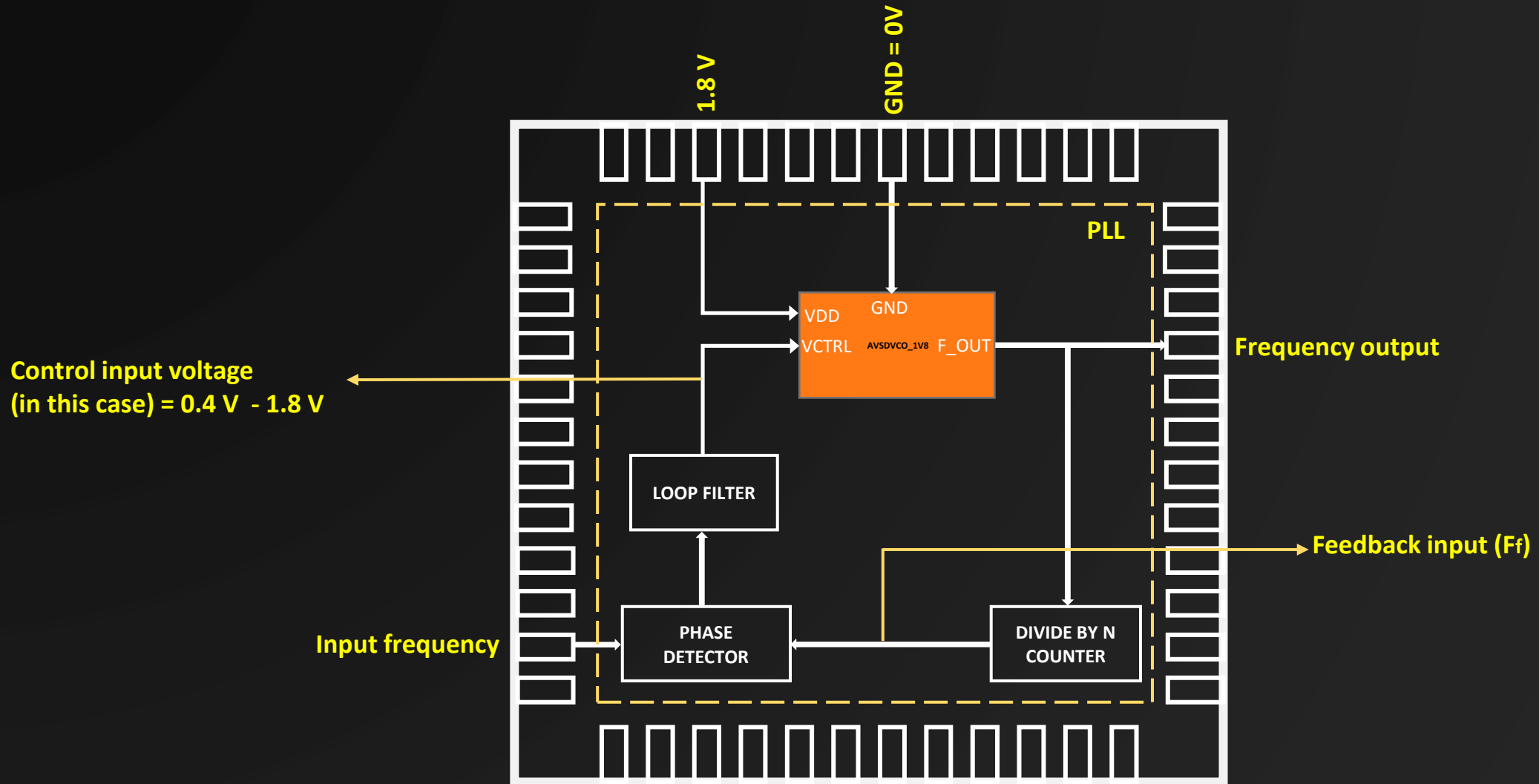


VCO (avsdvco_1v8)

TARGET SPECIFICATIONS

NALINKUMAR S

APPLICATION NOTE FOR AVSDVCO_1V8



AVSDVCO_1V8 – Preferred dimension , pin location and metal layer



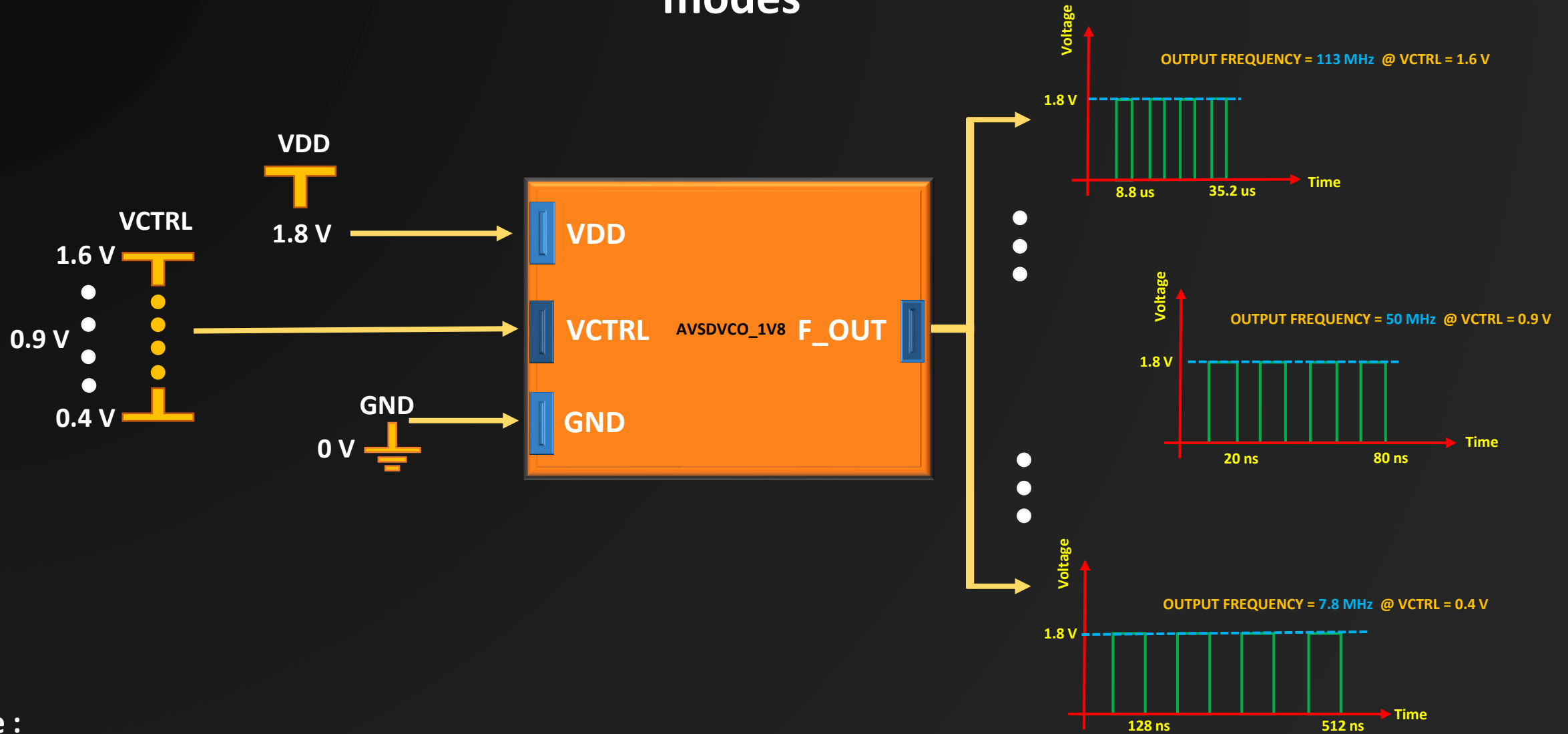
 VDD & GND PINS

 VCTRL & F_OUT PINS

AVSDVCO_1V8 – Operating modes



AVSDVCO_1V8 – Operating modes



Note :

All graphs are rough-sketched diagrams drawn for reference.

AVSDVCO_1V8 – Plots & Value needed

1. VCO OUTPUT FREQUENCY (F_OUT) Vs CONTROL VOLTAGE (VCTRL) @ VDD = 1.8 V & TEMP = 27 °C
2. BIAS STAGE CURRENT Vs CONTROL VOLTAGE (VCTRL) @ VDD = 1.8 V & TEMP = 27 °C
3. VCO OUTPUT FREQUENCY (F_OUT) Vs BIAS VOTAGE (VDD) @ VCTRL = 0.9 V & TEMP = 27 °C
4. TRANSIENT ANALYSIS (AMPLITUDE Vs TIME) @ VDD = 1.8 V , VCTRL = 0.9 V & TEMP = 27 °C