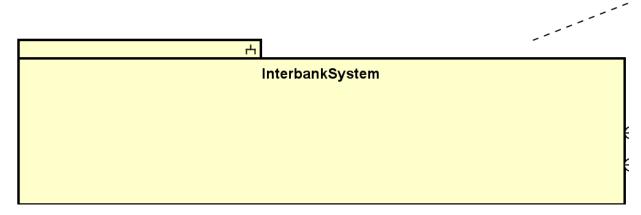
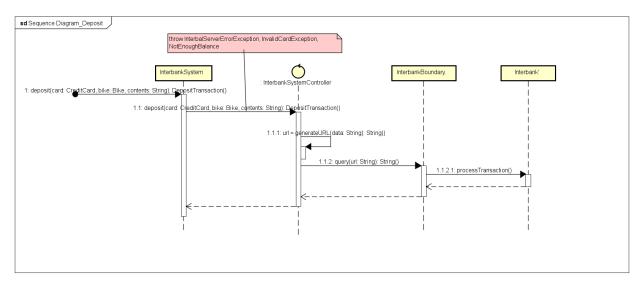
## Thiết kế giao diện hệ thống

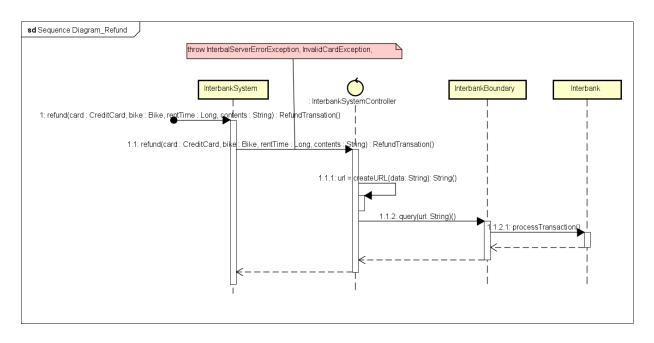
#### 1 Thiết kế Interface cho Subsystem

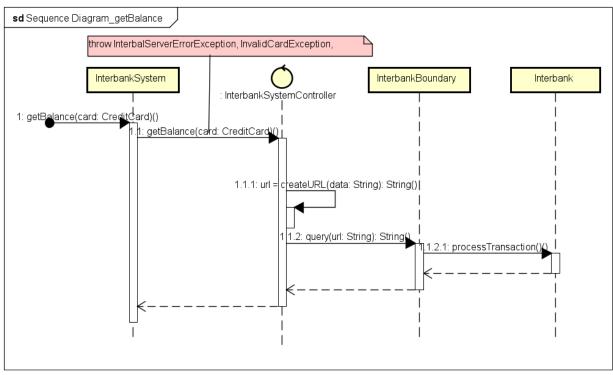
## 

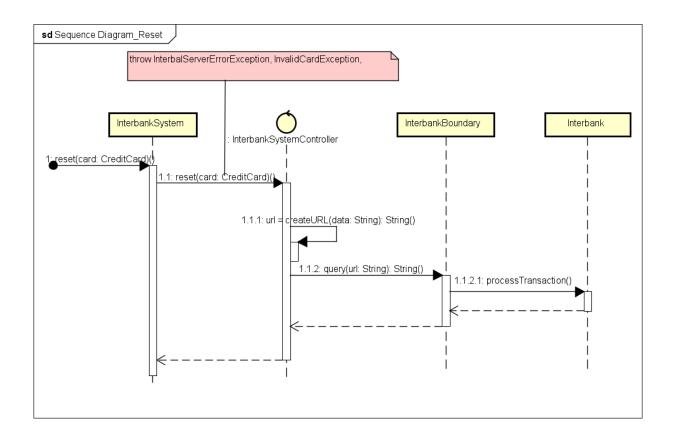


#### 2 Sequence diagram









# 3 Thiết kế chi tiết lớp

