

NAME: NAMAN GROYAL  
Roll No. 180123029

*N. Goyal*

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A1) In order issue is enforced because we want to get the next instruction from the FIFO queue. and when an instruction is issued, a reservation station is allocated to it that stores information abt instruction and buffers the operand value as it becomes available that helps in avoiding WAR hazards.

If we keep the issue also out of order then ~~then~~ there can be some instructions that are issued but the earlier instruction may not be issued which itself can cause hazards and thus stalls. and may be our final result/output may also get affected - so major problems are the above mentioned.