

# Nameer Iqbal Ansari



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<https://github.com/Nameer-Iqbal-Ansari>

## Summary

Nameer Iqbal Ansari is a undergraduate student from UIT(Usman Institute of Technology) in the field of Computer Systems Engineering (BE) involved in RISC-V based processor technology learning in MERL and doing hands on practice of processor making, testing and verification. Academic subjects like Digital Logic Design and Computer Architecture have developed his interest in processor technology. Internet of things(IoT), Artificial intelligence(AI), Block chain and embedded Systems, etc. are also the fields of his interests.

## Experience

### Intern

Micro Electronics Research Lab

Mar 2021 - Present (1 year 1 month +)

Working as a trainee in MERL(Micro electronic research lab). Learned implementations of digital logic design and computer architecture have made RISC-V architecture based 32 bit I-type processor coded in SystemVerilog (HDL).

## Education



**S.M.PUBLIC Academy**

Matriculation, Computer Science

2015 - 2017



**ADAM JEE government science college.**

Intermediate, Pre-Engineering

2017 - 2019



**Usman Institute of Technology**

Bachelor of Engineering - BE, Computer Systems

2019 - 2023

## Skills

C++ • Logisim • SystemVerilog • Assembly Language • Verilog • YAML • AutoCAD • Adobe Photoshop • Fusesoc • Self-learner