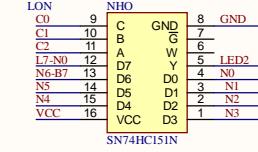
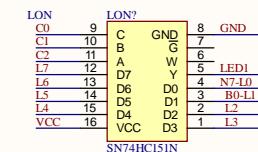
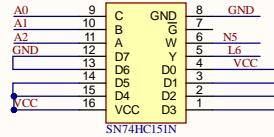
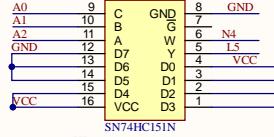
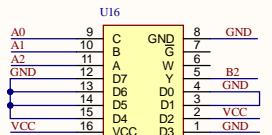
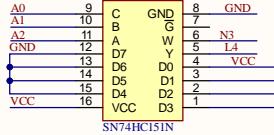
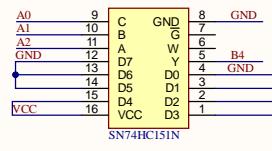
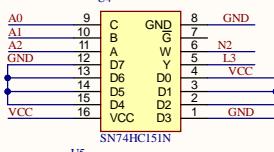
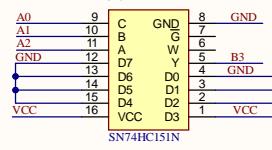
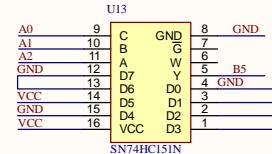
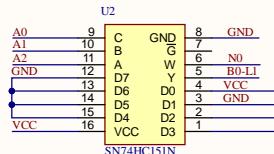
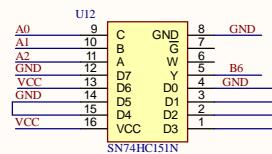
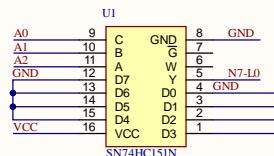


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REVISION	DESCRIPTION	DATE	APPROVED



APPROVALS	DATE	PROJECT	Altium		
ENG: .					
DSN: .		DOCUMENT REVISION:	Not in version control		
CHK: .		DESIGN ITEM:	Not in version control		
REFERENCE DOCUMENTS			TITLE *		
ASSY DWG:			REV		
FAB DWG:			SIZE A3		
PCB DWG:			SCALE:	FILE NAME	Main_Logiv.SchDoc
			SHEET	3	OF 3

Line #	Name	Description	Designator	Quantity	Manufacturer 1	Manufacturer Part Number 1	Manufacturer Lifecycle 1	Supplier 1	Supplier Part Number 1	Supplier Unit Price 1	Supplier Subtotal 1
	0.33uF	Cárm	C1	1							
	M7	Dán		1							
	101	Dán	C2	1							
	LED 0805	Dán	L2, L3, L4	3							
	LED 0805	Dán	L5, L6, L7	3							
	LED 0805	Dán	L8, L9, L10	3							
	10k	Dán	R1, R2, R3	3							
	SN74HC151N	Integrated Circuit	BANG, BANG1, LON1, LON?, NHO, NHO1, U1, U2, U3, U4, U5, U6, U7, U8, U9, U10, U11, U12, U13, U14, U15, U16, U17	34							
	JACKDC 5.5	JACKDC 5.5	JACKDC1	1							
	L7805CDT-TR	Linear Voltage Regulator IC 1 Output 1.5A DPAK	L1	1							
	A6DR-6100	Switch	S1	1							