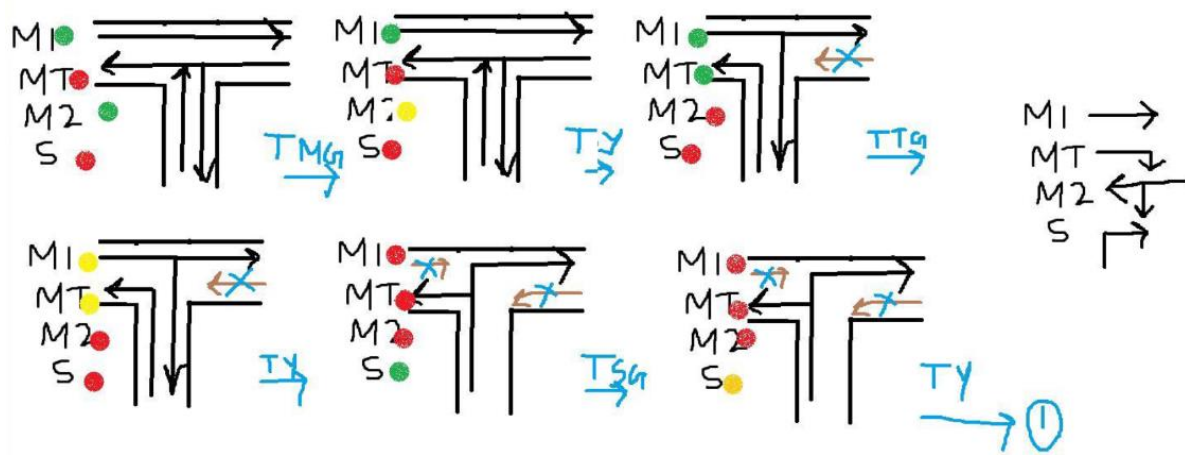
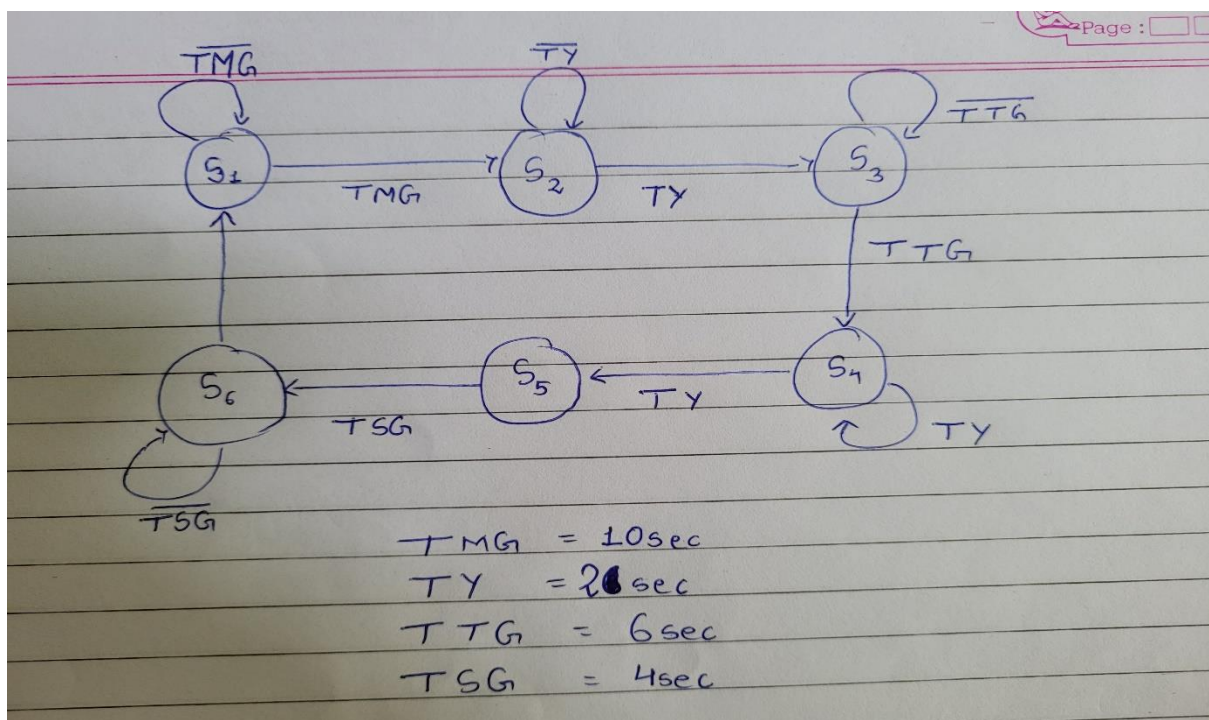


The aim of the project is to design a traffic controller for a T-intersection



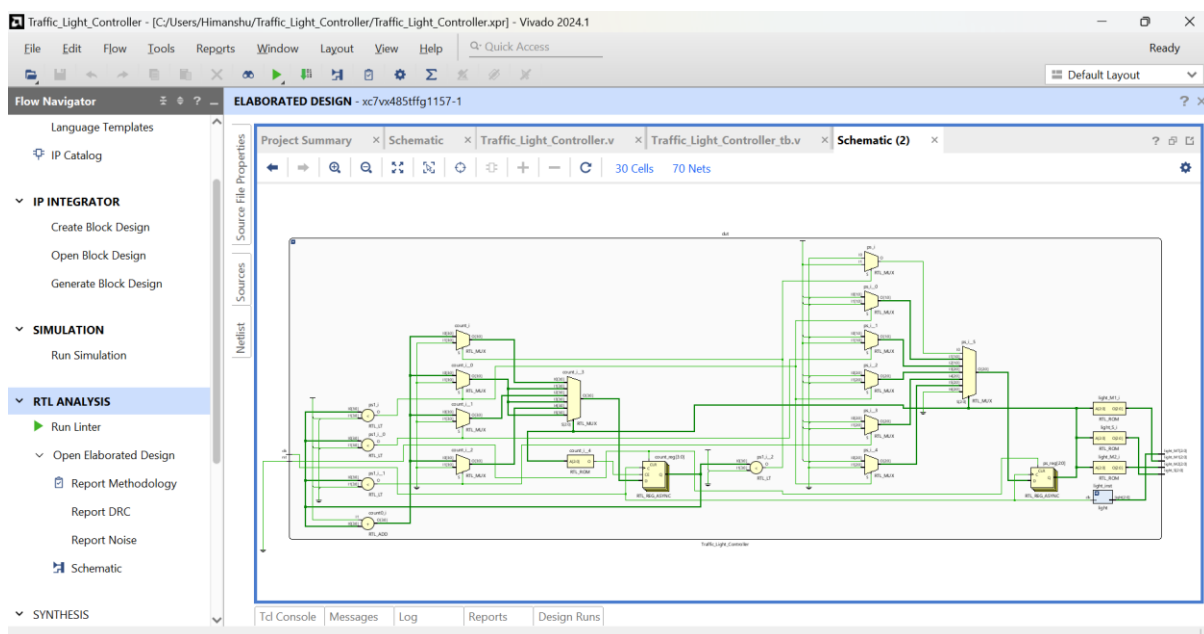
These six states will be used as six cases and the diagram of these cases is given below:



## Transition Table

Present State	Input	Next State	M1	M2	MT	S
ABC		$A*B*C*$				
000	-	001	000	000	000	000
001	$\overline{TMG}$	001	001	001	100	100
	TMG	010				
010	$\overline{TY}$	010	001	010	100	100
	TY	011				
011	$\overline{TTG}$	011	001	100	001	100
	TTG	100				
100	$\overline{TY}$	100	010	100	010	100
	TY	101				
101	$\overline{TSG}$	101	100	100	100	001
	TSG	110				
110	$\overline{TY}$	110	100	100	100	010
	TY	001				
111	-	000	000	000	000	000

## RTL – SCHEMATIC



# SIMULATED WAVEFORM

