Anticlockmin # include celdio h> # include Seg51.h7 Char xdata porta at 0x800; char xdata port at 0x803°, char ¿data acc at 0x30°, elelay ()?

Firty,

glar()=0; gc800cg++) usid mais () 3 char port, acc, porta; post = 0x80; post = 0x80;

white(1)?

acc = 6x88;

bosta = acc;

delay ();

acc = 0x44;

posta = acc;

delay();

acc = 0x22;

delay();

posta = acc;

delay();