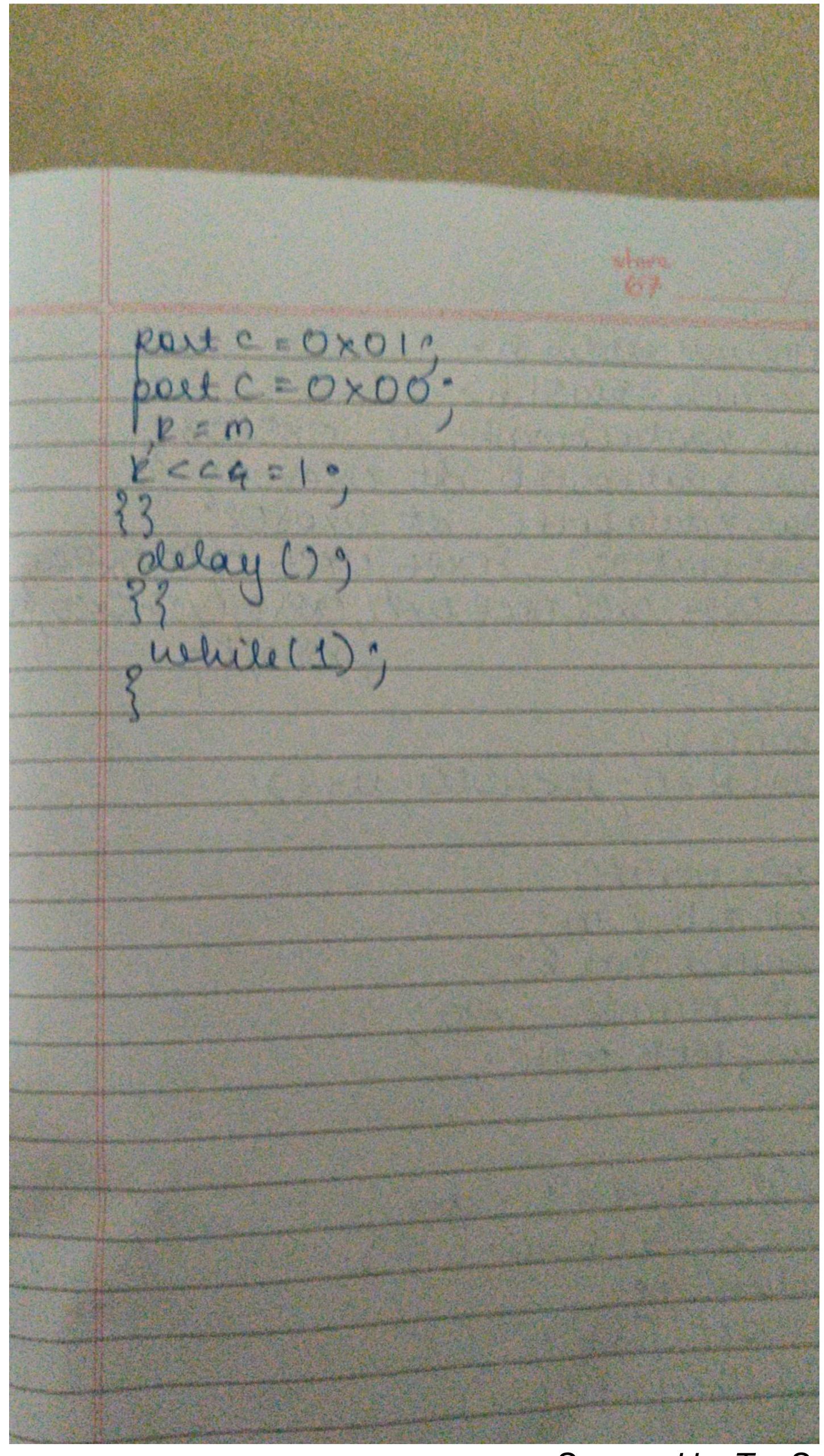
```
# include cetalin by
  # include < reg 51.h>
   char xdata ammw_at_oxe803;
   char x data port B det oxe801; char x data port C_at_oxe802;
    Char post (20) = 80xff, 0xff, 0xff, 0xff, 0xff
0x83, 0x88, 0x88, 0x88, 0x82, 0x88, 0xc7, 0xc0, 0x86
0x863, 80
  delay 0;

3 long u;

for (d=0; uc4000; u++);
    word main()
   Fint d, b, é, m;
uneigned charke
  char commw=6x80;
char post B, poet C.
port B=0x00;
```

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