

NANDAN U

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Career Objective

Start career as a **Trainee Engineer** in Electronics and Communication, utilizing skills in **embedded systems, RTOS, firmware development, ARM programming, microcontrollers, and microprocessor applications** to contribute to innovative and reliable electronic solutions.

Education

B.E. – Electronics and Communication Engineering , VVIET, Mysuru	2021–2025
CGPA: 8.71/10 (87.11%)	
PUC – Daksha PU College, Mysuru	2019–2021, 89.66%
SSLC – Sri Vasavi Vidya Kendra, Kollegala	2018–2019, 86.40%

Technical Skills

Hardware: Circuit assembly, soldering, PCB prototyping, testing and debugging

Embedded Programming: C, C++, Embedded C, RTOS, ARM, Microprocessor, Firmware, Python, Verilog

Tools: Keil μ Vision, MultiSIM, MATLAB, CST Studio, Git, Linux, Cadence

Microcontrollers: Arduino, ESP32, NodeMCU

Soft Skills: Communication, Teamwork, Problem Solving, Adaptability

Experience

Embedded Systems Trainee – Emertxe, Bangalore	Jun 2025 – Present
Training in Linux internals, Embedded C, RTOS, ARM programming, microcontroller projects, and firmware development (Skill India, NSDC certified). Projects include automotive communication, firmware, and debugging.	
Intern – VLSI Design Engineer (Skill India, ESSCI)	Oct 2024 – May 2025
780 hours training in RTL design, verification, and simulation; exposure to physical design; Certified NSQF Level 5.	
Intern – Organization for the Development of People (ODP), Mysuru	Nov – Dec 2023
Conducted seminars on cancer awareness; studied sustainable development and microfinance models.	

Projects

ECOMATIC – IoT Waste Collection Robot (KSCST)

Autonomous robot using image + weight sensors for waste segregation. [Report](#) | [KSCST](#) | [Images](#)

FlexiHand Assist Glove – Motion-sensing prosthetic glove with real-time feedback.

8-bit Magnitude Comparator (Verilog) – RTL design verified in UVM; GDS-II via OpenROAD.

Helical Antenna – CubeSat – 2.45 GHz RF antenna in CST; published in IJCRT. [Cert](#) | [Report](#)

Dielectric Antenna – 5G – 15 GHz high-gain DRA (10.4 dBi); optimized bandwidth; published in IJCRT. [Cert](#) | [Report](#)

Certifications

- **VLSI Design Engineer** – Rooman Technology
- **VLSI Design Engineer** – Skill India, ESSCI, NSDC, PMKVY
- **Life Skills (Jeevan Kaushal) 2.0** – Wadhvani Foundation
- **Embedded Systems** – NIELIT
- **Cybersecurity** – Infosys Springboard
- **Python** – NIELIT
- **UVM + RTL** – IEEE CASS
- **ML using Python** – NIELIT
- **MATLAB** – NIELIT

Languages

English, Kannada, Telugu, Hindi

Achievements

- Runner-up – Project Expo (IEEE MCE ComSoc Chapter)
- Papers published – NCSSPES & IJCRT, 2024 ([link](#))