

A

B

C

D

A

B

C

D

VALVEBOARD_Top.SchDoc

1

2

3

4

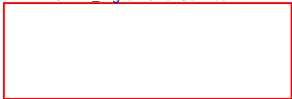
1

2

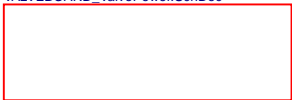
3

4

DigitalPower
VALVEBOARD_DigitalPower.SchDoc



ValvePower
VALVEBOARD_ValvePower.SchDoc

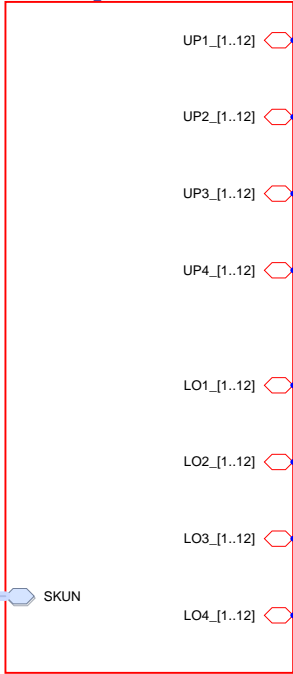


DigitalInterface
VALVEBOARD_DigitalInterface.SchDoc



SKUN

Controller
VALVEBOARD_Controller.SchDoc



UP1_[1..12]

UP2_[1..12]

UP3_[1..12]

UP4_[1..12]

LO1_[1..12]

LO2_[1..12]

LO3_[1..12]

LO4_[1..12]

UP1 [1..12]

UP2 [1..12]

UP3 [1..12]

UP4 [1..12]

LO1 [1..12]

LO2 [1..12]

LO3 [1..12]

LO4 [1..12]

UP1

UP2

UP3

UP4

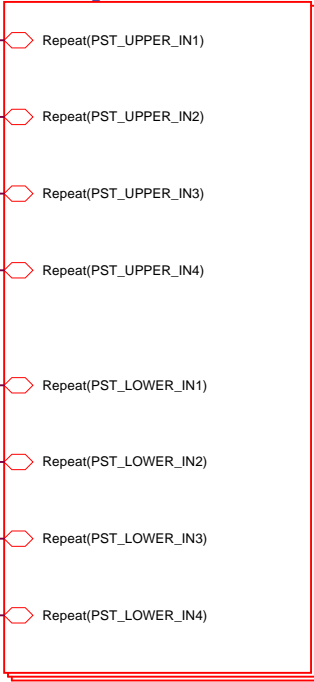
LO1

LO2

LO3

LO4

Repeat(PostIsolation,1,12)
VALVEBOARD_PostIsolation.SchDoc



Repeat(PST_UPPER_IN1)

Repeat(PST_UPPER_IN2)

Repeat(PST_UPPER_IN3)

Repeat(PST_UPPER_IN4)

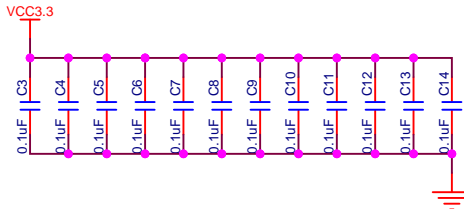
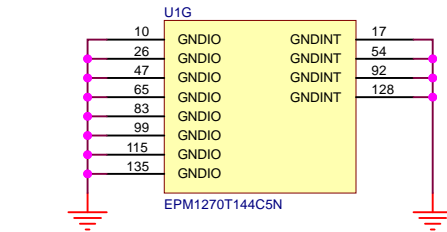
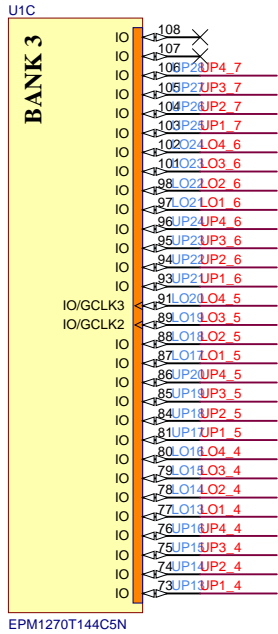
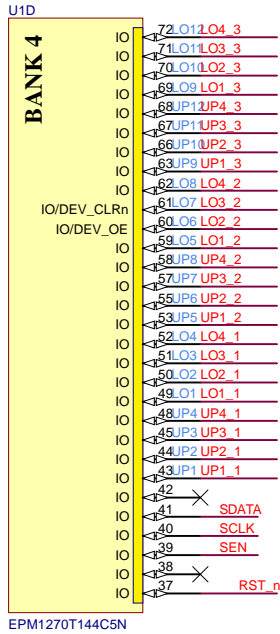
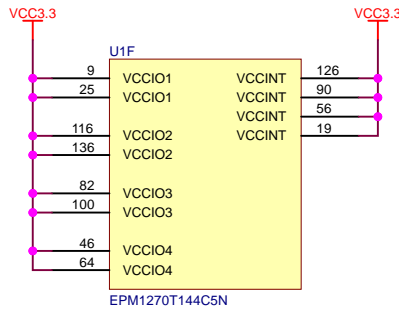
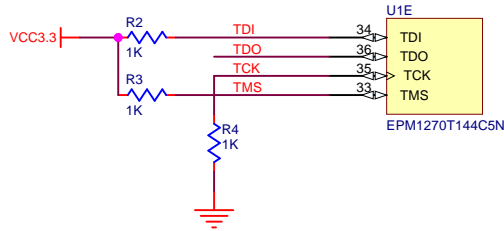
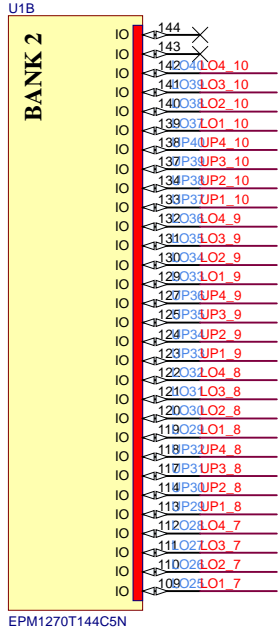
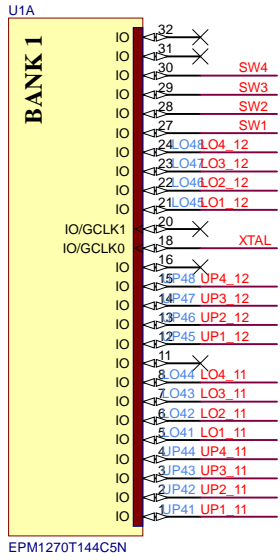
Repeat(PST_LOWER_IN1)

Repeat(PST_LOWER_IN2)

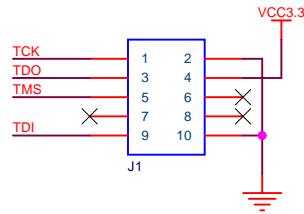
Repeat(PST_LOWER_IN3)

Repeat(PST_LOWER_IN4)

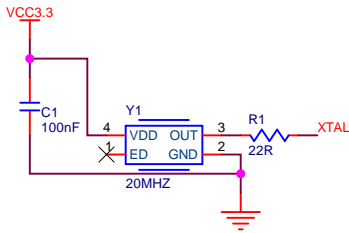
CPLD



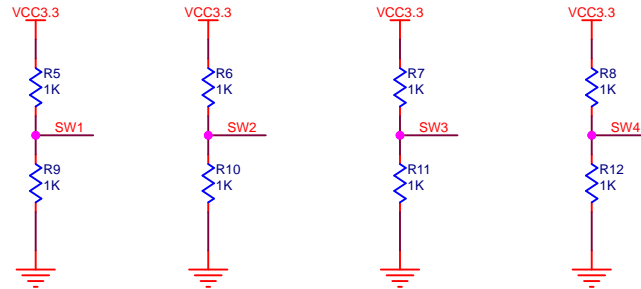
JTAG



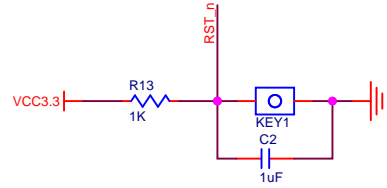
XTAL



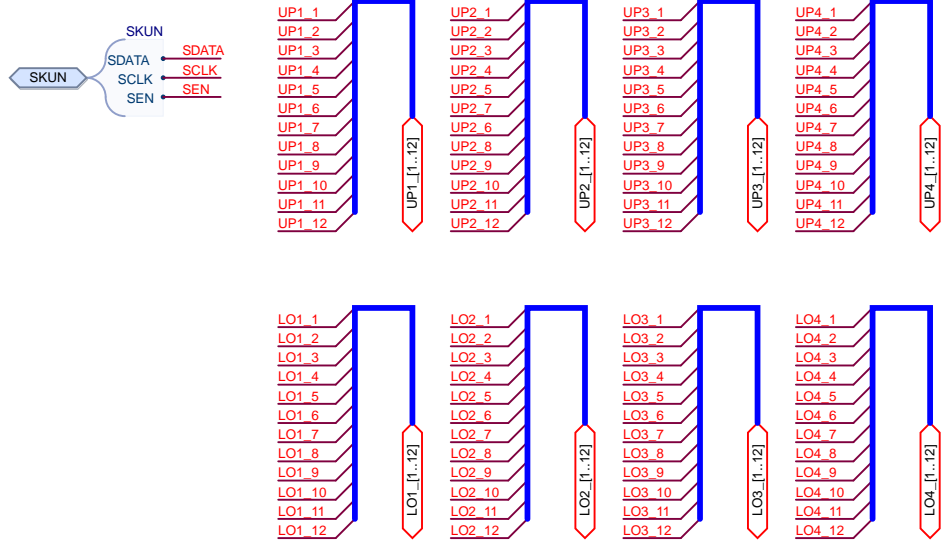
Boad Version



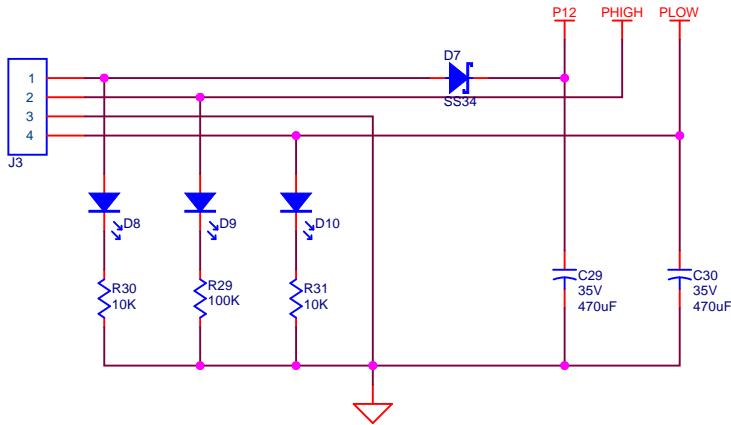
Reset



Sch Ports



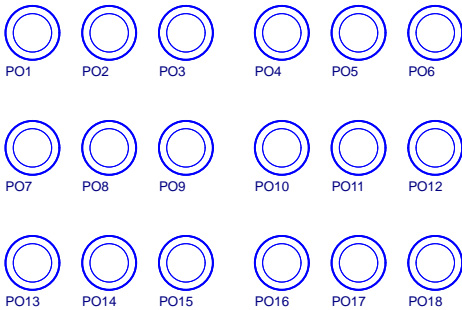
Valve Power

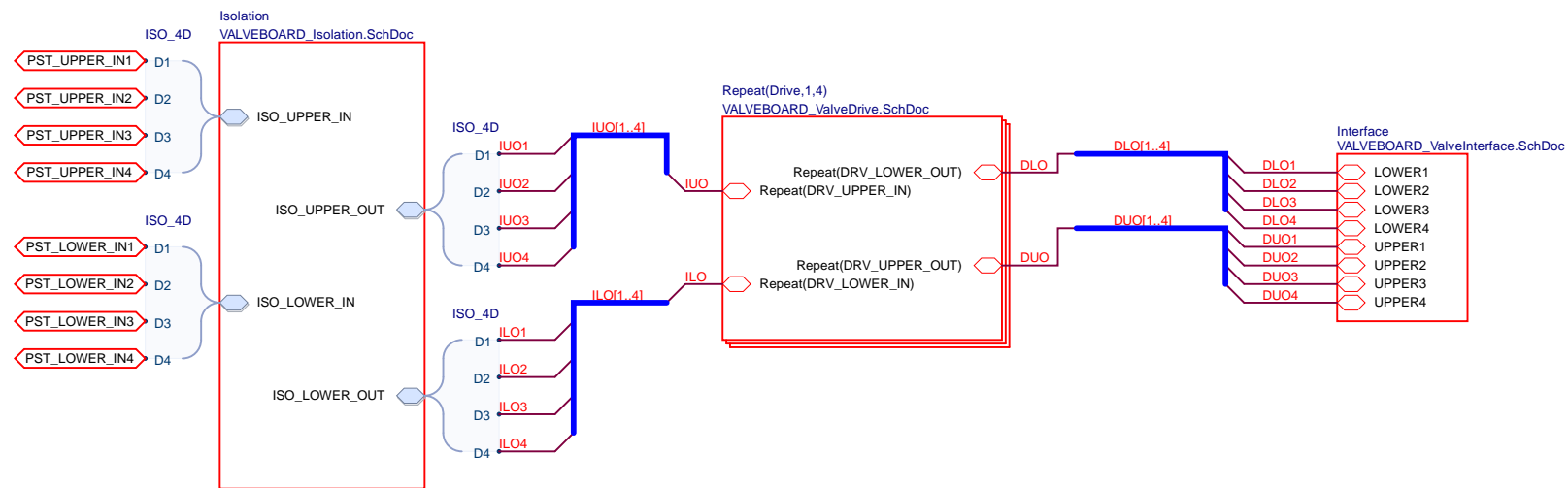


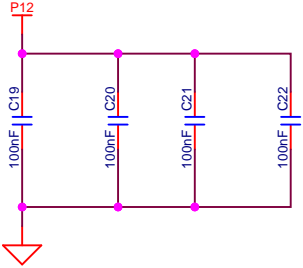
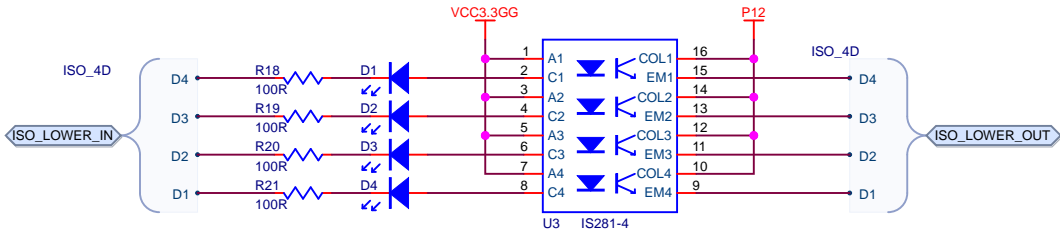
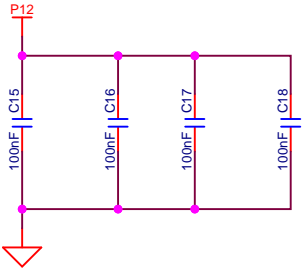
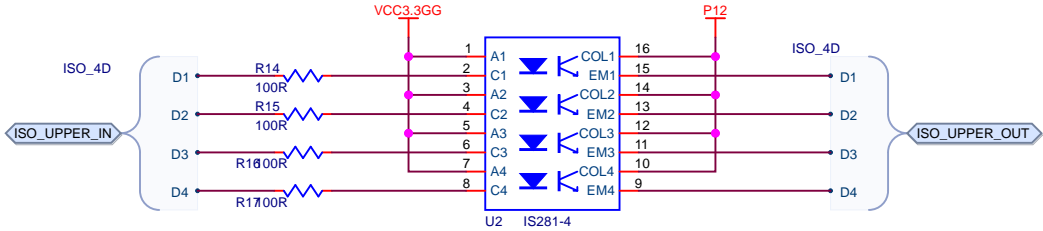
MARKPOINT

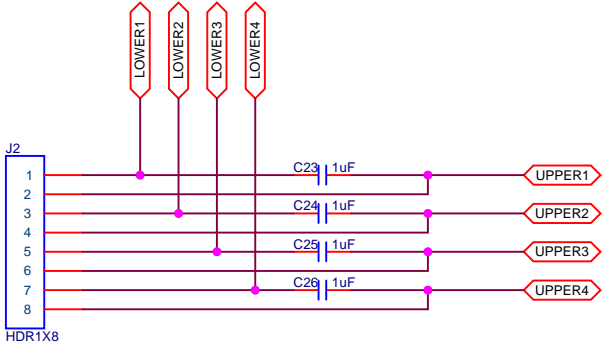
- MK1
- MK2
- MK3
- MK4

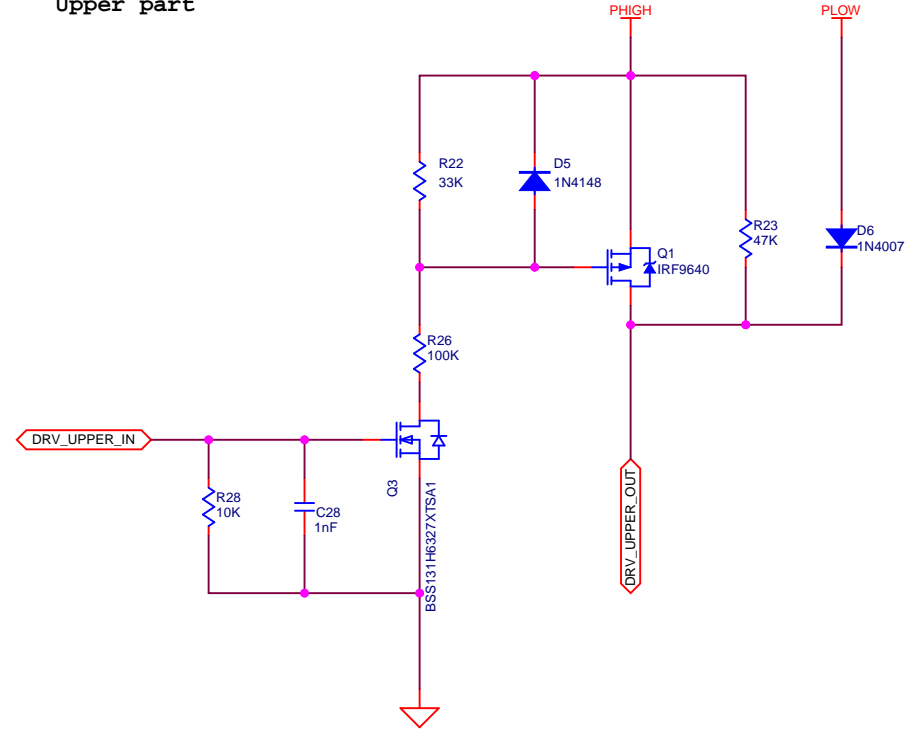
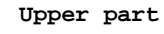
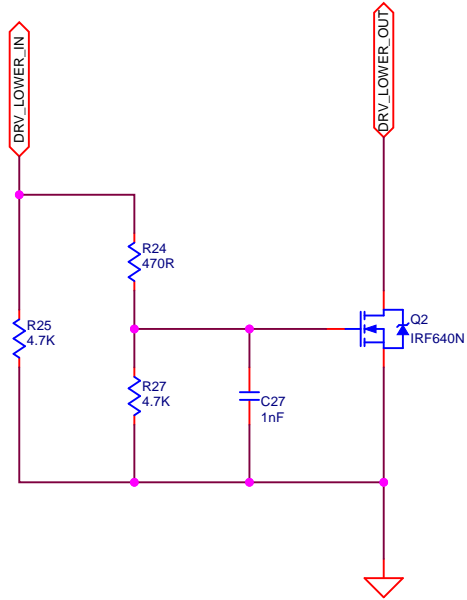
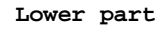
HOLE



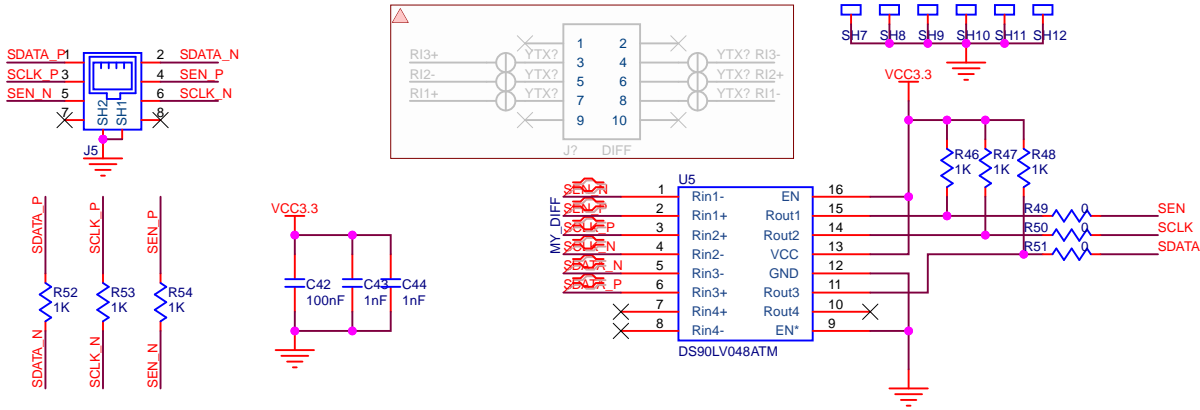




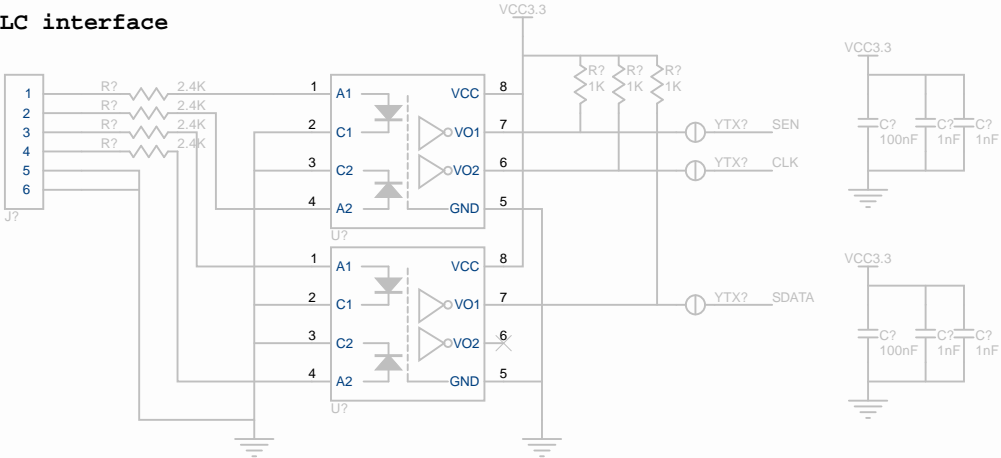




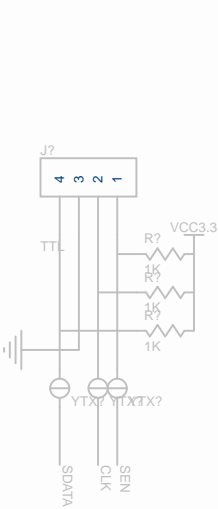
Diff interface



PLC interface



TTL interface



SKUN Bus

