# IN100 Errata

## About Documentation

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|---|---|---|
| **Document name** | IN100 Errata | |
| **Product type number** | IN100 | |
| **Control number** | IN1EDOC-ERRATA-IN100-EN-V1.0 | For external use |
| **Revision** | V1.0 | |

# Contents

# 1   Introduction

This document describes IN100 errata. The affected IN100 part numbers are listed in shown in Table 1.

Table 1: IN100 part numbers

| Part number | Package |
| --- | --- |
| IN100-D1-R-YC1I | QFN18 |
| IN100-D1-R-YC1F | QFN18 |
| IN100-Q1-R-RC1I | QFN18 |
| IN100-Q1-R-RC1F | QFN18 |
| IN100-D1-R-RC1I | DFN8 |
| IN100-D1-R-RC1F | DFN8 |
| IN100-D1-R-YC1I | DFN8 |
| IN100-D1-R-YC1F | DFN8 |

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## 2 Errata Overview

| Erratum ID | Title | Detailed description |
|---|---|---|
| E001 | Hardware race condition when simultaneous resolvable address | Section 3.1 |
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# 3 Errata Details

## 3.1 Hardware race condition when simultaneous resolvable address generation and AES-EAX encryption

**Problem description**

When both the resolvable address generation and AES-EAX encryption are enabled, there is a hardware race condition which can cause the IN100 device to stop advertising.

**Work-around**

Currently, there is no word-around for this issue. The users should avoid to enable both resolvable address generation and AES-EAX encryption in their application.

# 4 Revision History

| Version | Change | Date |
|---------|--------|------|
| V1.0 | Add Errata "Hardware race condition when simultaneous resolvable address generation and AES-EAX encryption" | 03/19/2023 |
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