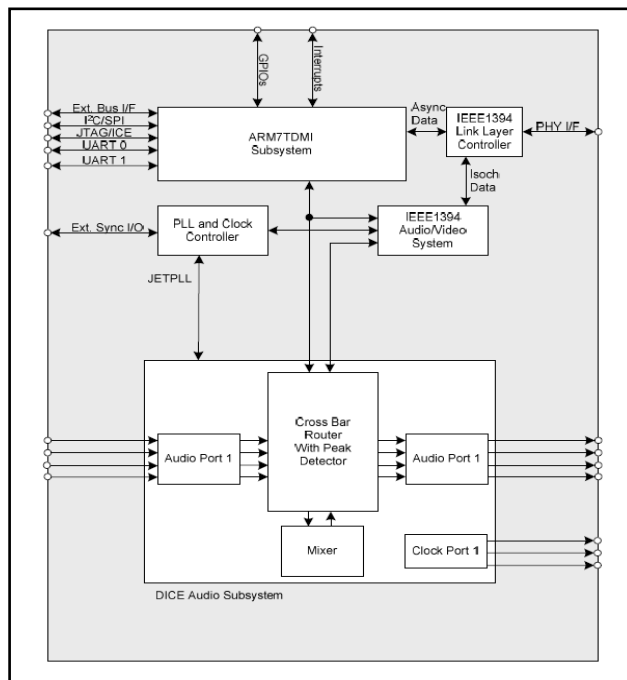
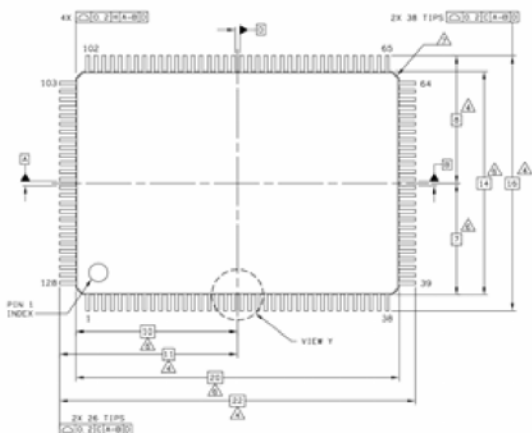


PRELIMINARY DATA Rev 0.75

- Complete **IEEE1394**, **AES3**, **ADAT®**, **I²S**, **I⁴S** and **I⁸S** transceiver
- **IEC 61883-6** Compliant Audio Streaming
- Optional **OGT** connection management support
- +1.8V core supply, +3.3V I/O
- A total of 32 IEEE 1394 FireWire® audio channels
- No-jitter solution using patented **JETTM PLL** technology
- **AES** receiver/transmitter handling 8 channels in each direction at any rate.
- Dual **ADAT** receiver/transmitter with **S-MUX** support for total 8 channels @ 96kHz
- Glueless connection of AD/DA converters (16 channels in each direction)
- 18 by 16 channel hardware mixer
- Hardware meter support for all channels
- **ARMTM** 32 bit RISC processor
- IEEE 1394 Link Layer Controller
- Available in QFP-128 package
- Patented Low jitter clock recovery



The full **SDK** supports Hardware Abstraction Layer (**HAL**) libraries and **IEEE 1394** stacks with support for **OGT**, **AVC** (Class Compliant) and **TCP/IP**.



TC Applied Technologies • 156 Duncan Mill Road, Suite 1A • Toronto, ON • M3B 3N2, Canada
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Chip Features

CPU core

- Full 32-bit ARM7TDMI RISC processor
- 32-bit internal bus
- 16-bit Thumb mode
- 32 Kb 0 wait state RAM
- 15 general-purpose 32-bit registers
- 32-bit program counter and status register
- 5 supervisor modes, 1 user mode
- External Bus Interface (EBI)
- Remap of Internal RAM during boot.

I2C Interface

- Standard and Full Speed support
- Slave mode with address match logic
- Master Mode
- 10 bit and 7 bit addressing mode
- 16 deep FIFO buffer

SPI Interface

- Master and Slave mode
- GPIO used for Slave Select
- Interrupt on Byte transfer complete

Dual Timer Unit

- 32 bit counter
- Free running and user-defined count
- Interrupt on counter wrap
- Clocked by CPU clock

Watch Dog

Dual Universal Asynchronous Receiver Transmitter (UART)

- Industry standard 16550 Compliant
- 16 deep receive and transmit FIFO's
- Supports all standard RS232 Rates
- Supports MIDI rate

General Purpose Input Output (GPIO)

- 8 individual ports
- Each port configurable as input or output

- Each port configurable for level or edge sensitive interrupts

Quad Rotary Encoder Interface (Gray Decoder)

- individual rotary encoder counters
- 8 bit signed counter per port
- Configurable interrupt on value change

IEEE 1394 Link Layer Controller (LLC)

- IEEE 1394a compliant LLC
- Compliant PHY interface
- Support for isolation barrier
- 512x32 FIFO for asynchronous communication

Digital Interface Communication Engine (DICE)

- JETTM PLL
- Cross-bar router with peak detector
- 1 Generic Audio Receive Port (16 ch. per port)
 - 4 x 2 ch. of I²S (32KHz to 192KHz)
 - 4 x 4 ch. of I⁴S (32KHz to 192KHz)
 - 2 x 8 ch. of I⁸S (32KHz to 96KHz)
 - 4 x 2 ch. of AES (32KHz to 192KHz)
 - 2 x 8 ch. of ADAT (8 ch. @96KHz, 4 ch @ 192KHz)
- 1 Generic Audio Transmit Port (16. ch per port)
 - 4 x 2 ch. of I²S (32KHz to 192KHz)
 - 4 x 4 ch. of I⁴S (32KHz to 192KHz)
 - 2 x 8 ch. of I⁸S (32KHz to 96KHz)
 - 4 x 2 ch. of AES (32KHz to 192KHz)
 - 2 x 8 ch. of ADAT (8 ch. @ 96KHz, 4 ch @ 192KHz)
- ARM Audio Receiver/Transmitter, 8 channels (32KHz to 192KHz)
- IEC 61883-6 Isoc. Receiver, 16 channels
- IEC 61883-6 Isoc. Transmitter, 16 channels
- Mixer 18x16 ch. (18x4 ch. @ 192kHz)

Power and operating voltage

- 900 mW maximum, 700 mW typical (TBD)
- 3.3 volts - I/O
- 1.8 volts – core

For further information contact:

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