```
 1  ;
 2  ;  DZ80 V3.4.1 Z80 Disassembly of build/pio_test.bin
 3  ;  2017/10/23 14:35
 4  ;
 5      org 0x0
 6  ;
 7  X0000:  jp   X0100
 8  ;
 9  X0003:  rst 0x38
10  X0004:  rst 0x38
11  ;
12      org 0x38
13  ;
14      jp X0100
15  ;
16      org 0x100
17  ;
18  X0100:  ld   sp,Xffff
19      call    X0200
20      jp X0109
21  ;
22  X0109:  halt
23  ;
24      rst 0x38
25  ;
26      org 0x200
27  ;
28  X0200:  ld   b,0x0
29      push    bc
30      xor a
31      push    af
32      inc sp
33      ld   hl,X0003
34      push    hl
35      ld   l,0x0
36      push    hl
37      call    X0255
38      pop af
39      pop af
40      inc sp
41      ld   hl,X0000
42      push    hl
43      ld   l,0x0
44      push    hl
45      call    X028e
46      pop af
47      pop af
48      pop bc
49  X0220:  push    bc
50      push    bc
51      inc sp
52      ld   hl,X0000
53      push    hl
54      call    X02e0
55      pop af
56      inc sp
```

```
57        pop  bc
58        ld   a,b
59        cpl
60        ld   b,a
61        jr   X0220
62  ;
63        ld   hl,X0002
64        add  hl,sp
65        ld   c,(hl)
66        ld   b,0x0
67        ld   hl,X4200
68        add  hl,bc
69        ld   iy,X0003
70        add  iy,sp
71        ld   a,(iy+0x0)
72        ld   (hl),a
73        ret
74  ;
75        ld   hl,X0002
76        add  hl,sp
77        ld   c,(hl)
78        ld   b,0x0
79        ld   hl,X4200
80        add  hl,bc
81        ld   l,(hl)
82        ret
83  ;
84  X0255:  push    ix
85        ld   ix,X0000
86        add  ix,sp
87        ld   a,(ix+0x6)
88        rrca
89        rrca
90        and  0xc0
91        or   0xf
92        ld   d,a
93        ld   e,(ix+0x4)
94        inc  e
95        inc  e
96        ld   l,e
97        ld   h,0x0
98        ld   bc,X4200
99        add  hl,bc
100       ld   (hl),d
101       ld   a,(ix+0x6)
102       sub  0x3
103       jr   nz,X028b
104       ld   a,(ix+0x7)
105       or   a
106       jr   nz,X028b
107       ld   c,(ix+0x8)
108       ld   d,0x0
109       ld   hl,X4200
110       add  hl,de
111       ld   (hl),c
112  X028b:  pop  ix
```

```
113        ret
114    ;
115    X028e:  ld   hl,X0004
116        add  hl,sp
117        ld   a,(hl)
118        rlca
119        rlca
120        rlca
121        rlca
122        and  0xf0
123        or   0x7
124        ld   c,a
125        ld   hl,X0002
126        add  hl,sp
127        ld   e,(hl)
128        inc  e
129        inc  e
130        ld   d,0x0
131        ld   hl,X4200
132        add  hl,de
133        ld   (hl),c
134        ret
135    ;
136        push     ix
137        ld   ix,X0000
138        add  ix,sp
139        ld   a,(ix+0x6)
140        or   0x17
141        ld   d,a
142        ld   e,(ix+0x4)
143        inc  e
144        inc  e
145        ld   l,e
146        ld   h,0x0
147        ld   bc,X4200
148        add  hl,bc
149        ld   (hl),d
150        ld   c,(ix+0x8)
151        ld   d,0x0
152        ld   hl,X4200
153        add  hl,de
154        ld   (hl),c
155        pop  ix
156        ret
157    ;
158        ld   hl,X0002
159        add  hl,sp
160        ld   c,(hl)
161        ld   b,0x0
162        ld   hl,X4200
163        add  hl,bc
164        ld   l,(hl)
165        ret
166    ;
167    X02e0:  ld   hl,X0004
168        add  hl,sp
```

```
169        ld   c,(hl)
170        ld   hl,X0002
171        add  hl,sp
172        ld   e,(hl)
173        ld   d,0x0
174        ld   hl,X4200
175        add  hl,de
176        ld   (hl),c
177        ret
178    ;
179        rst  0x38
180    ;
181    ;   Miscellaneous equates
182    ;
183    ;   These are addresses referenced in the code but
184    ;   which are in the middle of a multibyte instruction
185    ;   or are addresses outside the initialized space
186    ;
187    X0002    equ 0x2
188    X4200    equ 0x4200
189    Xffff    equ 0xffff
190    ;
191        end
192    ;
193
194
```