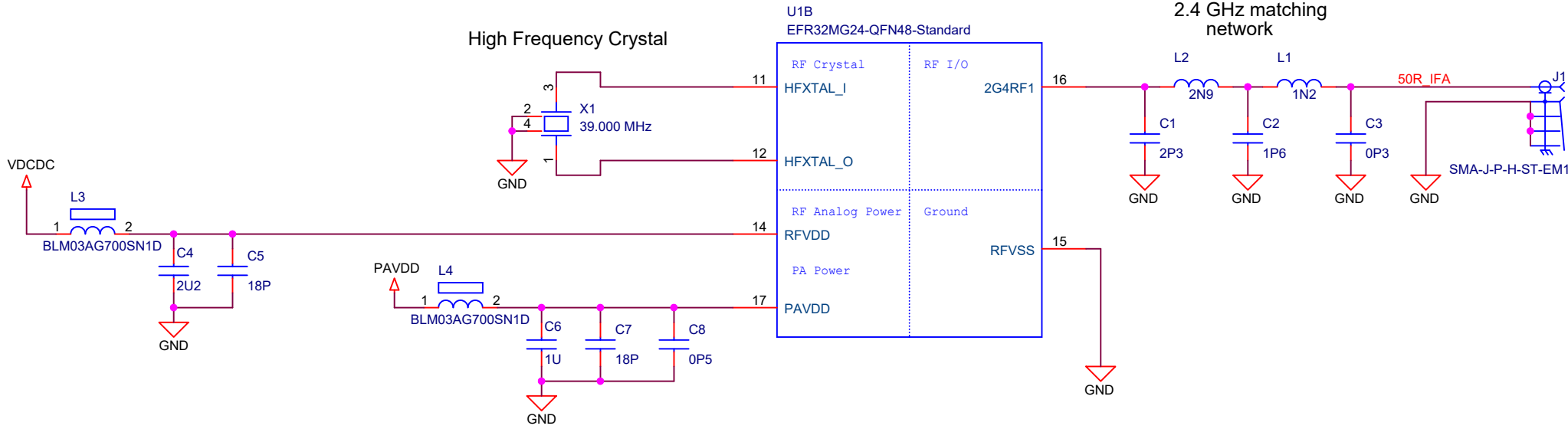
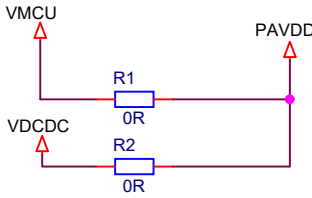


Antenna & Radio Interface

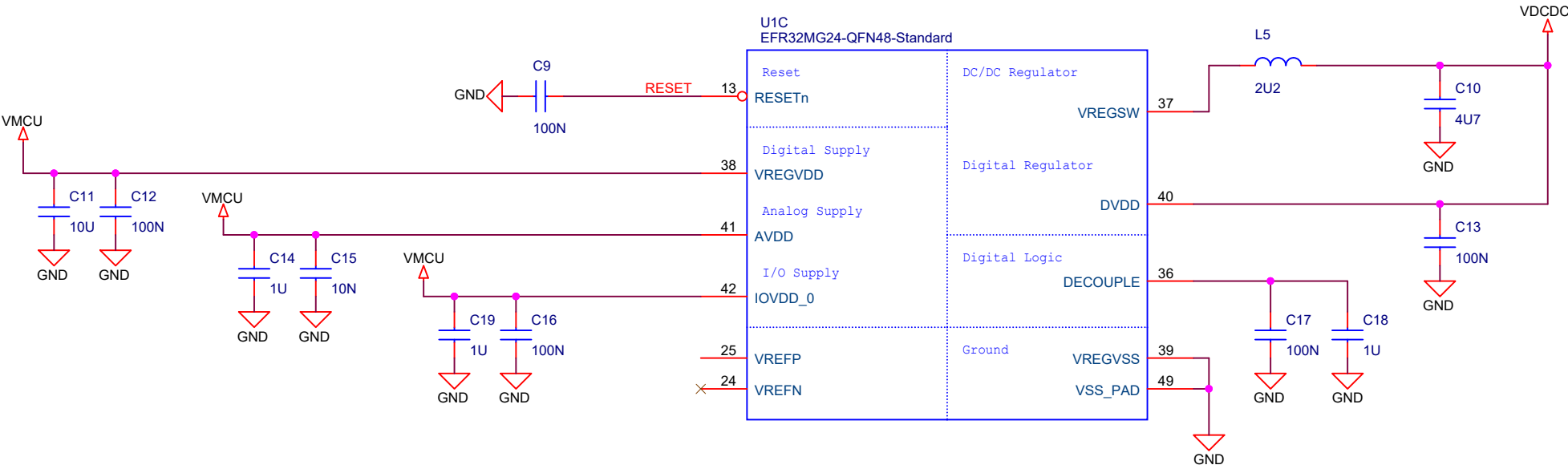


PAVDD Configuration

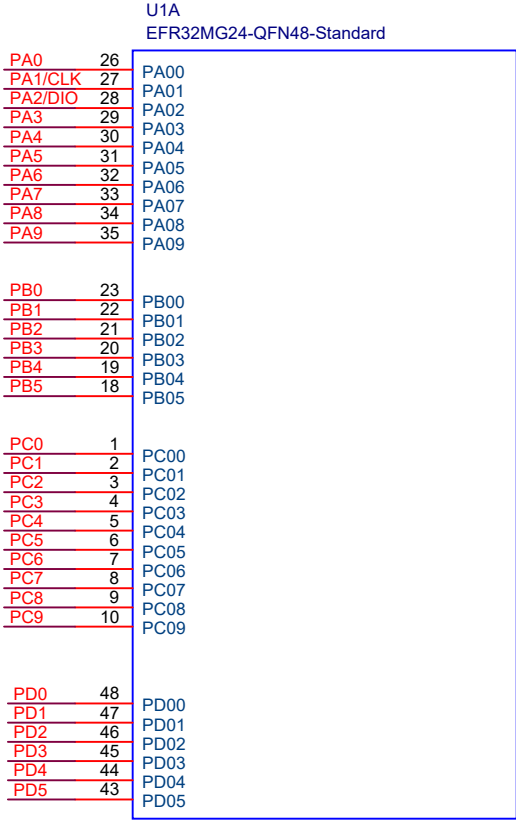
	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R1	Mount	Not mount
R2	Not mount	Mount



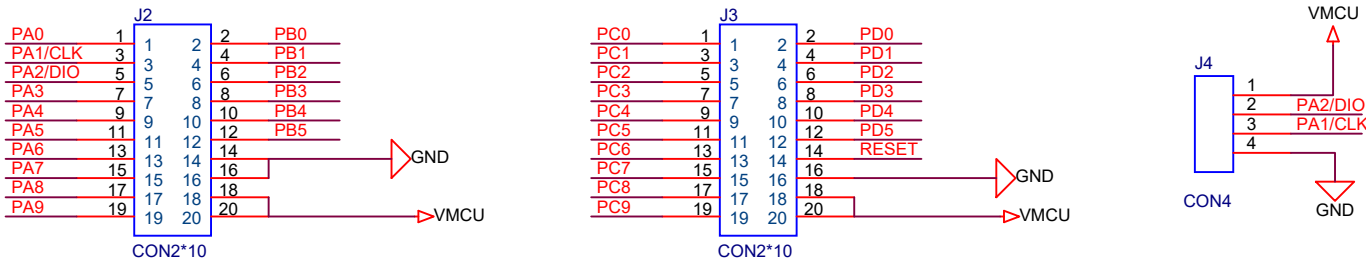
Power & Decoupling



I/O Port Pins



EXP Header



Designed		Approved		Board Name	
				EFR32MG24-QFN48-Standard	
Size	Sheet Modified Date			Revision v2.1	
A3	Tuesday, November 01, 2022				
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