R.V.COLLEGE OF ENGINEERING

Mysore Road, Bangalore 560059 (India)

(Affiliated to)

VISVESWARAIAH TECHNOLOGICAL UNIVERSITY, BELGAUM

Official Transcript/Consolidated Statement of Marks

Name of the student – **TARA NAIR** Reg No – **1RV98EC106**

Father's/Mother's Name – **R MAHAVEERA** Branch – **ELECTRONICS & COMMUNICATION**

S. No	Subjects	Max Marks	Marks Obtnd	S. No	Subjects	Max Marks	Marks Qbtnd
B.E. First Semester – March 1999				**B.E. Second Semester – July 1999**			
1	Engg Mathematics I	125	122	1	Engg Mathematics II	125	115
2	Engg Physics	125	083	2	Engineering Chemistry	125	091
3	Elements of Civil Engg	125	095	3	Strength of Materials	125	110
4	Mech Engg Sciences	125	091	4	Computer Concepts & C. Programming	125	111
5	Electrical Sciences	125	092	5	Basic Electronics	125	101
6	Workshop Practice	125	088	6	Engineering Graphics	125	088
7	Engg Physics Laboratory	125	091	7	Computer Programming Laboratory	125	082
	Grand Total	875	662	8	Engineering Chemistry Laboratory	125	121
					Grand Total	1000	819
Result: First Class with Distinction				Result: **First Class with Distinction**			
B.E. Third Semester – January 2000				**B.E. Fourth Semester – August 2000**			
1	Engg Mathematics III	125	112	1	Engg Mathematics IV	125	111
2	Network Analysis	125	105	2	Electronics Circuits II	125	087
3	Transducers & Instrumentation	125	077	3	Linear ICs & Applications	125	094
4	Electronic Circuits I	125	067	4	Digital Ics & Applications	125	092
5	Digital Electronic Fundamentals	125	092	5	Analog Communications	125	099
6	Signals & Systems	125	097	6	Fields & Waves	125	082
7	Electronics Circuits Lab I	125	109	7	Electronic Circuits Lab II	125	091
8	IC Lab I	125	116	8	IC Lab II	125	102
	Grand Total	1000	775		Grand Total	1000	758
Result: **First Class with Distinction**				Result: **First Class with Distinction**			
B.E. Fifth Semester - February 2001				**B.E. Sixth Semester – Jul 2001**			
1	Control Engineering	125	083	1	Information Theory & Coding	125	104
2	Digital Signal Processing	125	101	2	Digital Communications	125	103
3	Microprocessor	125	081	3	Adv. Microprocessor & Microcontroller	125	092
4	Microwave Circuits & Devices	125	052	4	Project Work I	125	118
5	Microprocessor & DSP Lab	125	077	5	Advanced Communication Lab	125	097
6	Communication Lab	125	109	6	Adv.Microprocessor & M:Controller Lab	125	106
7	Telecommunication & Switching	125	093	7	VHDL	125	094
8	Object Oriented Programming	125	095	8	Adaptive Signal Processing	125	114
	Grand Total	1000	691	9	System Programming	125	082
					Grand Total	1125	910
Result: **First Class**				Result: **First Class with Distinction**			
B.E. Seventh Semester – January 2002				**B.E. Eighth Semester – July 2002**			
1	VLSI Design	125	100	1	Seminar	050	048
2	Computer Communication Networks	125	106	2	Project Work	200	185
3	Antenna & Wave Propagation	125	088	3	Motorola Microcontroller	125	099
4	Power Electronics	125	081		Grand Total	375	332
5	Power Electronics & Controls Lab	125	108				
6	CCN Lab	125	109				
7	CAD tools for VLSI	125	105				
	Grand Total	875	697				
Result: **First Class with Distinction**				Result: **First Class with Distinction**			

3/8/02