

## R.V.COLLEGE OF ENGINEERING

Mysore Road, Bangalore 560059 (India)

(Affiliated to)

## VISVESWARAIAH TECHNOLOGICAL UNIVERSITY, BELGAUM

## Official Transcript/Consolidated Statement of Marks

Name of the student -

TARA NAIR Father's/Mother's Name - R MAHAVEERA Reg No - 1RV98EC106

Branch - ELECTRONICS & COMMUNICATION

S. No	Subjects	Max Marks	Marks Obtnd	S. No	Subjects	Marks Marks	Marks
						1.1110	
B.E. First Semester - March 1999			B.E. Second Semester – July 1999				
1	Engg Mathematics I	125	122	1	Engg Mathematics II	125	115
2	Engg Physics	125	083	2	Engineering Chemistry	125	091
3	Elements of Civil Engg	125	095	3	Strength of Materials	125	110
4	Mech Engg Sciences	125	091	4	Computer Concepts & C. Programming	125	111
5	Electrical Sciences	125	092	5	Basic Electronics	125	101
5	Workshop Practice	125	088	6	Engineering Graphics	125	088
7	Engg Physics Laboratory	125	091	7	Computer Programming Laboratory	125	082
	Grand Total	875	662	8	Engineering Chemistry Laboratory	125	121
-616		0,5	002	0	Grand Total	1000	819
Resi	ult: First Class with Distinction			Res	pult: First Class with Distinction	Lucy	[idi
						15	PIN I
	. Third Semester - January 2000			B.E	. Fourth Semester - August 2000	t	-
	Engg Mathematics III	125	112	1	Engg Mathematics IV	125	1111
2	Network Analysis	125	105	2	Electronics Circuits II	1125	087
3	Transducers & Instrumentation	125	077	3	Linear ICs & Applications	125	094
1	Electronic Circuits I	125	067	4	Digital Ics & Applications	125	092
5	Digital Electronic Fundamentals	125	092	5	Analog Communications	1125	099
5	Signals & Systems	125	097	6	Fields & Waves	125	082
7	Electronics Circuits Lab I	125	109	7	Electronic Circuits Lab II	125	100000000000000000000000000000000000000
100		100000000000000000000000000000000000000	75-01-02-PC	10000		123	091
3	IC Lab I	125	116	8	IC Lab II	105	100
3	Grand Total	125	116 775	8	IC Lab II Grand Total	125	102
3			775	11.72	Grand Total	125	758
} ≷est	Grand Total ult: First Class with Distinction		The state of the s	Res	Grand Total ult: First Class with Distinction	1000	100000000000000000000000000000000000000
Resu	Grand Total  ult: First Class with Distinction  Fifth Semester - February 2001	1000	The state of the s	Res	Grand Total ult: First Class with Distinction  Sixth Semester – Jul 2001	1000	100000000000000000000000000000000000000
Resu	Grand Total  ult: First Class with Distinction  Fifth Semester - February 2001  Control Engineering	1000	083	Res	Grand Total ult: First Class with Distinction  Sixth Semester – Jul 2001	1000	7.758
Resu	Grand Total  ult: First Class with Distinction  Fifth Semester - February 2001  Control Engineering  Digital Signal Processing	1000	775	Res B.E	Grand Total ult: First Class with Distinction  Sixth Semester – Jul 2001 Information Theory & Coding	1000	104
Resu	Grand Total  ult: First Class with Distinction  Fifth Semester - February 2001  Control Engineering  Digital Signal Processing  Microprocessor	1000	083	B.E 1 2	Grand Total ult: First Class with Distinction  Sixth Semester – Jul 2001 Information Theory & Coding Digital Communications	125	104 103
Resu	Grand Total  alt: First Class with Distinction  Fifth Semester - February 2001  Control Engineering  Digital Signal Processing  Microprocessor  Microwave Circuits & Devices	1000 125 125	775 083 101	Res B.E	Grand Total  ult: First Class with Distinction  Sixth Semester – Jul 2001  Information Theory & Coding  Digital Communications  Adv. Microprocessor & Microcontroller	125 125 125 125	104 103 092
Resu	Grand Total  alt: First Class with Distinction  Fifth Semester - February 2001  Control Engineering  Digital Signal Processing  Microprocessor  Microwave Circuits & Devices  Microprocessor & DSP Lab	125 125 125 125	083 101 081 052	Res 1 2 3 4	Grand Total  ult: First Class with Distinction  Sixth Semester – Jul 2001  Information Theory & Coding  Digital Communications  Adv. Microprocessor & Microcontroller  Project Work I	125 125 125 125	104 103 092 118
Resu	Grand Total  alt: First Class with Distinction  Fifth Semester - February 2001  Control Engineering  Digital Signal Processing  Microprocessor  Microwave Circuits & Devices	125 125 125 125 125 125 125	083 101 081 052 077	Res B.E 1 2 3 4 5	Grand Total ult: First Class with Distinction  Sixth Semester – Jul 2001 Information Theory & Coding Digital Communications Adv. Microprocessor & Microcontroller Project Work I Advanced Communication Lab	125 125 125 125 125 125	104 103 092 118 097
Resu	Grand Total  alt: First Class with Distinction  Fifth Semester - February 2001  Control Engineering Digital Signal Processing Microprocessor Microwave Circuits & Devices Microprocessor & DSP Lab Communication Lab	1000 125 125 125 125 125 125 125 125	083 101 081 052 077 109	B.E 1 2 3 4 5 6	Grand Total  ult: First Class with Distinction  Sixth Semester – Jul 2001  Information Theory & Coding Digital Communications Adv. Microprocessor & Microcontroller Project Work I Advanced Communication Lab Adv.Microprocessor & M:Controller:Lab	125 125 125 125 125 125 125	104 103 092 118
Resu	Grand Total  ult: First Class with Distinction  Fifth Semester - February 2001  Control Engineering Digital Signal Processing Microprocessor Microwave Circuits & Devices Microprocessor & DSP Lab Communication Lab Telecommunication & Switching	125 125 125 125 125 125 125 125 125 125	083 101 081 052 077 109 093	B.E 1 2 3 4 5 6 7	Grand Total  ult: First Class with Distinction  Sixth Semester – Jul 2001  Information Theory & Coding Digital Communications Adv. Microprocessor & Microcontroller Project Work I Advanced Communication Lab Adv.Microprocessor & M:Controller:Lab VHDL	125 125 125 125 125 125 125 125 125 125	104 103 092 118 097
3 Resu	Grand Total  alt: First Class with Distinction  Fifth Semester - February 2001  Control Engineering Digital Signal Processing Microprocessor Microwave Circuits & Devices Microprocessor & DSP Lab Communication Lab	125 125 125 125 125 125 125 125 125 125	083 101 081 052 077 109 093 095	Res B.E 1 2 3 4 5 6 7 8	Grand Total  ult: First Class with Distinction  Sixth Semester – Jul 2001  Information Theory & Coding Digital Communications Adv. Microprocessor & Microcontroller Project Work I  Advanced Communication Lab Adv.Microprocessor & M:Controller:Lab VHDL Adaptive Signal Processing	125 125 125 125 125 125 125	104 103 092 118 097 106
Resu	Grand Total  alt: First Class with Distinction  Fifth Semester - February 2001  Control Engineering Digital Signal Processing Microprocessor Microwave Circuits & Devices Microprocessor & DSP Lab Communication Lab Telecommunication & Switching Object Oriented Programming Grand Total	125 125 125 125 125 125 125 125 125 125	083 101 081 052 077 109 093	B.E 1 2 3 4 5 6 7	Grand Total  ult: First Class with Distinction  Sixth Semester – Jul 2001  Information Theory & Coding Digital Communications Adv. Microprocessor & Microcontroller Project Work I  Advanced Communication Lab Adv.Microprocessor & MiController Lab VHDL Adaptive Signal Processing System Programming	125 125 125 125 125 125 125 125 125 125	104 103 092 118 097 106 094
Resu	Grand Total  ult: First Class with Distinction  Fifth Semester - February 2001  Control Engineering Digital Signal Processing Microprocessor Microwave Circuits & Devices Microprocessor & DSP Lab Communication Lab Telecommunication & Switching Object Oriented Programming	125 125 125 125 125 125 125 125 125 125	083 101 081 052 077 109 093 095	Res B.E 1 2 3 4 5 6 7 8 9	Grand Total ult: First Class with Distinction  Sixth Semester – Jul 2001 Information Theory & Coding Digital Communications Adv. Microprocessor & Microcontroller Project Work I Advanced Communication Lab Adv.Microprocessor & M:Controller:Lab VHDL Adaptive Signal Processing System Programming Grand Total	125 125 125 125 125 125 125 125 125 125	104 103 092 118 097 196 094 114
Resu	Grand Total  alt: First Class with Distinction  Fifth Semester - February 2001  Control Engineering Digital Signal Processing Microprocessor Microwave Circuits & Devices Microprocessor & DSP Lab Communication Lab Telecommunication & Switching Object Oriented Programming Grand Total  alt: First Class	125 125 125 125 125 125 125 125 125 125	083 101 081 052 077 109 093 095	Res B.E 1 2 3 4 5 6 7 8 9	Grand Total  ult: First Class with Distinction  Sixth Semester – Jul 2001  Information Theory & Coding Digital Communications Adv. Microprocessor & Microcontroller Project Work I  Advanced Communication Lab Adv.Microprocessor & MiController Lab VHDL Adaptive Signal Processing System Programming	125 125 125 125 125 125 125 125 125 125	104 103 092 118 097 106 094 114 082
Resu	Grand Total  alt: First Class with Distinction  Fifth Semester - February 2001 Control Engineering Digital Signal Processing Microprocessor Microwave Circuits & Devices Microprocessor & DSP Lab Communication Lab Telecommunication & Switching Object Oriented Programming Grand Total  alt: First Class  Seventh Semester - January 2002	125 125 125 125 125 125 125 125 125 125	083 101 081 052 077 109 093 095	Res B.F 1 2 3 4 5 6 7 8 9	Grand Total ult: First Class with Distinction  Sixth Semester – Jul 2001 Information Theory & Coding Digital Communications Adv. Microprocessor & Microcontroller Project Work I Advanced Communication Lab Adv.Microprocessor & M:Controller:Lab VHDL Adaptive Signal Processing System Programming Grand Total sult: First Class with Distinction	125 125 125 125 125 125 125 125 125 125	104 103 092 118 097 106 094 114 082
Resu	Grand Total  alt: First Class with Distinction  Fifth Semester - February 2001  Control Engineering Digital Signal Processing Microprocessor Microwave Circuits & Devices Microprocessor & DSP Lab  Communication Lab Telecommunication & Switching Object Oriented Programming Grand Total  alt: First Class  Seventh Semester - January 2002  VLSI Design	125 125 125 125 125 125 125 125 125 125	083 101 081 052 077 109 093 095 691	Res B.E 1 2 3 4 5 6 7 8 9	Grand Total ult: First Class with Distinction  Sixth Semester – Jul 2001 Information Theory & Coding Digital Communications Adv. Microprocessor & Microcontroller Project Work I Advanced Communication Lab Adv. Microprocessor & M. Controller Lab VHDL Adaptive Signal Processing System Programming Grand Total sult: First Class with Distinction  E. Eighth Semester – July 2002	125 125 125 125 125 125 125 125 125 125	104 103 092 118 097 106 094 114 082 910
Rest	Grand Total  alt: First Class with Distinction  Fifth Semester - February 2001 Control Engineering Digital Signal Processing Microprocessor Microwave Circuits & Devices Microprocessor & DSP Lab Communication Lab Telecommunication & Switching Object Oriented Programming Grand Total  alt: First Class  Seventh Semester - January 2002 VLSI Design Computer Communication Networks	125 125 125 125 125 125 125 125 125 125	083 101 081 052 077 109 093 095 691	Res B.E 1 2 3 4 5 6 7 8 9 Res	Grand Total ult: First Class with Distinction  Sixth Semester – Jul 2001 Information Theory & Coding Digital Communications Adv. Microprocessor & Microcontroller Project Work I Advanced Communication Lab Adv.Microprocessor & M:Controller:Lab VHDL Adaptive Signal Processing System Programming Grand Total sult: First Class with Distinction  Eighth Semester – July 2002 Seminar	125 125 125 125 125 125 125 125 125 125	104 103 092 118 097 106 094 114 082 910
Rest	Grand Total  alt: First Class with Distinction  Fifth Semester - February 2001 Control Engineering Digital Signal Processing Microprocessor Microwave Circuits & Devices Microprocessor & DSP Lab Communication Lab Telecommunication & Switching Object Oriented Programming Grand Total  alt: First Class  Seventh Semester - January 2002 VLSI Design Computer Communication Networks Antenna & Wave Propagation	125 125 125 125 125 125 125 125 125 125	083 101 081 052 077 109 093 095 691	Res B.E 1 2 3 4 5 6 7 8 9 Res B.E 1 2	Grand Total ult: First Class with Distinction  Sixth Semester – Jul 2001 Information Theory & Coding Digital Communications Adv. Microprocessor & Microcontroller Project Work I Advanced Communication Lab Adv.Microprocessor & M:Controller:Lab VHDL Adaptive Signal Processing System Programming Grand Total sult: First Class with Distinction  Eighth Semester – July 2002 Seminar Project Work	125 125 125 125 125 125 125 125 125 125	104 103 092 118 097 106 094 114 082 910
Results Result	Grand Total  alt: First Class with Distinction  Fifth Semester - February 2001 Control Engineering Digital Signal Processing Microprocessor Microwave Circuits & Devices Microprocessor & DSP Lab Communication Lab Telecommunication & Switching Object Oriented Programming Grand Total  alt: First Class  Seventh Semester - January 2002 VLSI Design Computer Communication Networks Antenna & Wave Propagation Power Electronics	125 125 125 125 125 125 125 125 125 125	083 101 081 052 077 109 093 095 691	Res B.E 1 2 3 4 5 6 7 8 9 Res	Grand Total  ult: First Class with Distinction  Sixth Semester – Jul 2001  Information Theory & Coding Digital Communications Adv. Microprocessor & Microcontroller Project Work I  Advanced Communication Lab Adv.Microprocessor & M:Controller:Lab VHDL  Adaptive Signal Processing System Programming Grand Total  sult: First Class with Distinction  Eighth Semester – July 2002  Seminar Project Work Motorola Microcontroller	125 125 125 125 125 125 125 125 125 125	104 103 092 118 097 106 094 114 082 910
B.E.	Grand Total  alt: First Class with Distinction  Fifth Semester - February 2001  Control Engineering Digital Signal Processing Microprocessor Microwave Circuits & Devices Microprocessor & DSP Lab  Communication Lab Telecommunication & Switching Object Oriented Programming Grand Total  alt: First Class  Seventh Semester - January 2002  VLSI Design Computer Communication Networks Antenna & Wave Propagation Power Electronics Power Electronics & Controls Lab	125 125 125 125 125 125 125 125 125 125	083 101 081 052 077 109 093 095 691	Res B.E 1 2 3 4 5 6 7 8 9 Res B.E 1 2	Grand Total ult: First Class with Distinction  Sixth Semester – Jul 2001 Information Theory & Coding Digital Communications Adv. Microprocessor & Microcontroller Project Work I Advanced Communication Lab Adv.Microprocessor & M:Controller:Lab VHDL Adaptive Signal Processing System Programming Grand Total sult: First Class with Distinction  Eighth Semester – July 2002 Seminar Project Work	125 125 125 125 125 125 125 125 125 125	104 103 092 118 097 106 094 114 082 910
Result Re	Grand Total  alt: First Class with Distinction  Fifth Semester - February 2001  Control Engineering Digital Signal Processing Microprocessor Microwave Circuits & Devices Microprocessor & DSP Lab  Communication Lab Telecommunication & Switching Object Oriented Programming Grand Total  alt: First Class  Seventh Semester - January 2002  VLSI Design Computer Communication Networks Antenna & Wave Propagation Power Electronics Power Electronics & Controls Lab CCN Lab	125 125 125 125 125 125 125 125 125 125	083 101 081 052 077 109 093 095 691 100 106 088 081 108	Res B.E 1 2 3 4 5 6 7 8 9 Res B.E 1 2	Grand Total  ult: First Class with Distinction  Sixth Semester – Jul 2001  Information Theory & Coding Digital Communications Adv. Microprocessor & Microcontroller Project Work I  Advanced Communication Lab Adv.Microprocessor & M:Controller:Lab VHDL  Adaptive Signal Processing System Programming Grand Total  sult: First Class with Distinction  Eighth Semester – July 2002  Seminar Project Work Motorola Microcontroller	125 125 125 125 125 125 125 125 125 125	104 103 092 118 097 106 094 114 082 910
B.E.	Grand Total  alt: First Class with Distinction  Fifth Semester - February 2001  Control Engineering Digital Signal Processing Microprocessor Microwave Circuits & Devices Microprocessor & DSP Lab  Communication Lab Telecommunication & Switching Object Oriented Programming Grand Total  alt: First Class  Seventh Semester - January 2002  VLSI Design Computer Communication Networks Antenna & Wave Propagation Power Electronics Power Electronics & Controls Lab CCN Lab	125 125 125 125 125 125 125 125 125 125	083 101 081 052 077 109 093 095 691 100 106 088 081 108	Res B.E 1 2 3 4 5 6 7 8 9 Res B.E 1 2	Grand Total  ult: First Class with Distinction  Sixth Semester – Jul 2001  Information Theory & Coding Digital Communications Adv. Microprocessor & Microcontroller Project Work I  Advanced Communication Lab Adv.Microprocessor & M:Controller:Lab VHDL  Adaptive Signal Processing System Programming Grand Total  sult: First Class with Distinction  Eighth Semester – July 2002  Seminar Project Work Motorola Microcontroller	125 125 125 125 125 125 125 125 125 125	104 103 092 118 097 106 094 114 082 910
Rest Rest	Grand Total  alt: First Class with Distinction  Fifth Semester - February 2001  Control Engineering Digital Signal Processing Microprocessor Microwave Circuits & Devices Microprocessor & DSP Lab  Communication Lab Telecommunication & Switching Object Oriented Programming Grand Total  alt: First Class  Seventh Semester - January 2002  VLSI Design Computer Communication Networks Antenna & Wave Propagation Power Electronics Power Electronics & Controls Lab	125 125 125 125 125 125 125 125 125 125	083 101 081 052 077 109 093 095 691 100 106 088 081 108	Res B.E 1 2 3 4 5 6 7 8 9 Res B.E 1 2	Grand Total  ult: First Class with Distinction  Sixth Semester – Jul 2001  Information Theory & Coding Digital Communications Adv. Microprocessor & Microcontroller Project Work I  Advanced Communication Lab Adv.Microprocessor & M:Controller:Lab VHDL  Adaptive Signal Processing System Programming Grand Total  sult: First Class with Distinction  Eighth Semester – July 2002  Seminar Project Work Motorola Microcontroller	125 125 125 125 125 125 125 125 125 125	104 103 092 118 097 106 094 114 082 910

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