



Visvesvaraya Technological University, Belagavi Karnataka State, INDIA

220201200259

TRANSCRIPT AS PER RECORDS

Name: **NALLAGATLA VENKATA NARASIMHA AKHIL**

University Seat No: **1BY16EC053**

Year of admission: **2016**

Mon & Yr. of completion: **082020**

Academic Program: **Bachelor of Engineering**

(Electronics & Communication Engineering)

1. Duration of the Course: 4 Years

2. Medium of Instructions: English

3. Percentage = (CGPA-0.75) * 10

4. First class with Distinction(FCD): Greater than or equal to 70%

First Class (FC): Greater than or equal to 60 % but less than 70%

SecondClass (SC): Less than 60%

Course Code	Course Name	Credits	Grade	Mon&Yr	Attempts
I Semester					
15MAT11	Engineering Maths-I	4	A	042017	1
15PHY12	Engineering Physics	4	B	042017	1
15CIV13	Elements of Civil Engg. & Mechanics	4	E	072017	2
15EME14	Elements of Mechanical Engineering	4	E	042017	1
15ELE15	Basic Electrical Engineering	4	C	042017	1
15WSL16	Workshop Practice	2	S	042017	1
15PHYL17	Engg. Physics Lab.	2	B	042017	1
15CPH18	Const. of India, Prof. Ethics & Human Rights	0	PP	042017	1
II Semester					
15MAT21	Engineering Maths-II	4	B	072017	1
15CHE22	Engineering Chemistry	4	C	072017	1
15PCD23	Programming in C & Data Structures	4	C	072017	1
15CED24	Computer Aided Engineering Drawing	4	C	072017	1
15ELN25	Basic Electronics	4	B	072017	1
15CPL26	Computer Programming Lab.	2	C	072017	1
15CHEL27	Engineering Chemistry Lab.	2	A	072017	1
15CIV28	Environmental Studies	0	PP	072017	1
III Semester					
15MAT31	Engineering Mathematics - III	4	B	012018	1
15EC32	Analog Electronics	4	A	012018	1
15EC33	Digital Electronics	4	A	012018	1
15EC34	Network Analysis	4	D	012018	1
15EC35	Electronic Instrumentation	4	B	012018	1
15EC36	Engineering Electromagnetics	4	B	012018	1
15ECL37	Analog Electronics Lab	2	B	012018	1
15ECL38	Digital Electronics Lab	2	S	012018	1
IV Semester					
15MAT41	Engineering Mathematics-IV	4	B	072018	1
15EC42	Microprocessor	4	A	072018	1
15EC43	Control Systems	4	D	072018	1
15EC44	Signals and Systems	4	C	072018	1
15EC45	Principles of Communication Systems	4	B	072018	1
15EC46	Linear Integrated Circuits	4	B	072018	1
15ECL47	Microprocessor Lab.	2	S	072018	1
15ECL48	Linear ICs and Communication Lab.	2	D	072018	1

Course Code	Course Name	Credits	Grade	Mon&Yr	Attempts
V Semester					
15ES51	Management & Entrepreneurship Development	4	A	012019	1
15EC52	Digital Signal Processing	4	B	012019	1
15EC53	Verilog HDL	4	S	012019	1
15EC54	Information Theory and Coding	4	B	012019	1
15EC55	MSP430 Microcontroller	3	A	012019	1
15EC56	Object Oriented Programming Using C++	3	B	012019	1
15ECL57	DSP Lab.	2	S+	012019	1
15ECL58	HDL Lab.	2	S	012019	1
VI Semester					
15EC61	Digital Communication	4	B	072019	1
15EC62	ARM Microcontroller & Embedded Systems	4	S	072019	1
15EC63	VLSI Design	4	A	072019	1
15EC64	Computer Communication Networks	4	C	072019	1
15EC65	Digital Switching Systems	3	A	072019	1
15EC66	Digital System Design using Verilog	3	A	072019	1
15ECL67	Embedded Controller Lab.	2	S	072019	1
15ECL68	Computer Networks Lab.	2	S	072019	1
VII Semester					
15EC71	Microwave and Antennas	4	B	012020	1
15EC72	Digital Image Processing	4	A	012020	1
15EC73	Power Electronics	4	B	012020	1
15EC74	Cryptography	3	C	012020	1
15EC75	Satellite Communication	3	S	012020	1
15ECL76	Advanced Communication Lab.	2	S	012020	1
15ECL77	VLSI Lab.	2	S	012020	1
15ECP78	Project Phase - I + Seminar	2	S+	012020	1
VIII Semester					
15EC81	Wireless Cellular and LTE 4G Broadband	4	B	082020	1
15EC82	Fiber Optics & Networks	4	B	082020	1
15EC83	Network and Cyber Security	3	B	082020	1
15EC84	Internship/Professional Practice	2	S+	082020	1
15ECP85	Project Work	6	S+	082020	1
15ECS86	Seminar	1	S+	082020	1

CGPA: 7.30

Percentage: 65.50

Class of the Degree: First Class

Grade	S+	S	A	B	C	D	E	F
Grade Point	10	9	8	7	6	5	4	0
Marks Range	>=90	<90 >=80	<80 >=70	<70 >=60	<60 >=50	<50 >=45	<45 >=40	<40

PP- Mandatory non credit course



AUTHENTIC



*Based on First Attempt Marks of V to VIII Semesters # Number of Attempts taken to clear the semester

(* and # not applicable to Choice Based Credit System)

Registrar (Evaluation)