Question 1.

As represented on the diagrams and truth table below, when we have an OR operation between two inputs, the output will 1 if either input is 1, including both inputs being 1. While when we have a XOR operation between two inputs, the output will be 1 when either input is 1, excluding when both inputs are 1.

Question 2.

Question 3.

As represented on the diagrams below, when using a AND operation between two inputs, the output will be 1 when both inputs are 1. Applying the same logic with transistors AND gate, if we consider the inputs (A, B) as A = 0V and B = 5V, when both inputs are 0V or only one of them is 5V, both transistors (Q1 and Q2) are at zero state (which means that they are OFF).