

8c). Data = Roll Number = 419 in binary.

419's binary equivalent is  $110100011$   
 $\begin{matrix} & 8 & 7 & 6 & 5 & 4 & 3 & 2 & 1 & 0 \end{matrix}$

Divisor = 1011

I) Data in polynomial.

Dataword should be right shifted by 3 times because the divisor is of 4 bits or 3+1 bits.

Therefore final dataword,

$110100011000$   
 $\begin{matrix} 11 & 10 & 9 & 8 & 7 & 6 & 5 & 4 & 3 & 2 & 1 & 0 \end{matrix}$   
 $\cdot x^3 \cdot x^2 \cdot x^1$

$x^{11} + x^{10} + x^8 + x^4 + x^3$  in polynomial.

Codeword is  $1011$   
 $\begin{matrix} 3 & 2 & 1 & 0 \end{matrix}$

$x^3 + x + 1$

codeword =  $\underbrace{D \cdot 2^r}_{\downarrow}$  XOR  $\underbrace{C}_{\downarrow}$

right shifts  
by  $r$   
bits

appends  $r$  bits

II) Source side

$$\begin{array}{r}
 x^3 + x + 1 \mid x^{11} + x^{10} + x^8 + x^4 + x^3 (x^8 + x^7 + x^6 + x^5 + x + x^2) \\
 \underline{x^{11} + x^9 + x^8} \\
 x^{10} + x^9 + x^4 + x^3 \\
 \underline{x^{10} + x^8 + x^7} \\
 x^9 + x^8 + x^4 + x^3 + x^7 \\
 \underline{x^9 + x^7 + x^6} \\
 x^8 + x^4 + x^3 + x^6 \\
 \underline{x^8 + x^6 + x^5} \\
 x^4 + x^3 + x^5 \\
 \underline{x^4 + x^2 + x} \\
 x^5 + x^3 + x^2 + x \\
 \underline{x^5 + x^3 + x^2} \\
 x
 \end{array}$$

Redundant bits = is made of 3 bits as Generator is of 4 bits. So redundant bits is 010 or  $x$ .

$$\text{Codeword} = x^{11} + x^{10} + x^8 + x^4 + x^3 + x.$$

3) If error occurs in  $x^2$  i.e. is my case 0 should be flipped to 1. as 010 was redundant bits  $x^2$  is 0 turning it to 1 will change the codeword as

$$\text{Codeword} = x^{11} + x^{10} + x^8 + x^4 + x^3 + x^2 + x$$

made 1



$$\begin{array}{r}
 x^8 + x^7 + x^6 + x^5 + x^2 + x \\
 x^3 + x + 1 \overline{) x^{11} + x^{10} + x^8 + x^4 + x^3 + x^2 + x} \\
 \underline{x^{11} + x^9 + x^8} \phantom{+ x^4 + x^3 + x^2 + x} \\
 x^{10} + x^9 + x^4 + x^3 + x^2 + x \\
 \underline{x^{10} + x^8 + x^7} \phantom{+ x^4 + x^3 + x^2 + x} \\
 x^9 + x^8 + x^7 + x^4 + x^3 + x^2 + x \\
 \underline{x^9 + x^7 + x^6} \phantom{+ x^4 + x^3 + x^2 + x} \\
 x^8 + x^6 + x^4 + x^3 + x^2 + x \\
 \underline{x^8 + x^6 + x^5} \phantom{+ x^4 + x^3 + x^2 + x} \\
 x^5 + x^4 + x^3 + x^2 + x \\
 \underline{x^5 + x^3 + x^2} \phantom{+ x} \\
 x^4 + x \\
 \underline{x^4 + x^2 + x} \\
 x^2
 \end{array}$$

Source adds remainder of codeword by Generator to codeword if no error occurred then codeword should be completely divisible at ~~sender~~ receiver side. But it is not the case as  $x^2$  was flipped. Hence remainder is non zero and destination identifies the error has occurred.

1a)  $N=3$  nodes are active.

$$p = 0.43 \quad N_p = 3$$

(i) Slotted Aloha (only one station per slot)

$$\begin{aligned} \text{Efficiency} &= p(1-p)^{N-1} * N_p \\ &= 0.43(1-0.43)^{(3-1)} * 3 \\ &= 0.43(1-0.43)^2 * 3 \\ &= 0.43(0.57)^2 * 3 \\ &= 0.43 * 0.3249 * 3 \\ &= 0.1397 \text{ or } 13.97\% * 3 = 41.97\% \end{aligned}$$

(ii) Aloha for data transmission.

$$\begin{aligned} \text{efficiency} &= p(1-p)^{2(N-1)} * N_p = 0.43(1-0.43)^{2(3-1)} * 3 \\ &= 0.43(0.57)^{2*2} * 3 = 0.43(0.57)^4 * 3 \\ &= 0.04539 * 3 \\ &\text{or } 4.53\% * 3 \text{ efficient} \\ &= 13.59 \text{ or } \approx 13.6\% \end{aligned}$$

~~if only 1 station per slot then efficiency will be 13.6%~~



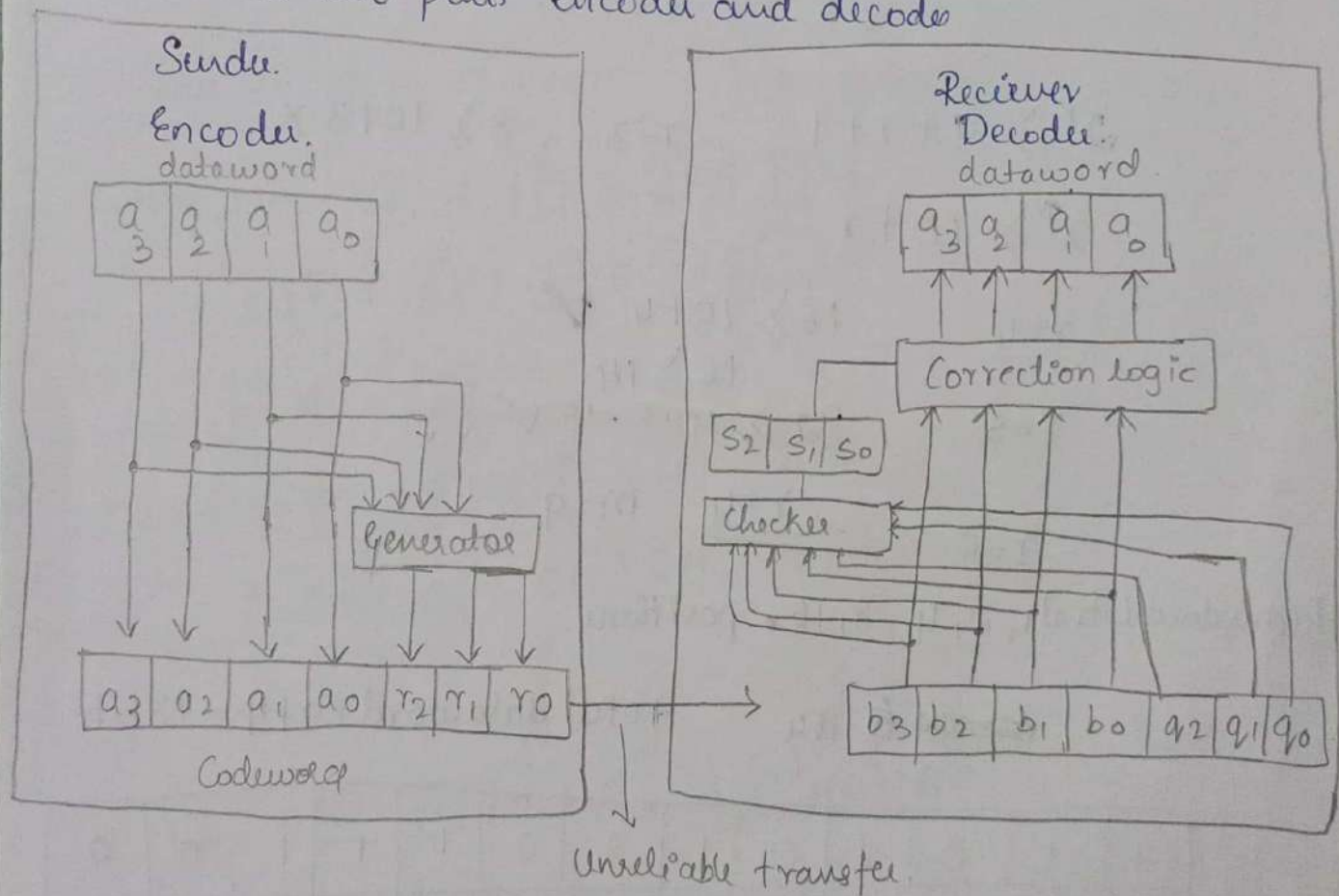
1c) i) Explain Hamming code.

\* Hamming Code was designed by mathematician R.W. Hamming

\* It follows that for data of  $m$  bits the no. of redundant bits is ' $r$ '. And this  $r$  should satisfy

$$2^r \geq m+r+1.$$

\* It has two parts encoder and decoder



$$r_0 = a_2 + a_1 + a_0 \text{ modulo } 2$$

$$r_1 = a_3 + a_2 + a_1 \text{ modulo } 2$$

$$r_2 = a_1 + a_0 + a_3 \text{ modulo } 2.$$

Using modulo 2 redundant bits are generated and embedded with dataword using generator to form codeword

At receive side

$$S_0 = b_2 + b_1 + b_0 + q_0 \text{ modulo } 2$$

$$S_1 = b_3 + b_2 + b_1 + q_1 \text{ modulo } 2$$

$$S_2 = b_1 + b_0 + b_3 + q_2 \text{ modulo } 2$$

$S_0, S_1, S_2$  bits indicate the error location.

ii) Roll No = 419 =  $\begin{matrix} 1 & 0 & 1 & 0 & 0 & 0 & 1 & 1 \\ 8 & 7 & 6 & 5 & 4 & 3 & 2 & 1 \end{matrix}$

$$2^r \geq q + r + 1 \quad r=3, 8 \geq 10+3 \times$$

$$2^r \geq 10 + r$$

$$r=4$$

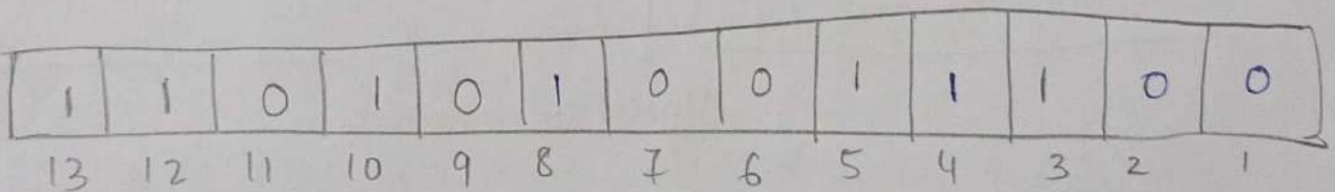
$$16 \geq 10 + 4 \quad \checkmark$$

$$16 \geq 14$$

$$\underline{r=4} \quad m=q$$

Redundant bits at 1, 2, 4, 8 positions.

$$\text{total dataword} = q + r = 13 \text{ bits.}$$



Even parity

$$R_1 = 3 \oplus 5 \oplus 7 \oplus 9 \oplus 11 = 1 \oplus 1 \oplus 0 \oplus 0 \oplus 0 = 0$$

$$R_2 = 3 \oplus 6 \oplus 7 \oplus 10 \oplus 11 = 1 \oplus 0 \oplus 0 \oplus 1 \oplus 0 = 0$$

$$R_4 = 6 \oplus 6 \oplus 7 = 1 \oplus 0 \oplus 0 = 1$$

$$R_8 = 9 \oplus 10 \oplus 11 \oplus 12 \oplus 13 = 0 \oplus 1 \oplus 0 \oplus 1 \oplus 1 = 1$$



Hamming code = 1101010011100

ii) 2nd bit from binary is at third position.  
i.e. 1 should be flipped to 0

So new hamming code incorporating error is

|    |    |    |    |   |   |   |   |   |   |   |   |   |
|----|----|----|----|---|---|---|---|---|---|---|---|---|
| 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 |
| 1  | 1  | 0  | 1  | 0 | 1 | 0 | 0 | 1 | 1 | 0 | 0 | 0 |

          
Error.

Identification of error. (Even parity)

$$R_1 = 1 \oplus 3 \oplus 5 \oplus 7 \oplus 9 \oplus 11$$

$$= 0 \oplus 0 \oplus 1 \oplus 0 \oplus 0 \oplus 0$$

$$= 1$$

$$R_2 = 2 \oplus 3 \oplus 6 \oplus 7 \oplus 10 \oplus 11$$

$$= 0 \oplus 0 \oplus 0 \oplus 0 \oplus 1 \oplus 0$$

$$= 1$$

$$R_4 = 4 \oplus 5 \oplus 6 \oplus 7 = 1 \oplus 1 \oplus 0 \oplus 0 = 0$$

$$R_8 = 9 \oplus 10 \oplus 11 \oplus 12 \oplus 13 \oplus 8 =$$

$$1 \oplus 0 \oplus 1 \oplus 0 \oplus 1 \oplus 1$$

$$= 0$$

$R_8 R_4 R_2 R_1$

0 0 1 1 = 3 = i.e. 2<sup>nd</sup> bit position.

8b) ARP -or- Address Resolution Protocol.

- It is a plug and play device protocol.
- It is used to obtain MAC address from IP address.
- It has two types of messages:

i) Query message: When some host wants to know the MAC of another it uses a query message puts destination IP and tells its adapter it needs MAC of this IP. Then adapter adds broadcast MAC FF-FF-FF-FF address and forwards this broadcast packet to all nodes. The one with matching IP replies back with its MAC.

ii) Response message: This is unicast message. As the destination knows the sender's MAC. He will reply only to sender his MAC address.

→ It is helpful for MAC addressing and to achieve forwarding with switches in LANs such as our University.

→ Why? For obtaining MAC address of nodes that can in turn achieve forwarding without much of layer 3 overhead.



3a)

Pg No - 10

Data center accommodates the resources of organizations like servers, switches, routers etc. all of these are connected via network.

Different types of cables are also used. Eg: Optic fibres.

There are different elements of data center that follow hierarchical topology:

⇒ Switches : Mainly used for LAN's. They can be manageable which helps the administrator to configure or program them - or not manageable or normal switches.

Manageable switches allow port configuration and are more costlier than normal ones. It operates at L2. It is also called as bridge.

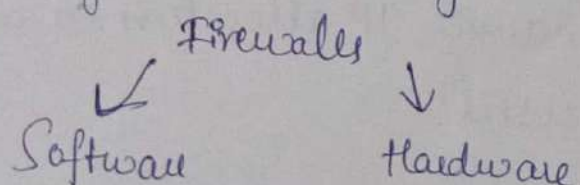
But this might lead to inefficient usage when users in a LAN are less so we go for virtual LAN's to make it efficient and logically isolate port groups to give a separate isolation cell as a physical LAN independent of their positions.

Physically different but logically on same LAN. It also increases a layer of security.

VLAN Trunking → Scalable approach to interconnecting VLAN switches.

VLAN tags → It is added in header that carries the identity of the VLAN to which frame belongs.

⇒ Firewalls: Used for security purpose. They protect internet traffic and not end systems. For end systems we use Anti-Virus.





→ Software firewalls are programmed to detect suspicious activities.

→ Hardware firewalls: looks like switches and come with dedicated preinstalled software. Costs around 21-50 lac.

Eg: Cisco Checkpoint firewall  
Sofos xg450

③ Router: L3 device helps to handle packets moving out in WAN.

④ Gateway: L4 device with knowledge of transport layer protocols. Helps to translate between heterogeneous protocols.

⑤ Access-points

① Entry-level: They come with 2/4/6 antennas. More the antenna more the speed, price and users support.

② High-end access points: More costly than entry level. All antennas are internal.

Eg: - Aruba, Cisco

To control access point we need access-point controller with

3AS - Authentication, Authorization and Accounting  
Radius Server.

→ L3 switch is more useful as it can switch as well as route

→ Core-switch - DHCP is configured

→ DHCP server for dynamic IP allocation so a separate server. Port 67 is used.



## ⑤ Local DNS

- It reduces latency
- Eg: Windows and Linux Update Servers -
- Lower Bandwidth consumption

## ⑥ NMS- Network Management Systems.

- To detect loops
- Which link is down
- SNMP (protocol) (Simple network management protocol) is used and packets are sent to check health status.
- Firewall monitoring
  - Get to know which users have browsed what, when, how long
  - All users currently logged in and history

This is applicable for university data centers. But in case MNC's they have set of data centers. Highly maintained. Usually shown with that colorful optic fibre's in google.

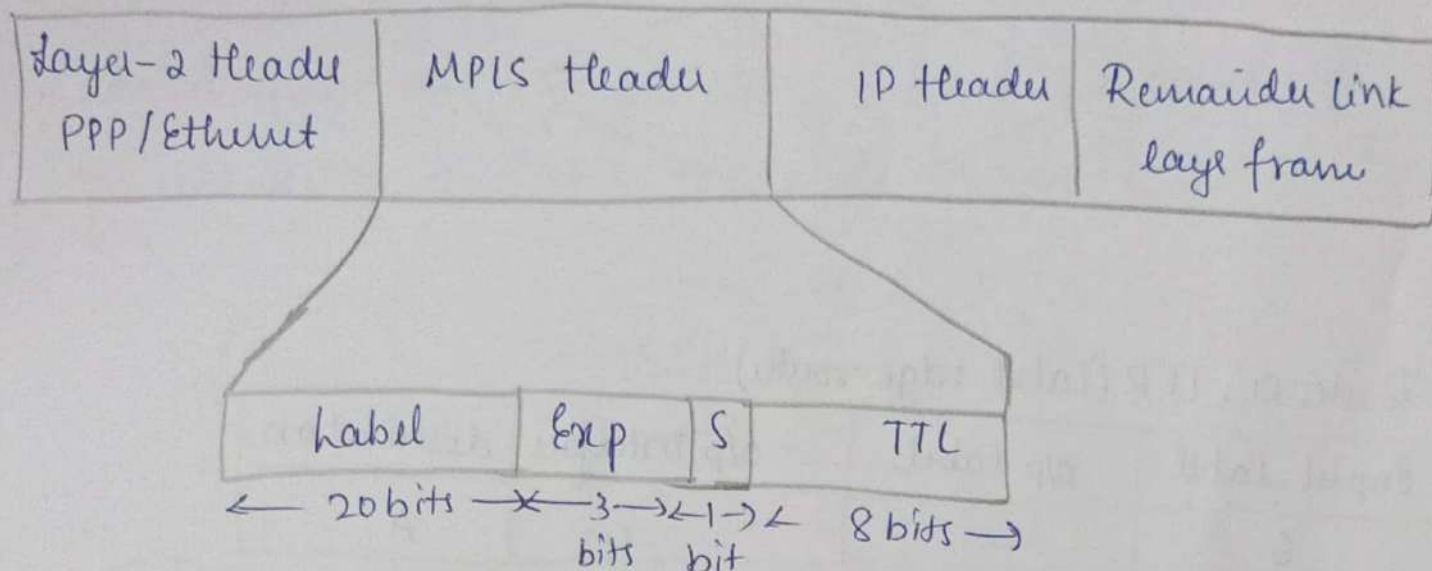
MNC's are more secured for their data they use own security rules.

3b)

### Multi-protocol label Switching (MPLS)

- Combines both the benefits of circuit and packet switching
- Switches based on labels.
- Each interface and dest have unique labels assigned
- Multi-protocol as it takes best of two circuit and packet switching and is independent of lower layer protocols.
- It works at border. i.e. between L3 and L2.
- Label switching → Based on packet i/p label are assigned and they are replaced by output labels just like in virtual circuits.
- It acts as router and switch. So also called as label switched router.
- Achieves extremely fast forwarding.
- It takes IP packet from layer 3 then add label and transmits to layer 2
- If router was used it would do longest prefix match for dest i/p then decide on which of its interface it needs to forward which is time-consuming.
- In virtual circuits VCI or virtual circuit identifiers are used instead of labels

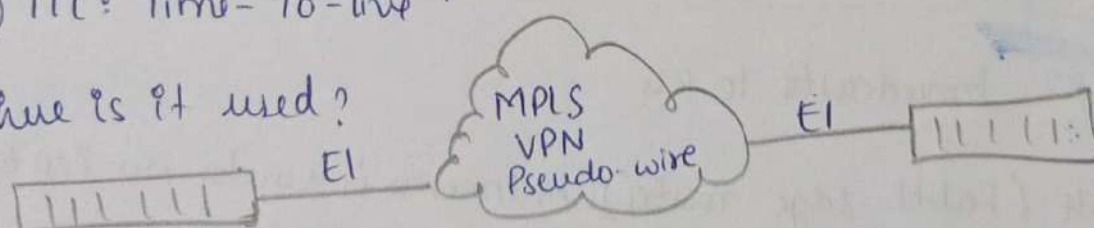




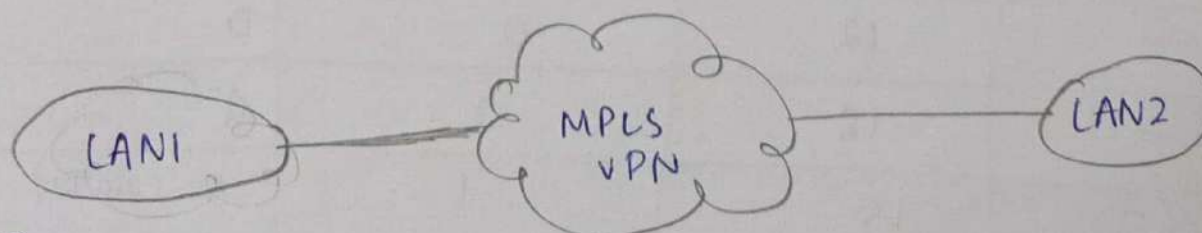
MPLS header is situated between layer 2 and layer 3 header. As it acts at border.

- (i) Labels: It acts as virtual circuit identifier as in case of virtual / circuit switching.
- (ii) Exp: 3 bits for experimental use.
- (iii) S: 1 bit indicating end of series of "stacked" MPLS headers.
- (iv) TTL: Time-to-live

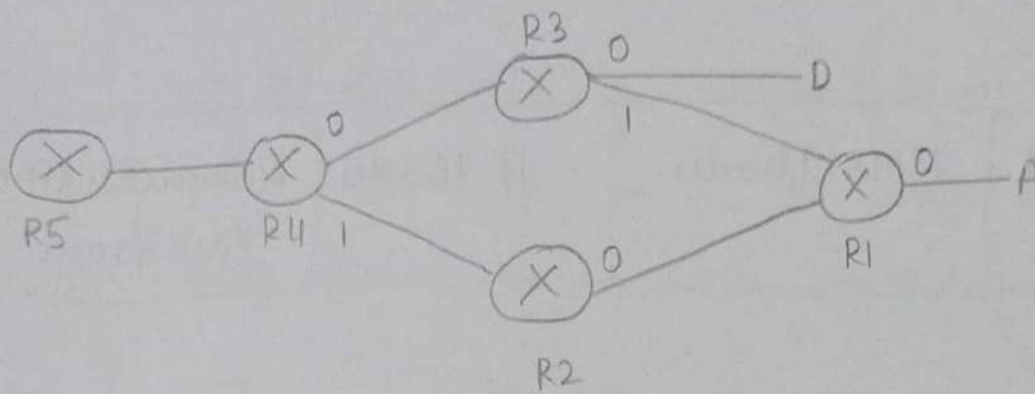
Where is it used?



- (i) When point-to-point connections exist.



- (ii) In Private LAN switches.



Router R1: LER (Label edge router)

| Input Label | o/p label | o/p interface | destination |
|-------------|-----------|---------------|-------------|
| 6           | -         | 0             | A           |

This table is broadcasted to R3 and R2.

Router R3 (LSR - Label switch router)

| I/p label | o/p label | o/p interface | destination |
|-----------|-----------|---------------|-------------|
| 12        | -         | 0             | D           |
| 18        | 6         | 1             | A           |

Router R2 (LSR)

| I/p label | o/p label | o/p interface | destination |
|-----------|-----------|---------------|-------------|
| 15        | 6         | 0             | A           |

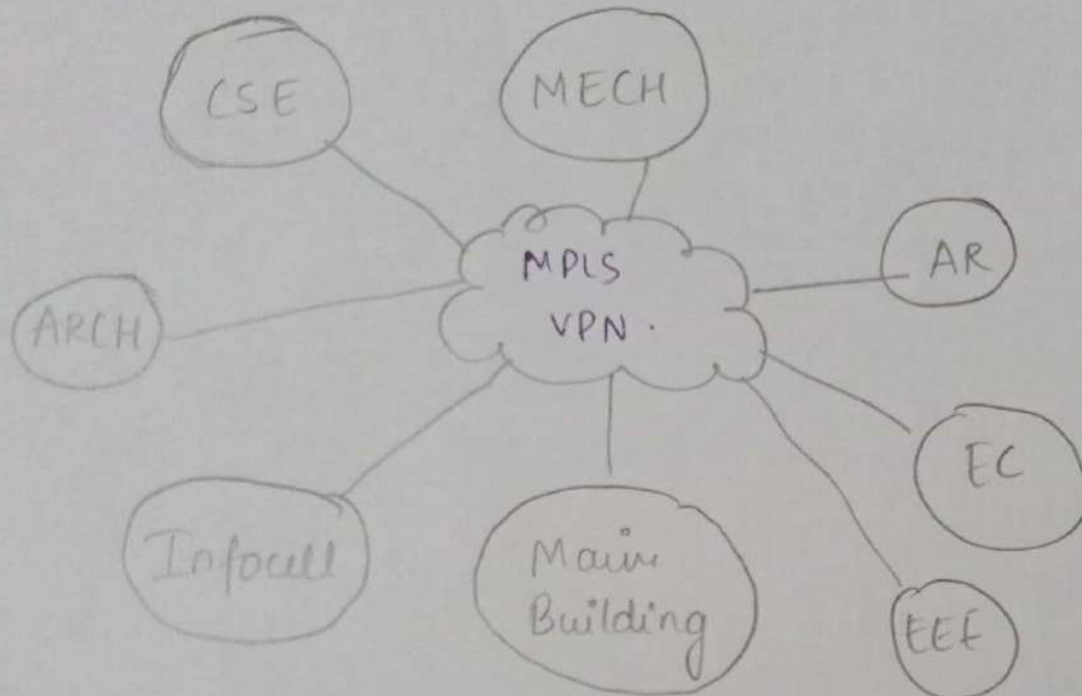
R2 and R3 broadcasts to R4

Router R4 (Label edge router) (As income is router no i/p label)

| I/p label | o/p label | o/p interface | destination |
|-----------|-----------|---------------|-------------|
| -         | 12        | 0             | D           |
| -         | 18        | 0             | A           |
| -         | 15        | 1             | A (via R1)  |



MPLS can be efficient by taking best of both ~~to~~ packet and circuit switching. We have LAN's at our university dedicated for each department.



So it is suitable to interconnect and use them in LAN switches for taking benefits.