

10/04/2020

TRANSACTION (Part I)

- Transaction can take place as:
read (x), modify w(x-1)
let 'x' be the no. of seats to be booked.

* Concurrent Database Access:

- Data present in database can be accessed by DBMS.
- Other softwares are present to control transactions concurrently.
- Multiple clients can access database in concurrent manner.
- Consistent = accurate.

2). Attribute level inconsistency :

If both clients are trying to access the data concurrently (at a same time) then, either Client 1 comes before Client 2 or Client 2 comes before Client 1.

But, result remains same. This is called concurrent execution.

L(Handled)

2). Tuple - level inconsistency :

- $C_1 < C_2$ (C_1 before C_2) } concurrently
- $C_2 < C_1$ (C_2 before C_1) } (one after other)
↳ consistency is maintained.

3). Table - level inconsistency :

C_1 // trying to update 'Emp' table

C_2 // trying to update 'Project'

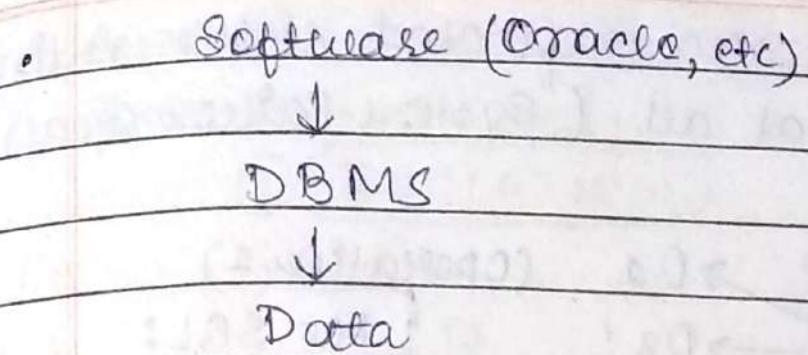
- $C_1 < C_2$ } consistency is maintained
- $C_2 < C_1$ } in both methods.

goal:

A transaction executes a set of operations running in isolated manner.

* Resilience To System Failure :-

- When there is a data transfer happening, then either there should be complete transfer or no transfer.
- Any data is first written into cache then to storage device.



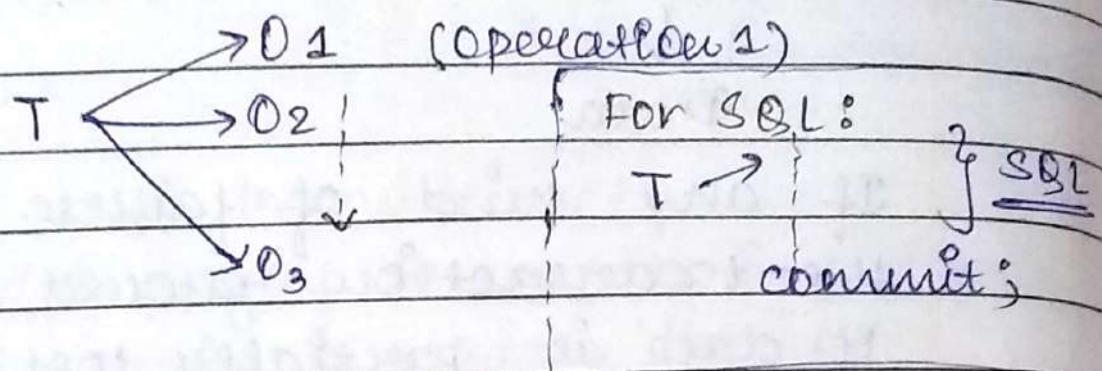
- Software (Oracle, etc) → DBMS → Data
- If any kind of failure occurs, the transaction should ensure that if data is partially lost then it is completely lost. → Resilience to System Failure.
- Once the transaction is committed completed, it should be committed.

Goal: all - or - nothing execution

Solution for both concurrency & failures is TRANSACTION.

- * TRANSACTION is a sequence of one or more SQL operations treated as a unit:
- Transaction appear to run in isolation. (concurrency goal)
- If the system fails, each transaction's

changes are reflected either entirely or not at all (System-Failure Goal).



- Once trans. is committed then anything after that is a new transaction.
- If trans. is not committed then it does "roll back".

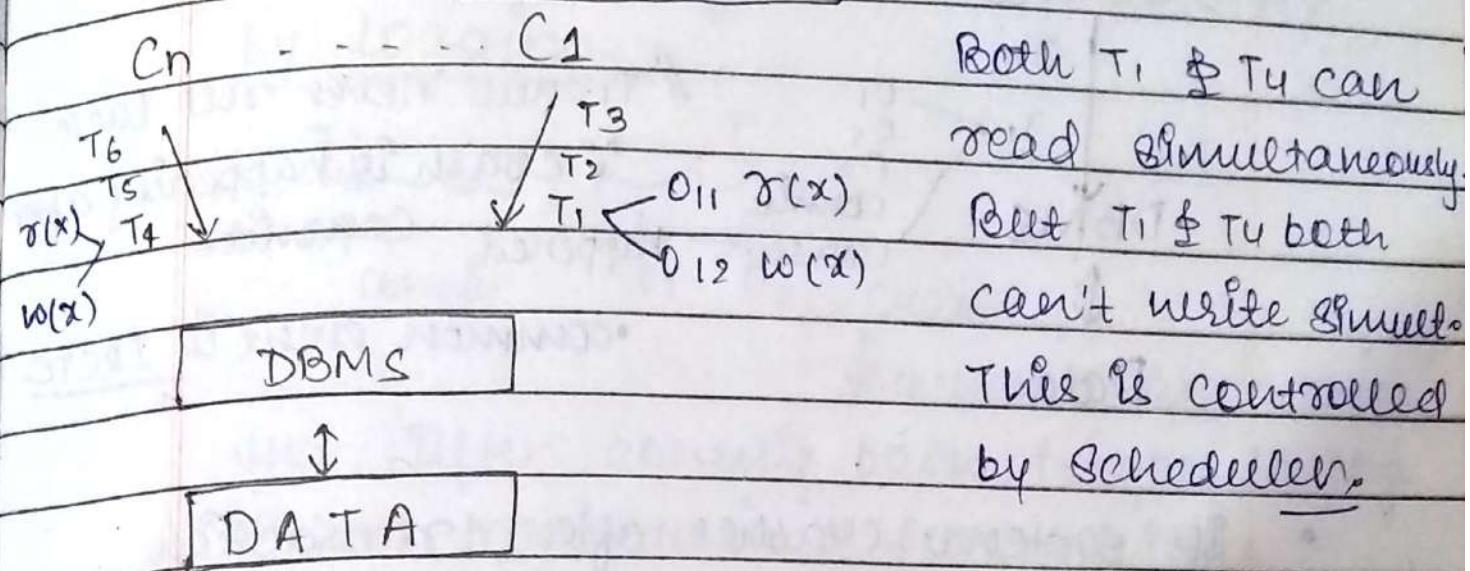
* TRANSACTION PROPERTIES:-

- 1) A Atomicity
- 2) C Consistency
- 3) I Isolation
- 4) D Durability

- Every Trans. 'T' will have Operation 'O' in it. If commit / abort is not present then it is partial transaction.

- Complete Transaction: 'commit' / 'abort' present at end.

① ISOLATION



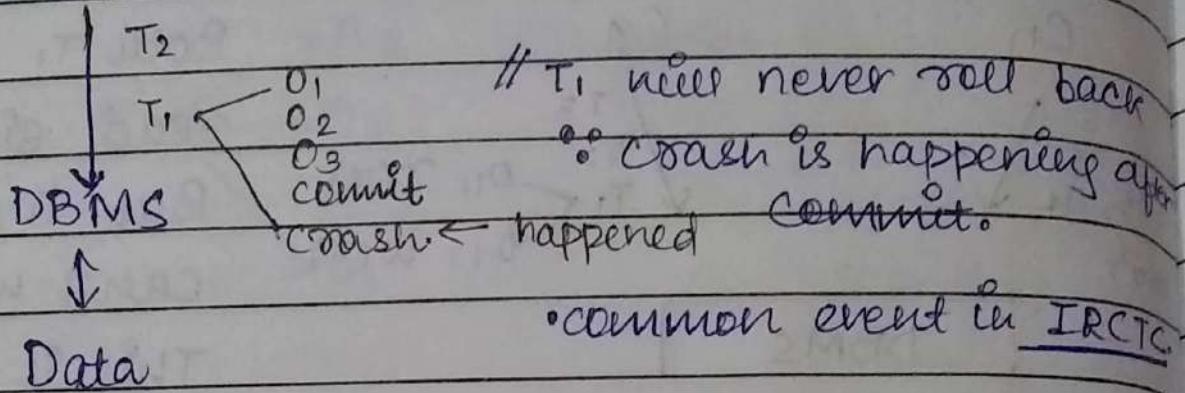
* Serializability :-

- Any tran. whose operations may be interleaved but exec. must be equivalent to some serial order. ~~if all~~
- System guarantees serializability by LOCKING.

Debit Transaction	Credit Transaction
↳ 03 operations	↳ 03 operations
ID1 • Read (A/c no, bal)	Ic1 • Read (A/c no, bal)
ID2 • bal = bal - Amt	Ic2 • bal = bal + Amt
ID3 • Write (A/c no, bal)	Ic3 • Write (A/c no, bal)

② DURABILITY:

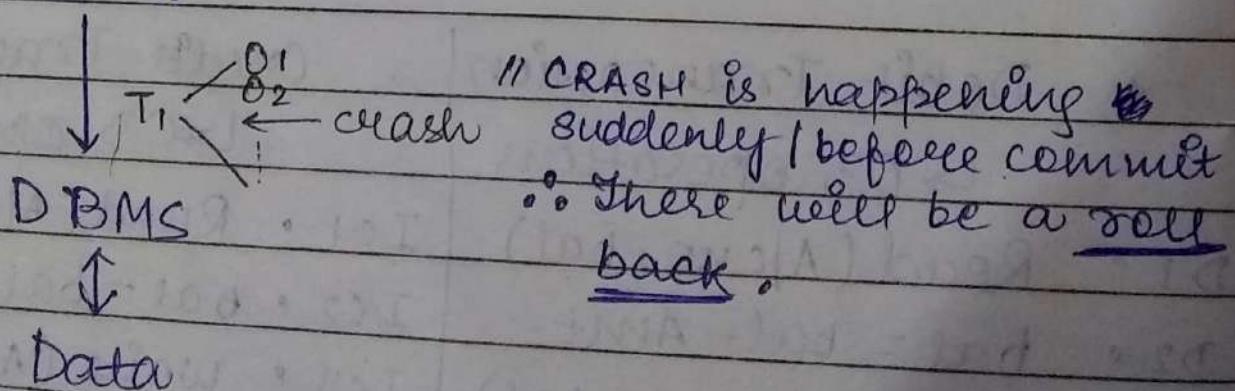
Client



- If system crashes after transaction commits, all effects of transaction remain in database.
- The system guarantees Durability by Logging.

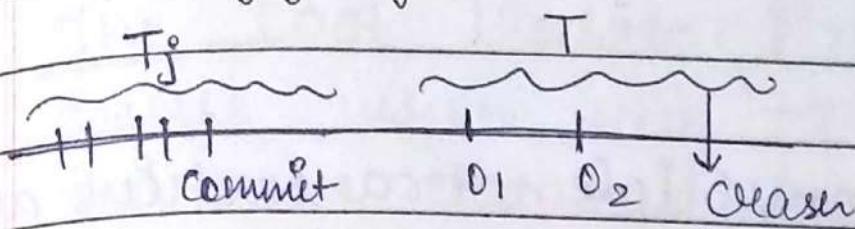
③ ATOMICITY:

Client



Each transaction is "all-or-nothing"
never left half done.

The system guarantees Atomicity
by logging:

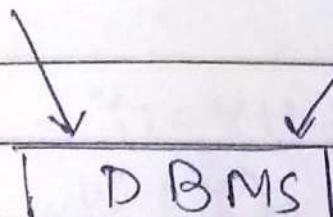


If a crash occurs
then, after coming back it goes to log
it has maintained for previous
transaction (T_j).

logging happens for every operation
of a transaction.

④ CONSISTENCY:

$C_n \dots C_1$



Each client, each trans:

- can assume all constraint hold when trans. begins
- must guarantee all constraints hold when transaction ends.

- * holding \rightarrow releasing \rightarrow commit
the const. (lock) the const. (unlock)
- * consistency is maintained.

Serializability \Rightarrow constraints always hold.

- Lock the resource \rightarrow unlock the resource after committing.
- The system guarantees consistency by
- When multiple transactions are accessing same data simultaneously, the access is protected through [concurrency control mechanism].
 \hookrightarrow Consistency.

E.g.

T₁

read-item (x);

$x := x - N;$

write-item (x);

read-item (y);

$y := y + N;$

write-item (y);

T₂

read-item (x);

$x :=$

write-item (x);

T₁: $\tau_1(x) \text{ } w_1(x) \text{ } \tau_1(y) \text{ } w_1(y)$

T₂: $\tau_2(x) \text{ } w_2(x)$

- * Advantages of concurrency control:
 - increased processor & disk utilization.
 - reduced average response time.

* The Lost Update Problem :-

Occurs when two trans. that access the same database items have their operations interleaved in a way that makes the value of some database item incorrect.

E.g.:

T_1

T_2

$\text{r}(x);$

$x := x - N;$

$w(x);$

$\text{r}(y);$

$y := y + N;$

$w(y);$

$\text{r}(x);$

$x := x + M;$

$w(x); \leftarrow$ Item X has

an incorrect
value because

its update by
 T_1 is "lost"
(Overwritten).

* Temporary Update (Dirty Read) Problem :

Occurs when one trans. updates a database item & then the trans. fails for some reason.

T₁T₂ $\gamma(x);$ $x := x - N;$ $w(x);$ $\gamma(x);$ $x := x + M;$ $w(x);$ $\gamma(y);$

crash → // Transaction T₁ fails & must change the value of X back to its old value ; meanwhile T₂ has read the "temporary" incorrect value of X.

* Incorrect Summary Problem :

↳ Correct summary is achieved when updation takes place after reading the item (data).

T₁

 r(x);

 x := x - N;

 w(x);

T₃

sum := 0;

 r(A);

 sum := sum + A;

:

read (x);

 sum := sum + x;

 r(y);

 sum := sum + y;

x {

 r(y);

 y := y + N;

 w(y);

T₃ reads x after N is subtracted

ϕ reads y before N is added.

A wrong summary is the result
(off by N).

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* Schedules: set of transactions in which the operations occur in an interleaved manner.

• Let T_1, T_2 : operation of $T_1 \& T_2$ can be interleaved but order of operations of $T_1 \& T_2$ should be maintained.

$$T_1 = R_1(x) W_1(x) \quad T_2 = R_2(x) W_2(x)$$

Eg * Schedule : $R_1(x) R_2(x) W_1(x) W_2(x)$
not a

* Schedule : $\cancel{W_1(x)} R_1(x) R_2(x) W_2(x)$
 \downarrow

Can't do $W_1(x)$ before $R_1(x)$ because the order of operation in transaction T_1 is $R_1(x)$ then $W_1(x)$.

* Serial Schedule:

- Serial Schedule ($T_1 < T_2$) : $R_1(x) W_1(x) R_2(x) W_2(x)$
- ($T_2 < T_1$) : Serial Schedule : $R_2(x) W_2(x) R_1(x) W_1(x)$

for n transactions $\rightarrow n!$ serial schedules
3 \longrightarrow 6 " "

\therefore For 'n' transactions $n!$ serial schedules are possible.

E.g. $R_1(x) R_2(x) W_1(x) W_2(x)$: Non-Serial Schedule

\because Interleaving is happening but not consecutively.

E.g.:

T₁

T₂

$R_1(x)$

$x := x - N;$

$R_2(x);$

$x := x + M;$

$W_1(x)$

$R_1(y)$

$W_2(x)$ \leftarrow Item x has

incorrect value because its update by T₁ is lost

$y := y + N;$

$W_2(y);$

(S_a) $R_1(x) R_2(x) W_1(x) R_1(y) W_2(x) W_2(y)$

E.g.

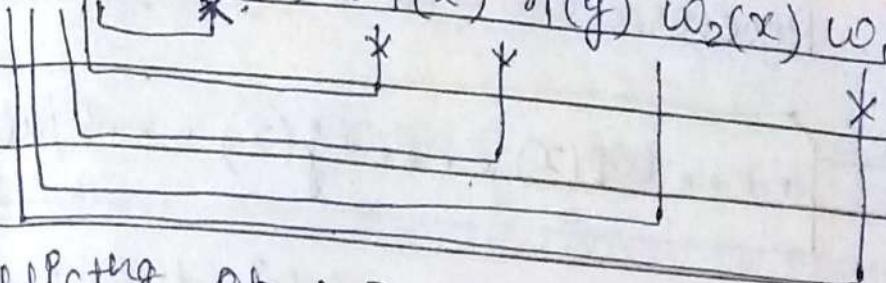
T ₁	T ₂
R ₁ (x);	
x := x - N;	
W ₁ (x)	
	R ₂ (x);
	x := x + M;
	W ₂ (x);
R ₁ (y);	Transact ⁿ . T ₁ fails & must change value of x back to its old value. T ₂ has read incorrect value of x.
	Schedule is : R ₁ (x) W ₁ (x) R ₂ (x) W ₂ (x) R ₁ (y). (S _b)

Conflicting Operations:

Two operations in a schedule are conflicting if :-

- They belong to different transaction
- They access the same item x
- At least one of them is a write(x) operation.

E.g. Sa: $\tau_1(x) \tau_2(x) w_1(x) \tau_1(y) w_2(x) w_1(y)$



∴ Conflicting op: $\tau_1(x) w_2(x)$
 $\tau_2(x) w_1(x)$
 $w_1(x) w_2(x)$

- $\tau(\cdot) \tau(\star)$ can never be conflicting operations.

* TYPES OF SCHEDULES:-

* Recoverable Schedule: if each transaction aborts/commits only after each trans. from which it has read has committed.

T_i	T_j	
:	:	
$w_i(x)$		If T_i commits/aborts before T_j then dirty read takes place.
	$\tau_j(x)$	
	:	
COMMIT/ ABORT	c_i/a_i	
	:	
	c_j/a_j	

→ If these properties are not preserved
then it is recoverable.

Recoverable Schedule :

..... $w_i(x)$ $r_j(x)$... c/a_i ... c/a_j

E.g.: S_a : recoverable

• $S_b : r_1(x); \underbrace{w_1(x); r_2(x)}_{\text{---}}; w_2(x); r_1(y); a_1;$

∴ a_2 will happen by default.

∴ Recoverable.

• $S_a' : r_1(x); r_2(x); w_1(x); r_1(y); w_2(x); c_2; w_1(y); c_1;$

∴ recoverable.

• $S_c : r_1(x); w_1(x); r_2(x); r_1(y); w_2(x); c_2; a_1;$
 //

• $S_d : r_1(x); w_1(x); r_2(x); r_1(y); w_2(x); w_1(y); c_1; c_2;$

// Recoverable

• $S_e : r_1(x); w_1(x); r_2(x); r_1(y); w_2(x); w_1(y); a_4; a_2;$

// Recoverable

② : cascading Rollback Schedule:

Avoiding Cascading Rollback: (ACR)

- A schedule is ACR if transactⁿ may read only values written by committed transactions.
- Note: One where every transactⁿ reads only the items that are written by committed transactions.

③ * Strict Schedule: A schedule in which a trans. can neither read/write an item x until the last trans.^{that} $wrote(x)$ has commit/aborted.

T_i

T_j

$R_i(x)$

$C_i(x)$

$r(\cdot)/w(\cdot)$

c_f/a_f

$S: w_1(x, 5); w_2(x, 8); a_1; // ACR$

// cascadeless; // NOT strict.
(ACR)

I

- * Relationship among different schedules
 - Every ACR is Recoverable.
 - Every strict schedule is ACR.
 - Every strict schedule is serializable.

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- * Serializability: A schedule S of n transactions is serializable if it is equivalent to some serial schedule of the same n transactions.

Ex. Let S_1 (non-serial) | S_2 (serial)
On executing, $\rightarrow \text{res1}()$ | $\text{res2}()$
we get

If $\cdot \text{res1}() = \text{res2}()$ then S_1 (non-serial) schedule is serializable.

- Any serial schedule is serializable always i.e. they give correct result.
- If non-serial schedule is strict then it is serializable.
- If non-serial schedule \rightarrow ACR then may or may not be serializable.

- i). Conflict Equivalence
- ii). View Equivalence

1]. Conflict Equivalence :-

$$T_1 = R_1(x) \quad w_1(x) \quad R_1(y) \quad w_1(y)$$

$$T_2 = R_2(x) \quad w_2(x)$$

Conflicting Operations

Non-serial : $S_1 : R_1(x) \quad w_1(x) \quad \underbrace{R_2(x) \quad w_2(x)}_{\text{Conflicting Operations}} \quad R_1(y) \quad w_1(y)$

Serial $S_2 : \underbrace{R_1(x) \quad w_1(x)}_{\text{Conflicting Operations}} \quad R_1(y) \quad w_1(y) \quad R_2(x) \quad w_2(x)$

- Two schedules are said to be conflict equivalent if the order of any two conflicting operations is the same in both schedules.

• Conflicting Operatⁿ of

$$S_1 : R_1(x) \quad w_2(x)$$

$$w_1(x) \quad R_2(x)$$

$$w_1(x) \quad w_2(x)$$

$\therefore S_1 \neq S_2$ are equivalent.

$$S_2 : R_1(x) \quad \underline{w_2(x)}$$

$$T_1 < T_2$$

$$w_1(x) \quad R_2(x)$$

$$T_1 \rightarrow T_2$$

$$w_1(x) \quad w_2(x)$$

Ques: Check if it is conflict serializable or not?

begin	T ₃	T ₄
	read(x)	write(x)
	write(x)	

Schedule: $r_3(x); w_4(x); w_3(x)$

↳ check if serializable or not?

- Then, find an equivalent schedule.
- ∵ we have 2 transactions then we can have 2! schedules.

a) $T_3 < T_4$

∴ Serial schedule: $T_3(x) W_3(x) W_4(x)$

Conflicting operation: $r_3(x) W_4(x)$

$W_3(x) W_4(x)$

∴ The conflicting operations order is not same as the conf. ops of the schedule given.

∴ It is not an equivalent serializable schedule.

Let's check and one.

b)

$$T_4 < T_3$$

∴ Serial Schedule : $w_4(x) \pi_3(x) w_3(x)$

∴ Conflicting op : $w_4(x) \pi_3(x)$

$w_4(x) w_3(x)$

Again the order doesn't match.

Soln: ∴ The given schedule is not conflicting serializable schedule.

NOTE: If the no. of transactions increases then we will have more no. of serial schedule. So, for checking the conflicting serializability, we go via Precedence - Graph() Algorithm.

* Precedence - Graph() Algorithm :

Step 1: Draw nodes corresponding to each of transaction in S.

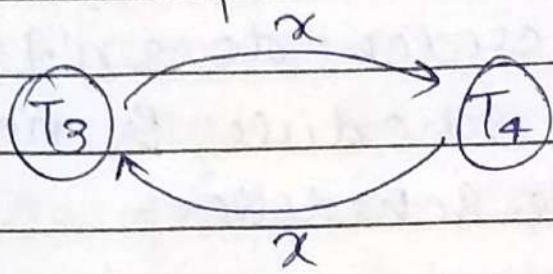
Step 2: Draw an edge from T_i to T_j , if T_i precedes & conflicts with T_j .
Complete the precedence graph.

(conflict) serializability.



Step 3: If precedence graph is acyclic:
then "S is conflict serializable".
else "S is non-conflict serializable".

Previous example:



\therefore conflict op are
 $R_3(x)$ $W_4(x)$
 $W_4(x)$ $W_3(x)$

\therefore precedence graph is cyclic

\therefore It is non-conflict serializability.

Ques:

T_1

T_2

T_3

$R_1(x);$

$W_1(x);$

$R_1(y);$

$W_1(y);$

Schedule
given :-

T₁

T₂

T₃

\because data items

In both are same, we take recent one.

{ R₁(x)

W₁(x) }

R₂(z);

R₂(y);

W₂(y);

R₃(y);

R₃(z);

W₃(y);

W₃(z);

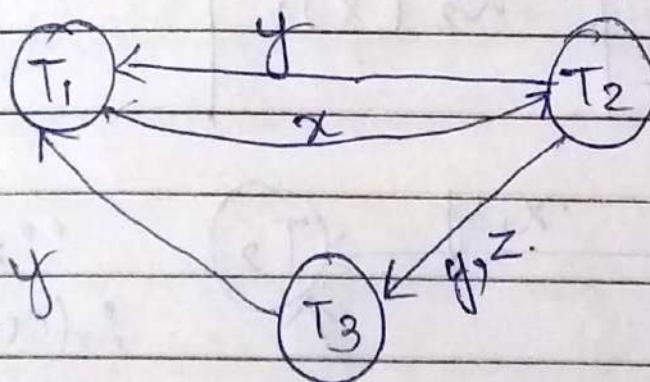
R₂(x)

R₁(y);

W₁(y)

W₂(x)

Sol^{n:}:

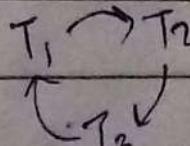


\because There exists a cycle $T_1 \rightarrow T_2 \rightarrow T_3 \rightarrow T_1$ in the precedence graph

\therefore It is non-conflict

serializable.

or non-serializable.



Ques:

T_1 | T_2 | T_3

$R_1(x);$

$w_1(x);$

T_2

T_3

$R_3(y);$

$R_3(z);$

$w_3(y);$

$w_3(z);$

$R_2(z);$

$R_1(y);$

$w_1(v);$

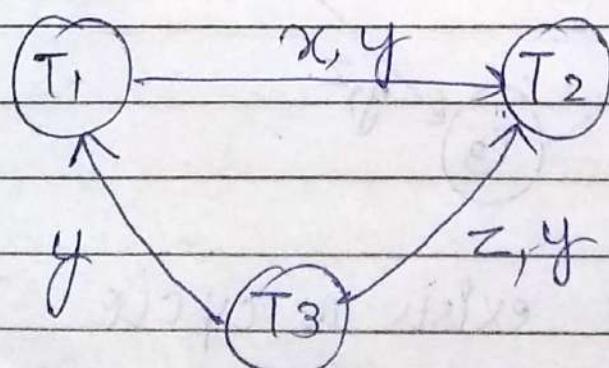
$R_2(y);$

$w_2(v);$

$R_2(x);$

$w_2(x);$

Soln:



\therefore Acyclic

\therefore (Conflict) Serializable

\therefore Equivalent serial schedule: $T_3 \rightarrow T_1 \rightarrow T_2$

Q7

View Serializability:

* $S_1 \oplus S_2$ are view equivalent if following condn's are met:-

- a) If T_i reads initial value of A in S_1 , then T_i also reads initial value of A in S_2 .
- b) If T_j reads value of A written by T_i in S_1 , then T_j also reads value of A written by T_i in S_2 .
- c) If T_i writes final value of A in S_1 , then T_i also writes final value of A in S_2 .

L.C.	S_1	S_2 (Serial)
1)	$\gamma_i^o(A) \dots$	$\gamma_i^o(A) \dots$
2)	$\dots w_j(A) \dots \gamma_j^o(A) \dots \rightarrow$	
3)	$\dots w_i^o(A) \rightarrow \dots i w_i^o(A)$	

R_0q	T_1	T_2	T_3
let $s_1 \rightarrow$	read(x)	write(x)	
	write(x)		write(x)

Sel^u: let a serial schedule for $T_1 < T_2 < T_3$

s_2 be $\rightarrow r_1(x) w_1(x) w_2(x) w_3(x)$

- 1st cond^u is satisfied [$\because R_1(x)$ is the starting value in both $s_1 \oplus s_2$.]
- 2nd cond^u doesn't come into picture [$\because w_i(x) \dots r_j(x)$ doesn't occur anywhere. \therefore need not check].
- $w_3(x)$ is the last value in both $s_1 \oplus s_2$.

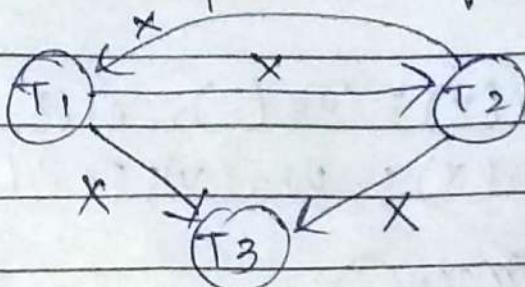
$\therefore s_1$ is equivalent to s_2 .
Thus, the given schedule is
view serializable.

$$S_1 \equiv S_2$$

"To check if a schedule is view serializable or not, check if all ni schedules.

This example only

let's check for conflict serializable also :-



cycle occurs $T_1 \rightarrow T_2$

\therefore Non-conflict serializable.

NOTE :

Every conflict serializable schedule is also view serializable.

may or may not be conflict serializable.

A view serializable schedule that is not conflict serializable has blind writes.

* Find other schedules which are view serializable with S1 :-

1) $T_1 < T_2 < T_3$ or $T_1 \rightarrow T_2 \rightarrow T_3$

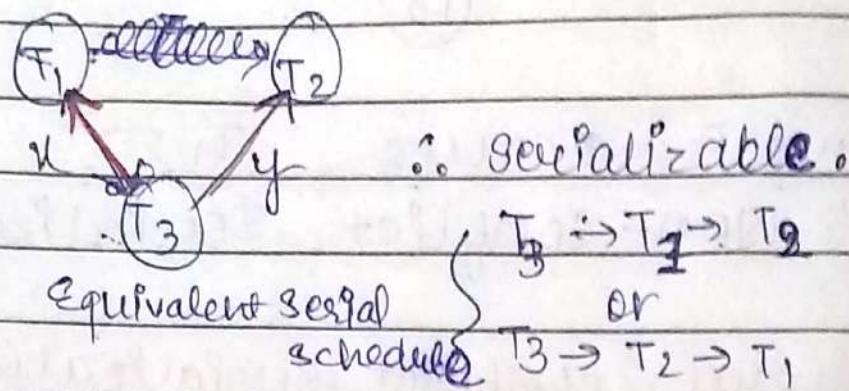
2) ~~$T_1 < T_3 < T_2$~~

Only this is possible.

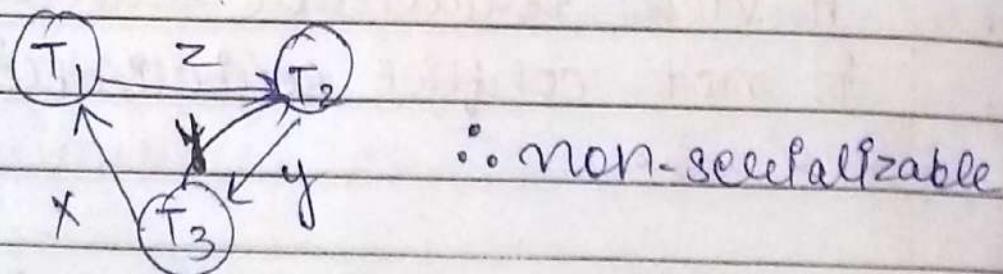
\because we need to have T_1 at first for getting $R_1(X)$ at first & T_3 at last to get $W_3(X)$ at last.

EXERCISES:

LP Q2 :- * $S_1 : \tau_1(x); \tau_2(z); \tau_1(x); \tau_3(x); \tau_3(v);$
 $w_1(x); w_3(y); \tau_2(y); w_2(z); w_2(y)$



* $S_2 : \tau_1(x); \tau_2(z); \tau_3(x); \tau_1(z); \tau_2(v);$
 $\tau_3(y); w_1(x); w_2(z); w_3(y); w_2(v);$



LP Q4 :- ~~Q3~~ $T_1 : R_1(x) R_1(y) W_1(x)$
 $T_2 : R_2(x) R_2(y) W_2(x) W_2(y)$

i) multi-read conflict: //non-serial
 $S : R_2(x) R_2(y) W_2(x) R_1(x) R_1(y) W_2(y)$
 $W_1(x)$

//multiple answers possible.

~~S: R₂(x) R₂(y) W₂(x) R₁ R₁(x) W₁(x) W₁(y)~~

~~R₁(y)~~

~~W₁~~

~~X~~

though, write-read conflict is there
but the order of operations should
be maintained in the transaction.

∴ The schedule is wrong.

∴ Not correct.

ii) read-write conflict :

• R₁(x) R₁(y) W₂(x) R₂(x) R₂(y) W₂(x) W₂(y)

or

• R₂(x) R₁(x) R₁(y) R₂(y) W₁(x) W₂(x)
W₂(y)

iii) write-write conflict :

R₁(x) R₁(y) R₂(x) R₂(y) W₁(x) W₂(x) W₂(y)

LP 85: PTO

Q

(5):

S₃: $\gamma_1(x); \gamma_2(z); \gamma_1(z); \gamma_3(\cancel{x}); \gamma_3(y);$
 ~~$w_1(x); c_1; w_3(y); c_3; \gamma_2(y);$~~
 ~~$w_2(z); w_2(y); c_2$~~ ↓
ACR
(not bad)

- ∴
 - Recoverable
 - ACR
 - Strict

S₄: $\gamma_1(x); \gamma_2(z); \gamma_1(z); \gamma_3(x); \gamma_3(y);$
 $w_1(x); w_3(y); \gamma_2(y); w_2(z); w_2(y);$
 $c_1; c_2; c_3;$ ↓
 not

- Recoverable
- not ACR
- not strict

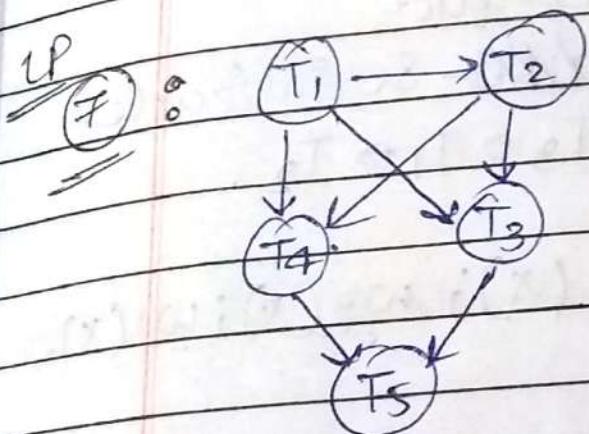
S₅: $\gamma_1(x); \gamma_2(z); \gamma_3(x); \gamma_1(z); \gamma_2(y); \gamma_3(y);$
 $w_1(x); w_2(z); w_3(y); c_2(y); c_3; c_2;$
 c_3 (not strict)

- Recoverable
 - ACR
 - ~~not~~ strict
- } // ∵ There is no write-read conflict of op

- ACR is better but Strict is best.
- Recoverable is minimum req.

	Recoverable	ACR	Strict
S3	✓	✓	✓
S4	✗	✗	✗
S5	✓	✓	✗

$\therefore S_3$ is best.
 S_5 is good enough.
 S_4 is bad.



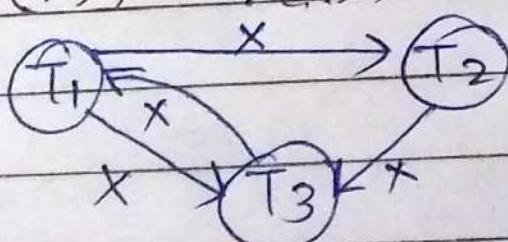
Topological Sort:

$$T_1 \rightarrow T_2 \rightarrow T_3 \leftarrow T_4 \rightarrow T_5$$

$$T_1, T_2, T_3, T_4, T_5$$

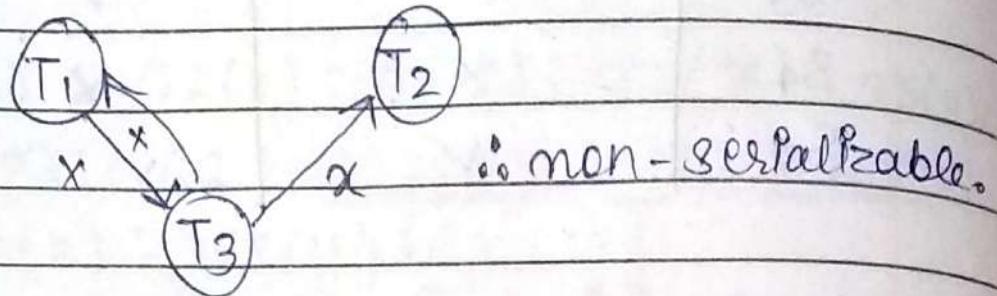
Ques: Whether conflict serializable or not?

a) $x_1(x); x_3(x); w_1(x); x_2(x); w_3(x)$:

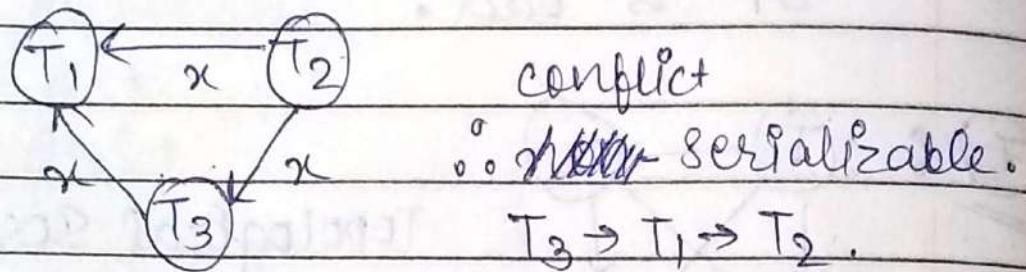


\therefore non-serializable.

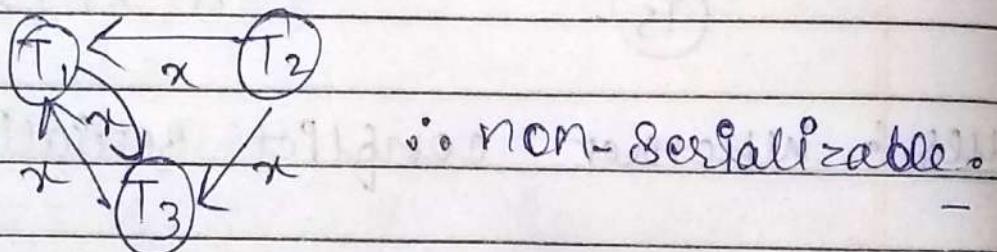
b). $\sigma_1(x); \sigma_3(x); w_3(x); r_1(x); \sigma_2(x)$



c). $\sigma_3(x); \sigma_2(x); w_3(x); r_1(x); w_1(x)$



d). $r_3(x); \sigma_2(x); r_1(x); w_3(x); w_1(x)$



21/04/2020

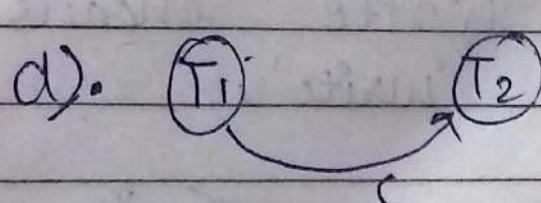
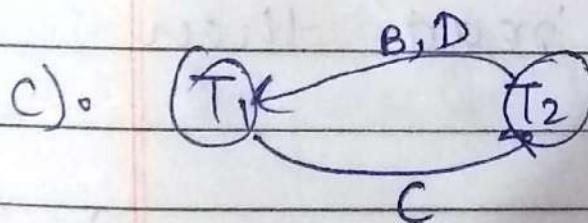
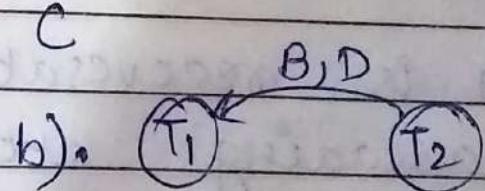
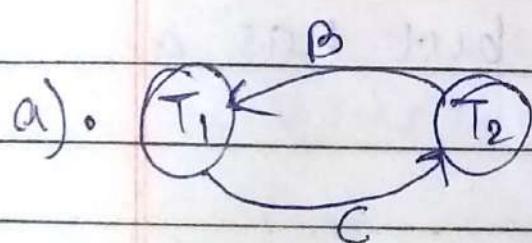
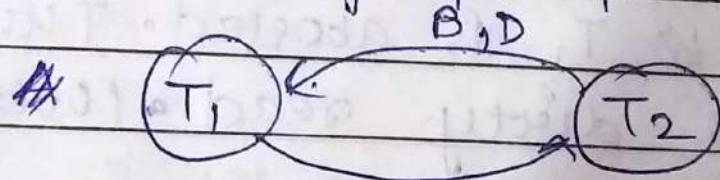
GATE question

(33)

$\sigma_1(A); \sigma_2(B); w_2(B); \sigma_1(C); \sigma_2(D);$
 $w_1(D); w_2(C); w_1(B); c_1; c_2;$

- a) $\tau_1(A); \tau_1(C); w_1(D); \tau_2(B); w_2(B); \tau_2(D)$
 $w_1(B); w_2(C); c_1; c_2;$
- b) $\tau_2(B); w_2(B); \tau_2(D); w_2(C); \tau_1(A); \tau_1(C);$
 $w_1(D); w_1(B); c_1; c_2;$
- c) $\tau_2(B); w_2(B); \tau_2(D); \tau_1(A); \tau_1(C);$
 $w_1(D); w_1(B); w_2(C); c_1; c_2;$
- d) $\tau_1(A); \tau_1(C); w_1(D); w_1(B); \tau_2(B);$
 $w_2(B); \tau_2(D); w_2(C); c_1; c_2;$

Solⁿ Precedence graph for question :-



\therefore question graph matches with 'c'.
 \therefore It is conflict serializable with 'c'.

GATE:

Ques: $S = \pi_2(x); \pi_1(x); \pi_2(y); w_1(x); \underline{\pi_1(y)}; w_2(x); a_1; a_2$

which is TRUE?

- a). S is non-recoverable.
- b). S is recoverable, but has a cascading abort.
- c). S does not have a cascading abort.
- d). S is strict.

Soluⁿ: $w_1(x); \pi_1(y); w_2(x); a_1; a_2$

After $w_1(x)$ is writing X , then for $w_2(x)$ i.e. T_2 writes X (reads X & then write X).

But ~~T_1~~ T_1 is aborted. Thus, there exists dirty read (overwriting).

\therefore It is recoverable but has a cascading abort.

// 'write' means 'read' then 'write'.

~~T_1~~ If T_1 is aborted then T_2 is also aborted here.

Gate 2015

Q9.

	T ₁	T ₂
1		
2	$\tau(A)$	
3	$w(A)$	
4		$\tau(C)$
5		$w(C)$
6		$\tau(B)$
7		$w(B)$
8		$\tau(A)$
9	$\tau(B)$	commit

Transaction T₁ fails immediately
after time instance 9. which is
correct?

- T₂ must be aborted & then both T₁ & T₂ must be restarted to ensure trans. atomicity.
- Schedule S is non-recoverable & can't ensure trans. atomicity.

Solⁿ: T₂ can't be aborted 'cause it is committed. 'S' is recoverable.

Atomicity → X not ensured (all or nothing)

NOTE: If T_2 is not committed at time instance 8 then option 'A' would be correct.

T_2 should be aborted & T_1 should re-start.

DBMS : Chapter 05

TRANSACTION PROCESSING

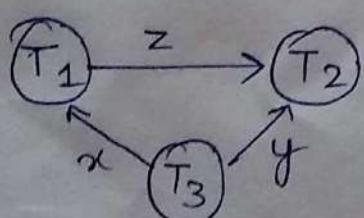
2). A user must guarantee that the transaction does not corrupt data or insert irrelevant data into the database.

E.g.: In a bank database, a user must guarantee that a cash withdrawal transaction must accurately model with the amount a person removes from his/her bank account.

A DBMS must guarantee that transactions are executed fully and independently of all other transactions. An essential property of a DBMS is that the transaction should execute in isolation and DBMS must guarantee isolation with respect to concurrent execution of several transactions and database consistency. DBMS can guarantee isolation through locking.

↳ [Questⁿ no. in LP (error)]

3). $S_1 : \tau_1(x); \tau_2(z); \tau_1(z); \tau_3(x); \tau_3(y); w_1(x);$
 $w_3(y); \tau_2(y); w_2(z); w_2(y);$



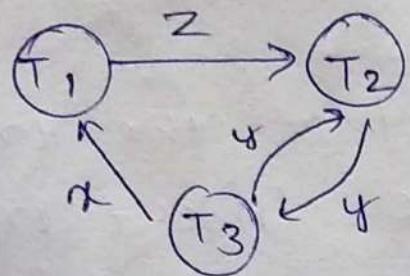
∴ Graph is acyclic.

∴ It is conflict-serializable.

∴ Equivalent Schedule is :

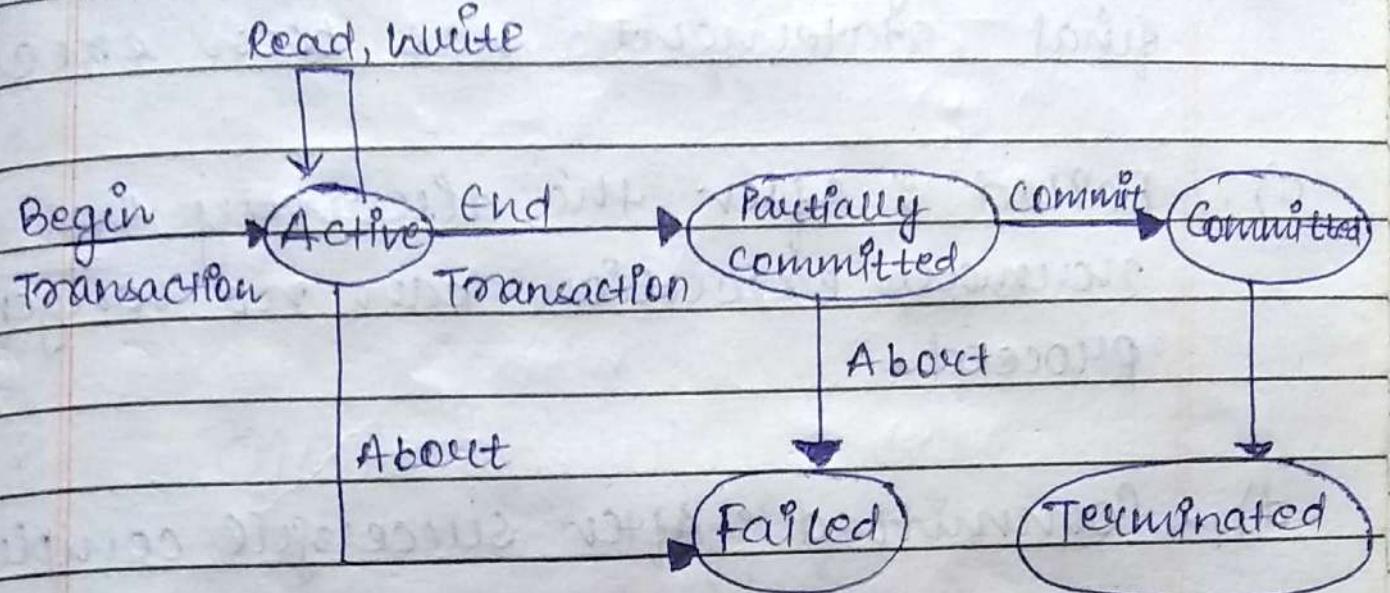
$$T_3 \rightarrow T_1 \rightarrow T_2$$

$S_2 : \tau_1(x); \tau_2(z); \tau_3(x); \tau_1(z); \tau_2(y); \tau_3(y); w_1(x);$
 $w_2(z); w_3(y); w_2(y);$



∴ Graph is cyclic.
∴ It is non-conflict
Serializable.

③ * TRANSACTION STATE :



A transaction is an atomic unit of work, that is either completed entirely or not done at all.

For recovery purposes, the system needs to keep track of when the transaction starts, terminates and commits or aborts.

A transaction must be in one of the following states:

a). Active : the initial state.

Transaction stays in it while it is executing.

- b). Partially committed : After the final statement has been executed.
- c). Failed : After the discovery that normal execution can no longer proceed.
- d). Committed : After successful completion.
- c). Aborted : After transaction has been rolled back and the database is restored to its prior state.
Two options are there, once it is aborted :
- KILL the transaction
 - restart the transaction (only if no integral logical error)

4).

(i) Write - Read conflict:

$\tau_2(x)$ $\tau_2(y)$ $w_2(x)$ $\underbrace{\tau_1(x)}$ $w_2(y)$ $w_1(x)$

(ii) Read - Read conflict:

$\tau_1(x)$ $\tau_1(y)$ $\tau_2(x)$ $\underbrace{\tau_2(y)}$ $w_1(x)$ $w_2(x)$ $w_2(y)$

(iii) Write - Write Conflict:

$\tau_1(x)$ $\tau_1(y)$ $\tau_2(x)$ $\tau_2(y)$ $w_1(x)$ $w_2(x)$ $\underbrace{w_2(y)}$

5). $S_1 : \tau_1(x); \tau_2(z); \tau_1(z); \tau_3(x); \tau_3(y); w_1(x); c_1;$
 $w_3(y); c_3; \tau_2(y); w_2(z); w_2(y); c_2$

Recoverable	ACR	Strict.
✓	✓	✓

T_1
 $\tau_1(x)$

$\tau_1(z)$

$w_1(x)$
 c_1

T_2
 $\tau_2(z)$

$\tau_2(y)$
 $w_2(z)$
 $w_2(y)$
 c_2

T_3

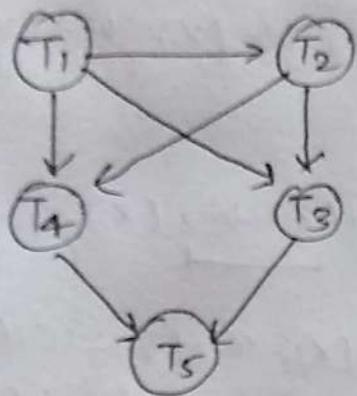
$\tau_3(x)$
 $\tau_3(y)$

$w_3(y)$
 c_3

$S_2 : \sigma_1(x); \tau_2(z); \tau_1(z); \sigma_3(x); \tau_3(y); w_1(x); w_3(y);$
 $\sigma_2(y); w_2(z); w_2(y); c_1; c_2; c_3;$

Recoverable		ACR		Stable
x		x		x

7).



∴ The precedence graph is acyclic. ∴ It is conflict-serializable & the equivalent serial schedule is :-

$T_1 \rightarrow T_2 \rightarrow T_3 \rightarrow T_4 \rightarrow T_5$

(Topological Sort)

① ISOLATION:

Serializability - Operations may be interleaved, but execution must be equivalent to some serial order of all transactions.

The System ensures serializability by locking.

Date _____
Page _____

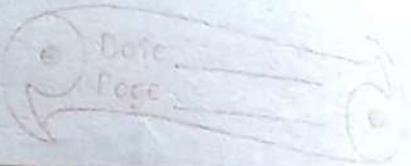
Transaction property Isolation tells that the results of one transaction will not be visible to other transaction till the transaction "commits".

② DURABILITY:

- If system crashes after transaction commits, all effects of transaction remain in database.
- It is guaranteed by Logging.
- The effect of committed transactions are not lost after commitment.

③ Atomicity:

- Each transaction is "all-or-nothing" never left half done.
- The system guarantees atomicity by Logging.



- Either all the instructions are executed in full or none.

④ CONSISTENCY :

- Each client, each transaction
 - can assume all constraints hold when transaction begins.
 - must guarantee all constraints hold when transaction ends.
- When multiple transactions are accessing the data simultaneously, the access is protected through concurrency control mechanisms.

21/04/2020



Chapter 0.6

CONCURRENCY CONTROL TECHNIQUES

- These are the algorithms that are going to be used in order to handle conflicting operations to ensure serializability.
 - Solution to issues in concurrency is serialization through Locking.
 - Before reading \rightarrow lock it
after writing \rightarrow unlock it
- * Optimistic CC Algo :
- Timestamp Alg (Basic timestamp)
Optimistic : Conflicts are extremely low.
- * Pessimistic CC Algo :
locking Based Alg (2 phase locking)
PPL
- Pessimistic : Conflicts are extremely high.

★ Pessimistic CC Algos :

* Locking Based Alg :

① Binary Lock :

LOCK \rightarrow 1

NO LOCK \rightarrow 0

disadv: we don't want the lock to be acquired for reading.

- It doesn't take care of read op where 'read' is a kind of shared operation.

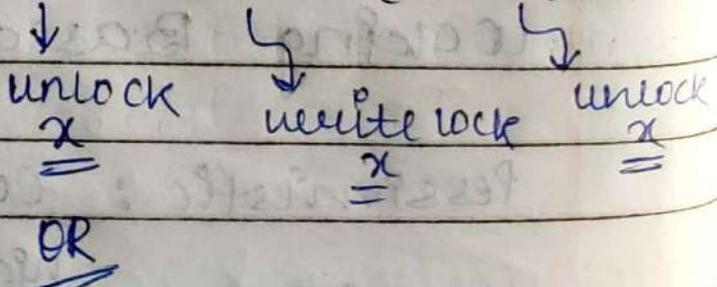
② Read/Write locks : (Shared/Exclusive)

read-lock(x) $\tau(x)$, $w(x)$ lock

write-lock(x).

- If we have both of $\tau(x)$ $w(x)$ to be performed, it is better to acquire write lock initially.

$\tau l(x)$ $\tau(x)$ $w l(x)$ $w(x)$ $w l(x)$



$wL(x) \cap (x)$

$w(x) \cap L(x)$

and $r(x)$

∴ we can perform $w(x)$, op when $wL(x)$ is acquired.
whereas, $w(x)$ can't be performed if we are holding a read lock rl.

24/04/2020

* **Goldilocks Rule**: One shouldn't be too optimistic or too pessimistic
↳ today used even for AI technology

NOTE: Shared / Exclusive locking scheme does not guarantee Serializability

In Order to guarantee Serializability we go for 2 PL (2 phase locking)

* **2 Phase Locking**:

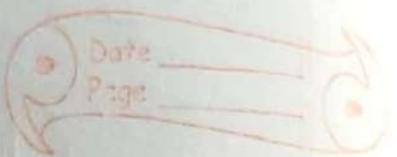
(Schedule is guaranteed to be serializable).

1st phase: growing / expanding phase.

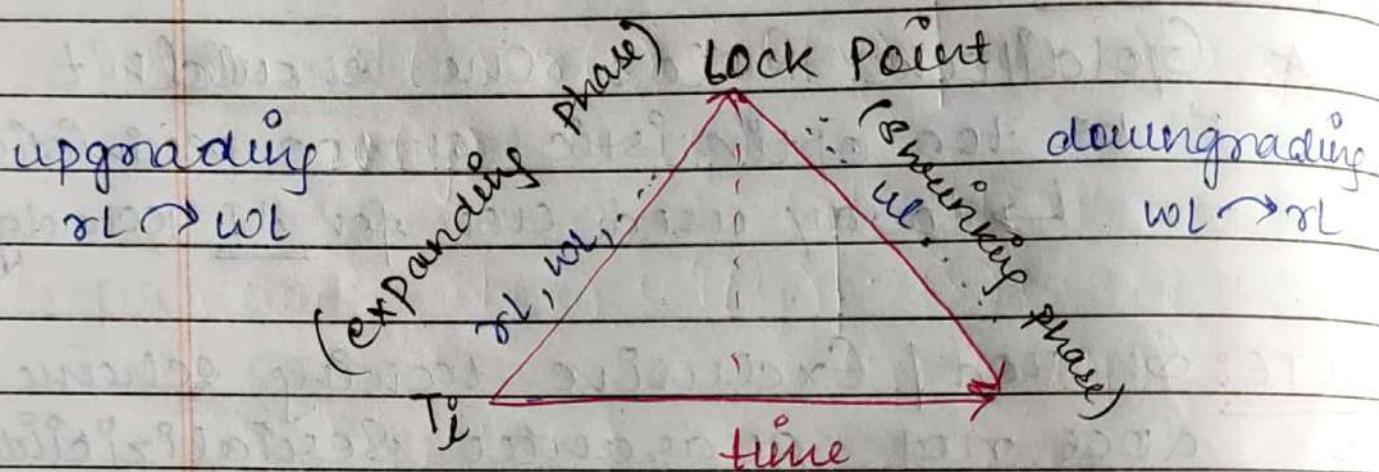
2nd phase: shrinking phase.

P.T.O

- $\text{rl} \rightarrow$ read lock
- $\text{wl} \rightarrow$ write lock
- $\text{ul} \rightarrow$ unlock



- In the growing phase, the transaction has to acquire all the locks of other op. Once it reaches the lock point, it will start its shrinking phase (decon). During shrinking phase, op can be performed and locks can be unlocked.



- **Upgradation** of locks can happen in growing phase.
→ upgrading rl to wl .
- **Downgrading** of locks can happen during shrinking phase.
→ we have wl & we want to convert to rl .

$\{$ guarantees serializability. $\}$
2PL \leftarrow T_i

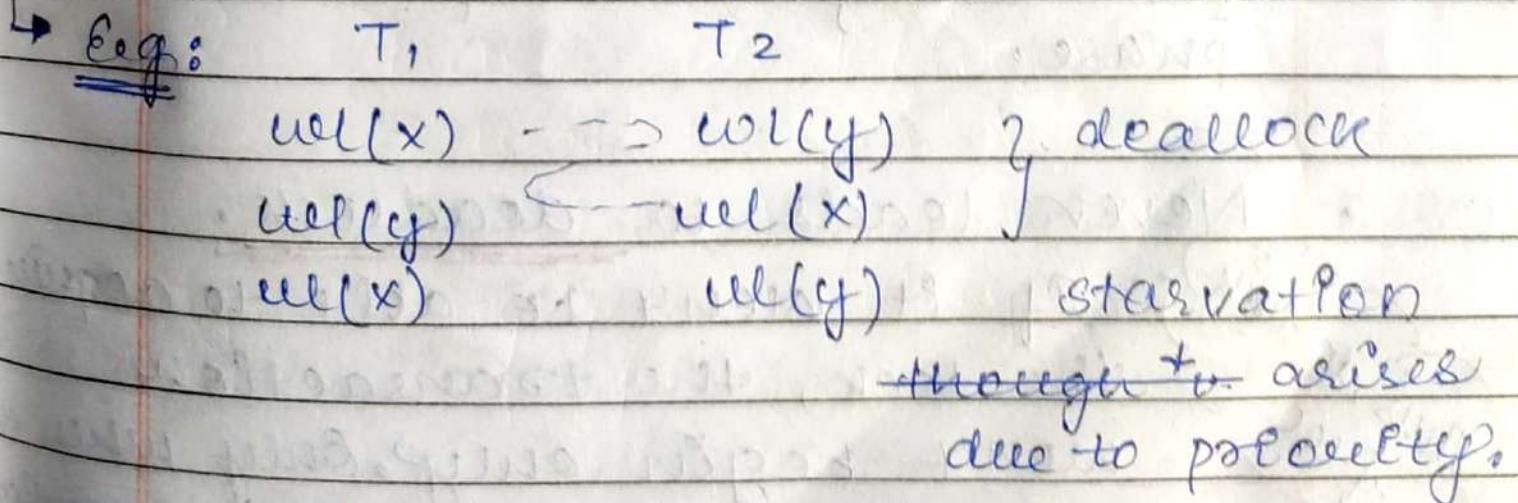
$\{$ doesn't guarantee serializability. $\}$
Shared / Exclusive
lock

E.g.	$\{$ $rl(Y);$ $r(Y);$ $wl(X);$ $ul(Y);$ $r(X);$ $x := x + Y;$ $w(X);$ $ul(X)$	$rl(Y);$ $r(Y);$ $wl(Y);$ $ul(X);$ $r(X);$ $x := x + Y;$ $w(X);$ $ul(X);$
shutting phase		

- The use of locks can cause two disadvantages:
 - deadlock
 - starvation

NOTE

- If every transaction in a schedule follows the 2PL protocol, the schedule is guaranteed to be serializable.



* Variations of QPL :

- Basic
- Conservative (deadlock free)
- Strict
- Rigorous

E.g.: $T_1 : \gamma(x) \gamma(y) w(y)$

$\therefore \underline{\text{QPL}} : \gamma_L(x) \gamma(x) w_L(y) | w(x) \gamma(y)$
 $w(y) w(y)$ lock point

$\therefore \underline{\text{conservative}} : \gamma_L(x) w_L(y) | \gamma(x) w(x) \gamma(y)$
 $w(y) w(y)$

* **Conservative QPL:** All the locks are acquired before execution begins of a transaction, then op. of unlocking takes place in shrinking phase.

• Never leads to deadlock.

(thus deadlock free) If it can't be able to acquire the lock, the transaction won't begin only. Only when the locks required for trans. execution are acquired, then only trans. starts.

- If both $\gamma(B)$ & $w(B)$ is there, then put $WL(B)$.

- * Strict 2PL: A trans. 'T' does not release any of its exclusive(written) locks until after it commits or aborts.

Strict: $UL_1(x) \dots C_1 \dots UL_1(x)$

2PL: $UL_1(x) \dots UL_1(x) \dots C_1$

- It may have deadlock.

- * Rigorous 2PL: trans. 'T' does not release any of its locks until after it commits or aborts. $\rightarrow (\gamma/w)$

- It guarantees strict schedules.

2PL Example (1)

$T_1 : \gamma(A);$
 $\gamma(B);$

if $A=0$ then $B=B+1;$
 $w(B);$

$T_2 : \gamma(B);$
 $\gamma(A);$

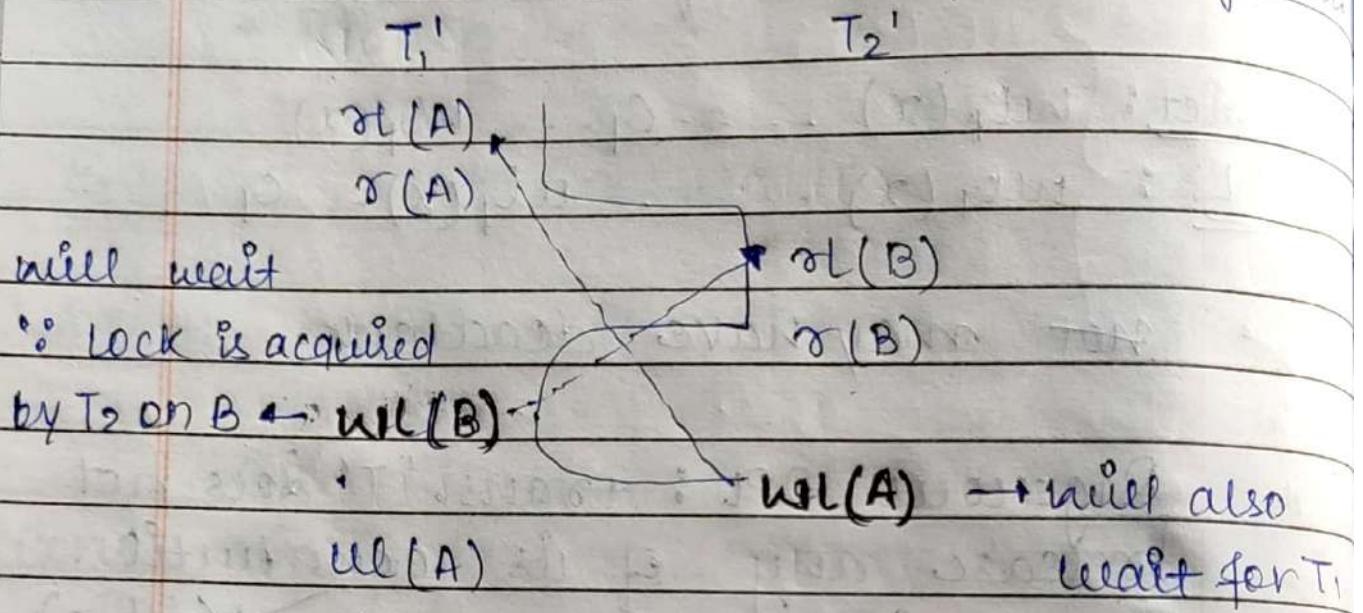
if $B=0$ then $A=A+1$
 $w(A);$

\leftarrow 2PL $T_1 : \gamma(A) \gamma(B) WL(B) \mid \cancel{UL(A)} \gamma(B)$
 $w(B) UL(B)$

$T_2 : \gamma(B) \gamma(A) WL(A) \mid \cancel{UL(B)} \gamma(A) w(A) UL(A)$

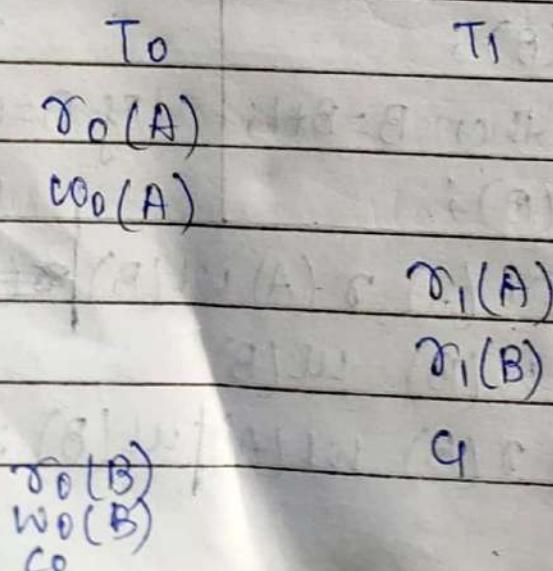
Q8 Can the execution of these transactions result in a deadlock?

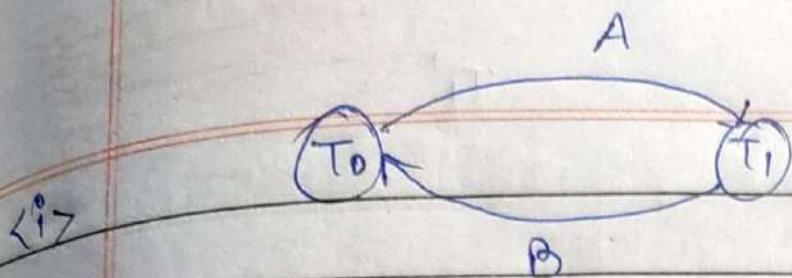
// Take An interleaved schedule of execution



∴ Both are waiting for each other
∴ This is where deadlock is occurring.

* Example (2)





so Non-conflict Serializable

Put them into QPL form first.
then check.

T_0'	T_1'	T'
WL(A)	RL(A)	RL(A)
$\gamma(A)$	$\gamma_L(A)$	$\gamma(A)$
$W(A)$	$\gamma(A)$	$WL(B)$
UL(B)	$\sigma(B)$	UL(A)
$UL(A)$	$UL(A)$	$\gamma(B)$
$\gamma(B)$	$UL(B)$	$UL(B)$
$W(B)$		C_1
$UL(B)$		
C_0		

Po To O

- Find QPL form
- Make a schedule
- check for serializability

(P_i) contd.

T₀'

T₁'

WL(A)

$\gamma(A)$

W(A)

UL(B)

UL(A)

$\gamma L(A)$

$\gamma(A)$

$\gamma(B)$

W(B)

UL(B)

∞

$\gamma L(B)$

UL(A)

$\gamma(B)$

UL(B)

C₁

A, B

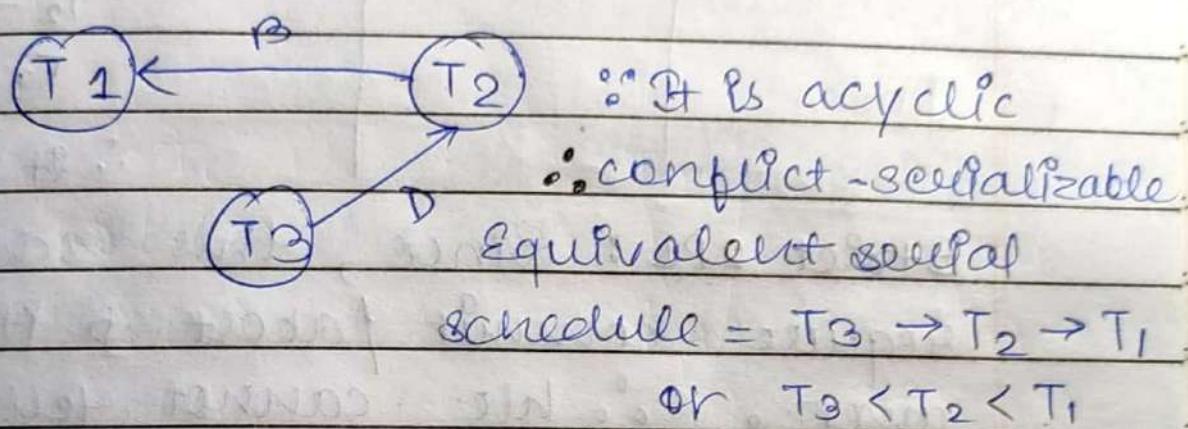


∴ conflict - Serializable.

* Example (3):

$S_1: \tau_3(D) \text{ w1(A)} \tau_2(B) \text{ w3(D)} \text{ w1(B)}$
 $\tau_2(D)$

Serializable :-



2PL :-

T_1'	T_2'	T_3'
$wL(A)$	$\tau L(D)$	
$w1(A)$	$\tau 3(D)$	
	$\tau L(B)$	
	$\tau 2(B)$	
		$w3(D)$ lock pt
		$w1(D)$
	$\tau L(D)$ lock pt	
	$w1(B)$	
$w1(A)$ lock point	$wL(B)$	
	$w1(B)$	
	$w1(A)$	
		P.T.O

T_1'	T_2'	T_3'
$w_e(B)$		
	$\gamma(D)$	\therefore It is not 2PL
	$w_e(D)$	

$T_1 \rightarrow 2PL \checkmark$

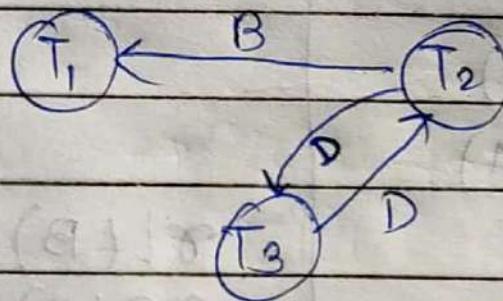
$T_2 \rightarrow 2PL \checkmark$

$T_3 \rightarrow 2PL \checkmark$

\therefore It is 2PL.

Strict 2PL: Since, this schedule requires commit / abort & Pt is not given. \therefore We cannot tell about Strict 2PL.

⑪ $S_2 : w_1(A) \gamma_2(B) w_3(D) w_1(B)$
 $\gamma_2(D) w_3(D)$



\therefore Non-conflict serializable.

NOTE: every 2PL must be serializable.

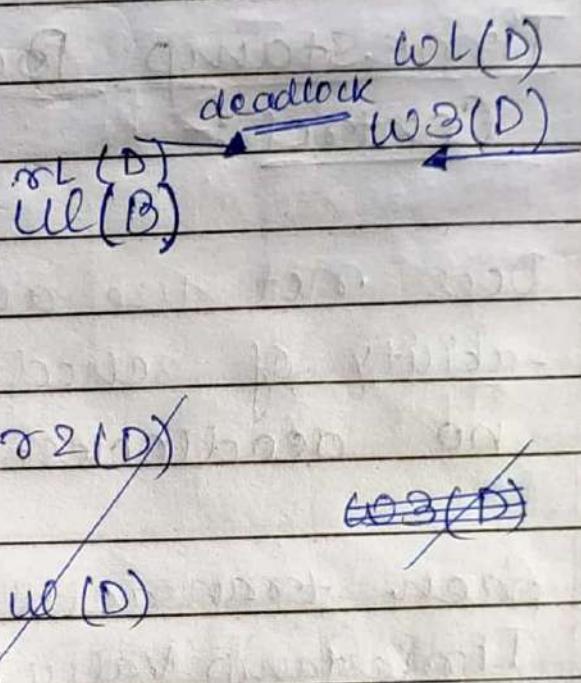
• 2PL :-	T ₁ '	T ₂ '	T ₃ '
	WL(A)		
	W ₁ (A)		
	WL(A)		
		WL(B)	
		W ₂ (B)	

T₂' will wait
for T₃' to release 'D'.

∴ Can't be 2PL.

¶ T₃' can't release
because W₁(B)

W₃(D) is again
present at end.



• Strict 2PL :- X Not strict 2PL.

V.V.I

NOTE: If a schedule is non-serializable
then it won't be in 2PL. Similarly,
it won't be in strict 2PL.

∴ Every 2PL must be
serializable.

(But, we need to show in exam).

epoch converter → time stamp generator
(system)

* TIMESTAMPING:

- A unique identifier created by DBMS that indicates relative starting time of a transaction : TIMESTAMP

* Timestamp Based concurrency Control :-

- Does not use any locks for serializability of schedules. Hence, no deadlocks.
- Each transaction will be assigned a Timestamp Value T_p based upon the order in which the transactions are executed.
- Recent one → Youngest One
Older one → Elder one

Any conflicting read/write opⁿ are executed in timestamp order.

- $W - TS(x)$: largest timestamp of any trans. that executed $W(x)$ successfully.
 - $R - TS(x)$: " " " " " $R(x)$.
 - $TS(T_i)$: timestamp (TS) of trans. T_i .

Basic timestamp Ordering Algo:

~~if~~ Ti requests for read(x)

```

if TS(Ti) < hi-Ts(x) // checking for
[ ] rollback; (abort) Older //
[ ] // came late //

```

else

5

read(x);

$$R-TS(x) = \max(R-TS(x), TS(\tau_i^o));$$

3

If Ti requests for write(x)

$\text{TS}(T_i) < R - TS(x) \quad \text{or} \quad TS(T_i) < w - TS(x)$

4 rollback; abort //oldest//

else

//Youngest//

$$\text{WTS}(x) = \text{TS}(\text{Ti});$$

4

1 Largest = Youngest

* Strict Timestamp Ordering:

- a). Trans. T issues a $w(x)$ operation:
if $TS(T) > R-TS(x)$ then delay T until the trans. T' that wrote or read x has terminated (committed or aborted).
- b). Trans. T issues a $r(x)$ operation:-
if $TS(T) > TS(x)$ then delay T until trans. T' that wrote or read x has terminated (committed or aborted).

* Thomas's Write Rule:

- If $TS(T_i) < TS(T_j)$ & T_i writes x before $TS(T_j)$ then we can just ignore that write, since it will be overwritten by T_j anyway.

Example a). $st_1 ; st_2 ; \downarrow w_1(A) ; r_2(A) ; w_1(B) ; w_2(B)$
start trans A start trans B old young

$$R-TS(x) = 2 // \text{trans 2} // \text{the one that occurs the latest is the highest.}$$

$$w_1-TS(x) = 1 // \text{whether } w_2(B) < w_1(B)$$

\therefore Accepted

no, it is not. \therefore It can write. $\therefore w(Ts)(x) = y_2$

b). $s+1; s+2; \gamma_2(A); c_02; w_1(A); w_1(A)$

for $\gamma_2(A)$

$$RFTS(A) = 2$$

co

for $w_1(A)$

$$R-TS(A) = 1/2$$

for $w_1(A)$

* write is requested but already there is a read that has happened *

same trans done read

∅ now wants to write

∴ check if $w_1 < \gamma_2$

∴ can't write
∴ write ∴ Accepted

Roll back

c). $s+1; s+2; s+3; \gamma_1(A); w_3(A); c_03; \gamma_2(B); w_2(A)$

According to Thomas's write rule, $w_2(A)$ needs to be ignored.

oldest youngest

d). $s+1; s+2; s+3; \gamma_1(A); w_1(A); \gamma_2(A);$

if $R_2(A) < R_1-TS(A)$: $R_1-TS(A) = 1$

// $\gamma_2(A)$ must wait (delay)
until $w_1(A)$ commits.

// strict. ∴ delayed

e). $s+1; s+2; s+3; \gamma_1(A); w_2(A); w_3(A); \gamma_2(A)$

∴ rolled back

∴ $\gamma_2(A) < w_3(A)$ // ∴ rolled back

check for :-

- Example 5 • Recoverable • conflict serializable
 • ACR
 • Strict
 • View Serializable

1). $T_1 : R(x) \quad T_2 : R(x) \quad T_1 : W(x) \quad T_2 : W(x)$
 \therefore Schedule: $R_1(x) \rightarrow R_2(x) \rightarrow W_1(x) \rightarrow W_2(x)$

Recoverable	ACR	Strict	conflict Ser.	View Serializ.
✓	✓	✗	✗	✗



NO such

for e.g. if conflicting operations

found.

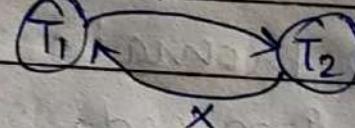
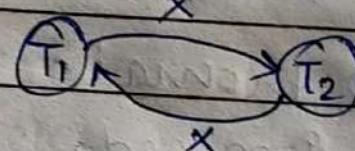
$T_1 < T_2$

$S_2 : R_1(x) \rightarrow W_1(x) \rightarrow R_2(x) \rightarrow W_2(x)$

Not View Serializable.

{ \because 3rd cond'n fails, thus not equivalent schedule, thus not view serializable}.

$T_2 < T_1 \quad X$



non-conflicting
serializable

Rec : WP -- CL -- DJ
 ACR : W1 -- C1 -- DJ



5). $T_1 : R(x) \quad T_2 : W(x) \quad T_1 : W(x) \quad a_2 \quad C_1$

$S : R_1(x) \quad W_2(x) \quad W_1(x) \quad a_2 \quad C_1$

recoverable	ACR	strict	cons.	view ser
✓	✓	✗	✗	
↓ no such conf. op.	↓			

\circlearrowleft

$(T_1) \quad (T_2)$

Let Serial Schedule be :

$T_1 < T_2 : - S_2 : R_1(x) \quad W_1(x) \quad C_1 \quad W_2(x) \quad a_2$
 2nd cond'n doesn't satisfy.

1). $R_1(x) \dots \rightarrow$ satisfied.

2). $\dots - W_1(x) \cdot \sigma(x) \dots \rightarrow$ not satisfied.

8). $S : W_1(x) \quad R_2(x) \quad W_2(x) \quad C_2 \quad C_1$

Rec.	ACR	strict	conflict	view ser
X	X	X	X Y	X

\circlearrowleft

$(T_1) \quad (T_2)$

view $T_1 < T_2$

• $W_1(x) \quad R_2(x) \quad C_1 \quad R_2(x) \quad C_2 \quad C_2$
 Not view serializable.

Example 6 • 2PL

- Strict 2PL
- Consecutive 2PL
- Timestamp Ordering
- Thomas Timestamp Ordering

1). $\delta_1(x) \quad \tau_2(x) \quad w_1(x) \quad w_2(x) \quad c_1 \quad c_2$
↳ optional.

2PL: $w_1(x) \quad \delta_1(x) \dots \delta_2(x)$ but no lock available

OR

T_1	T_2
$w_1(x)$	
$\delta_1(x)$	

→ waiting for lock

Strict 2PL: Commit before unlocking.

Consecutive 2PL: All locks must be acquired before transaction starts.

$w_1(x) \quad w_2(x)$ — cannot do

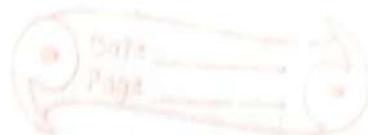
$\delta_1(x) \quad \tau_2(x) \quad w_1(x) \quad c_1 \quad w_2(x)$

↑
Should be there for

also

Strict 2PL

$\tau_1(x) \quad \tau_2(x) \quad \underbrace{c_2}_{\downarrow} \quad w_1(x)$



Timestamp Orderly

$\sigma_1(x)$ $R-TS(x) = 1$

$\sigma_2(x)$ $R-TS(x) = 2$

$\sigma_1(x) \quad \sigma_2(x) \quad w_1(x)$ If $TS(TP) < R-TS(x)$ ||...
 \therefore Rollback // true

Waiting after younger? $\rightarrow 2$ is youngest

\therefore Schedule is not

acceptable, \because schedule is rolling back.

Transaction has read.

Thomas with Thomas Write Rule:

If there are two writes :-

$w_1 \dots w_j$ where $w_i < w_j$

\therefore Ignore w_i .

$w_1(x) \quad w_2(x)$ // we can check here

But we cannot go till this op.

only because it fails at $w_1(x)$ only
 $\{$ same as above $\}$.

2PL	Strict 2PL	C-2PL	TO	Thomas
x	x	x	x	x

5). $R_1(x) \quad w_2(x) \quad w_1(x) \quad a_2 \quad c_1$

* TO

$\pi_1(x) \quad R_TS(x) = 1$

$w_2(x) \quad \text{if } TS(TP) < R_TS(x) \quad || \quad TS(TP) < W_TS(x)$
 $\hookrightarrow W_TS(x) = \frac{2}{2} < 1$

$w_1(x) \quad TS \quad 1 < 1 \quad || \quad 1 < 2$

abort. (Rollback)

Sch. is not acceptable. TO X

* Thomas

~~we can't do it~~ ignore it
 $\text{if } TS(TP) < W_TS(x) \quad \text{check } w_2(x) \quad w_1(x)$
 $1 < 2 \quad \text{Thus, schedule is acceptable}$

DPL: $w_1(x) \quad \pi_1(x) \quad w_2 - \pi_2(x) \quad \text{no lock avl.}$

Strict: 2 PL same as above.

cons. 2 PL

2PL	strict	cons.	TO	TO with Thomas
X	X	X	X	✓

Exercise 4 :-



b). conflict ser : put precedence graph
First construct an interleaved
schedule as :-

Schedule will be :

$w_1(C) \quad r_1(A) \quad w_1(A) \quad r_1(B) \quad w_1(B) \quad c_1$
 $r_2(B) \quad w_2(B) \quad r_2(A) \quad w_2(A) \quad c_2$

$\because r_2(B)$ } For $r_2(B)$ we need $L_2(B)$
but T_1 has already taken $L_2(B)$, thus
we need to wait until T_1 releases the
lock }. And unlock is happening at
end. Thus, ~~for~~ T_1 to unlock will
complete first then T_2 .

\therefore conflict serializable.

\because All the schedules will point
from T_1 to T_2 . $T_1 \rightarrow T_2$.

$$T_1 < T_2$$

\because It is conflict ser. & serial schedule

\therefore It will be recoverable, ACR }
Strict, serial } $\rightarrow Y$

No deadlock } $\because T_1$ finishes then
T2 starts.

CONCURRENCY CONTROLTECHNIQUES

Nidhi Singh

Chapter - 06

R018156

1). Conservative 2PL helps in preventing deadlock.

- Conservative 2PL is a protocol of locking all the items in a transaction schedule before starting the execution of the transaction, by pre-declaring its read & write set.
- In case, that ~~it is no~~ the data needed for locking is not available, then it will not lock any item and the transaction will not even begin.
- In such case, the transaction will wait till the data needed is available.
- Thus, it is a deadlock-free protocol & hence prevents deadlock.

3). Strict 2PL :

- Strict 2PL protocol explains that a transaction T' does not release any of its write-locks before it commits or aborts.
- It means that no other transaction can read or write an item that is written by T unless committed.

- Thus, leads to strict schedule for recoverability.
- However, Strict DPL is not a deadlock-free protocol.
- It blocks writer blocks all the readers until the writer commits or aborts.

* Prove that basic DPL guarantees conflict serializability of schedules:-

It can be proved that if every transaction in a schedule follows the DPL protocol, then the schedule is guaranteed to be serializable (conflict serializable).

E.g. Let us consider transaction $T_1 \oplus T_2$:-

$$T_1 : r_1(Y); r_1(X); X := X + Y; w_1(X)$$

$$T_2 : r_2(X); r_2(Y); Y := X + Y; w_2(X)$$

Now, by applying DPL protocol, transactions become:

T_1'	T_2'
read-lock(Y)	read-lock(X)
read(Y)	read(X)
write-lock(X)	write-lock(Y)
unlock(Y)	unlock(X)
read(X)	read(Y);
$X = X + Y$	$Y := Y + X$
write(X)	write(Y)
unlock(X)	unlock(Y)

Here, the transaction satisfies DPL.

Now, if we check for conflict serializability of schedule S , $T_1' < T_2'$ with let $X = 20$ {Initially} $Y = 30$ {}

$$\therefore S : T_1' < T_2' : X = 50 \quad Y = 80$$

$$\not\in S : T_1' < T_2' : X = 50 \quad Y = 80$$

\therefore DPL guarantees conflict serializability.

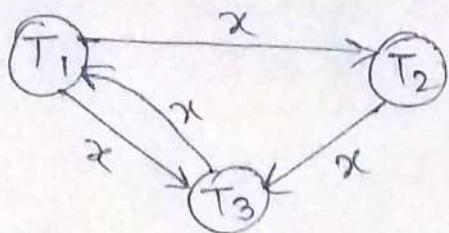
Q. Basic timestamp ordering algorithm to control concurrency :-

- It does not use any locks for the serializability of schedules. Hence, no deadlocks.
- If T_i requests $\text{read}(x)$:-
If $TS(T_p) < W_TS(x)$
 $\begin{cases} \text{rollback;} \\ \text{else} \end{cases}$
 $\begin{cases} \text{read}(x); \\ R_TS(x) = \max\{R_TS(x), TS(T_p)\}; \end{cases}$
- If T_i requests $\text{write}(x)$:-
If $TS(T_p) < R_TS(x) \quad || \quad TS(T_p) < W_TS(x)$
 $\begin{cases} \text{rollback;} \\ \text{else} \end{cases}$
 $\begin{cases} \text{write}(x); \\ W_TS(x) = TS(T_p); \end{cases}$
- Notations :- $TS(T_p)$: Timestamp value of Transaction T_p .

$R_TS(X)$: largest timestamp of any transaction that executed $R(x)$ successfully.

$W_TS(X)$: largest timestamp of any transaction that executed $W(x)$ successfully.

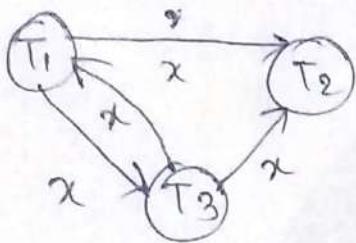
- 6). (a) $\tau_1(x)$; $\tau_3(x)$; $w_1(x)$; $\tau_2(x)$; $w_3(x)$;



∴ The precedence graph is cyclic in nature.

∴ Non-conflict serializable.

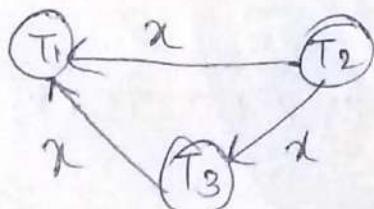
- (b) $\tau_1(x)$; $\tau_3(x)$; $w_3(x)$; $w_1(x)$; $\tau_2(x)$;



∴ Cyclic in nature.

∴ Non-conflict serializable.

- (c) $\tau_3(x)$; $\tau_2(x)$; $w_3(x)$; $\tau_1(x)$; $w_1(x)$;



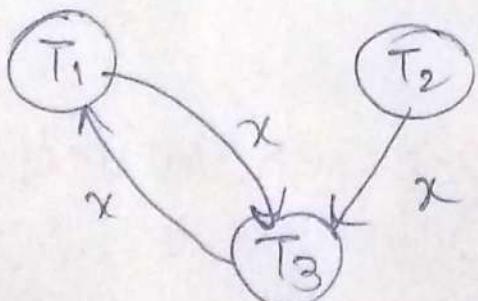
∴ Acyclic

∴ Conflict serializable.

Equivalent Serial Schedule

can be: $T_2 \rightarrow T_3 \rightarrow T_1$

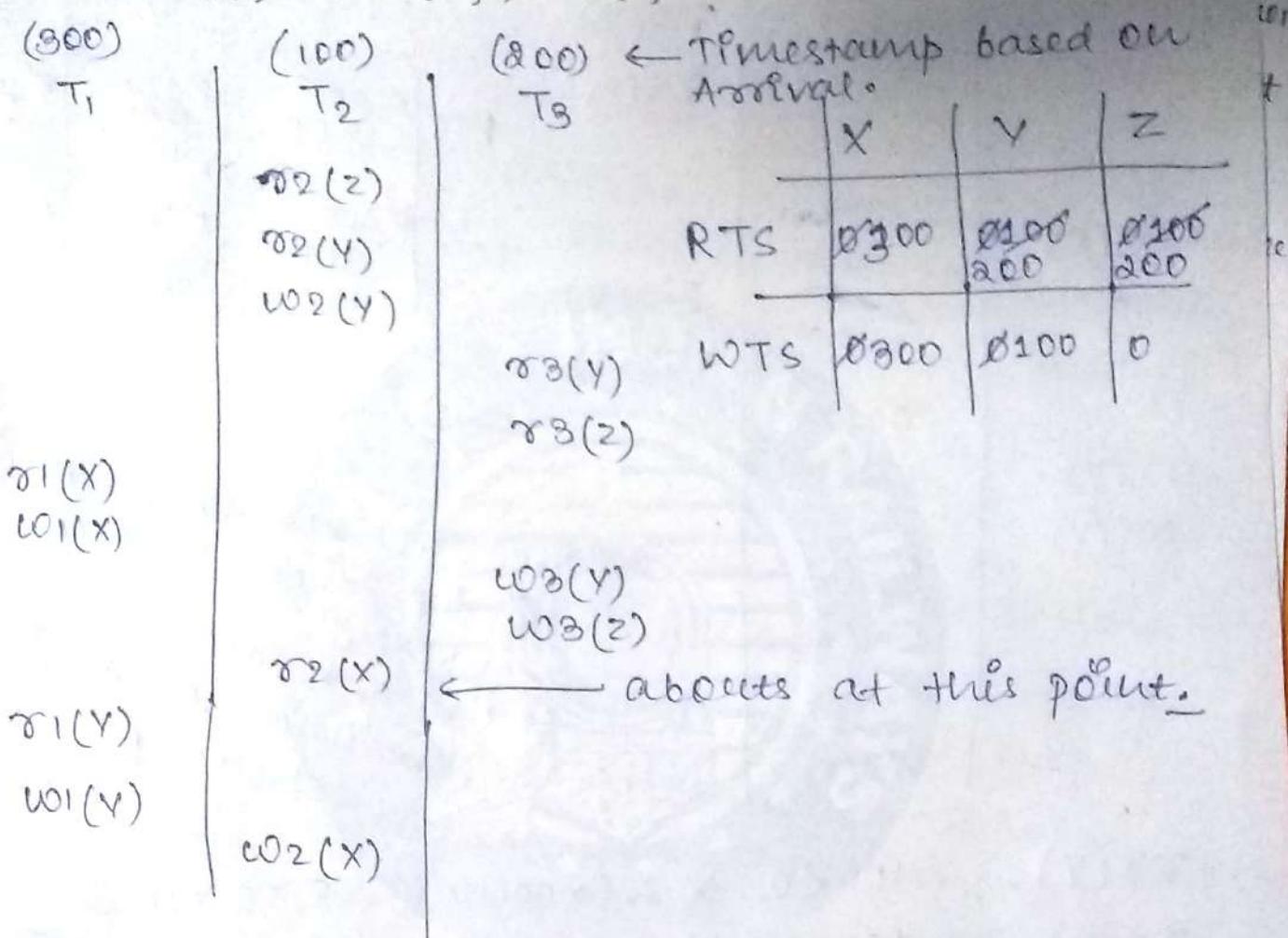
- (d) $\tau_3(x)$; $\tau_2(x)$; $\tau_1(x)$; $w_3(x)$; $w_1(x)$;



∴ Cyclic.

∴ Non-conflict serializa

- 4). $S_1 : \gamma_2(z); \gamma_2(y); w_2(y); \gamma_3(y); \gamma_3(z);$
 $\gamma_1(x); w_1(x); w_3(y); w_3(z); \gamma_2(x);$
 $\gamma_1(y); w_1(y); w_2(x)$



- $\gamma_2(z)$ Executed, $\therefore R-TS(z) = 100$
- $\gamma_2(y)$ Executed, $\therefore R-TS(y) = 100$
- $w_2(y)$ Executed, $\therefore W-TS(y) = 100$
- $\gamma_3(y)$ Executed, $\therefore R-TS(y) = \max(100, 200) = 200$
- $\gamma_3(z)$ Executed, $\therefore R-TS(z) = \max(100, 200) = 200$
- $\gamma_1(x)$ Executed, $\therefore R-TS(x) = 300$
- $w_1(x)$ Executed, $\therefore W-TS(x) = 300$
- $\gamma_2(x)$ $\because 100 < W-TS(x) \therefore$ Rollback (abort).
 $= 300$

Hence, ~~transaction~~ schedule aborts.

$S_2 : \tau_3(Y); \tau_3(Z); \tau_1(X); w_1(X); w_3(Y);$
 $w_3(Z); \tau_2(Z); \tau_1(Y); w_1(Y); \tau_2(Y);$
 $w_2(Y); \tau_2(X); w_2(X).$

<u>(200)</u>	<u>(300)</u>	<u>(100)</u> ← Timestamp Values	
<u>T₁</u>	<u>T₂</u>	<u>T₃</u>	
$\tau_1(X)$		$\tau_3(Y)$	X Y Z
$w_1(X)$		$\tau_3(Z)$	PTS 0 200 300 100 200 300
		$w_3(Y)$	WOTS 0 200 300 100 200 300
		$w_3(Z)$	
	$\tau_2(Z)$		
$\tau_1(Y)$			
$w_1(Y)$			
	$\tau_2(Y)$		
	$w_2(Y)$		
	$\tau_2(X)$		
	$w_2(X)$		

Since, all the transactions
 of the schedule are
 completed. ∴ The algo. allows
 execution of this
 schedule.

$\tau_3(Y)$	$100 < 0$	$X \therefore$ Executed, $\therefore R_TS(Y) = 100$
$\tau_3(Z)$	$100 < 0$	$X \therefore$ Executed, $\therefore R_TS(Z) = 100$
$\tau_1(X)$	$200 < 0$	$X \therefore$ Executed, $\therefore R_TS(X) = 200$
$w_1(X)$	$200 < 200 \ \ 200 < 0$	$X \therefore$ Executed, $\therefore W_TS(X) = 200$
$w_3(Y)$	$100 < 100 \ \ 100 < 0$	$X \therefore$ Executed, $\therefore W_TS(Y) = 100$
$w_3(Z)$	$100 < 100 \ \ 100 < 0$	$X \therefore$ Executed, $\therefore W_TS(Z) = 100$
$\tau_2(Z)$	$300 < 100$	$X \therefore$ Executed, $\therefore R_TS(Z) = \max(100, 300) = 300$
$\tau_1(Y)$	$200 < 100$	$X \therefore$ Executed, $\therefore R_TS(Y) = 200$
$w_1(Y)$	$200 < 200 \ \ 200 < 100$	$X \therefore$ Executed, $\therefore W_TS(Y) = 200$
$\tau_2(Y)$	$300 < 200$	$X \therefore$ Executed, $\therefore R_TS(Y) = 300$
$w_2(Y)$	$300 < 200 \ \ 300 < 200$	$X \therefore$ Executed, $\therefore W_TS(Y) = 300$
$\tau_2(X)$	$800 < 200$	$X \therefore$ Executed, $\therefore R_TS(X) = 300$
$w_2(X)$	$800 < 200 \ \ 300 < 300$	$X \therefore$ Executed, $\therefore W_TS(X) = 300$