



- FULL ADDER
- main
- Wiring
- Gates
  - NOT Gate
  - Buffer
  - AND Gate
  - OR Gate
  - NAND Gate
  - NOR Gate
  - XOR Gate
  - XNOR Gate
  - Odd Parity
  - Even Parity
  - Controlled Buffer
  - Controlled Inverter
- Processors
  - Arithmetic
  - Memory
  - Input/Output
  - Base

| Pin            |                    |
|----------------|--------------------|
| Facing         | East               |
| Output?        | No                 |
| Data Bits      | 1                  |
| Three state?   | No                 |
| Pull Behavior  | Unchanged          |
| Label          | Cin                |
| Label Location | West               |
| Label Font     | SansSerif Plain 12 |

