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Instruction	OPC	Func3	Func7	RegWriteD	MemwriteD	ALUSrcD	ResultSrcD	ALUControlD	ImmSrcD	BrenchD	JumpD
Add	0110011	000	0000000	1	0	0	00	000	000	000	00
Sub	0110011	000	0100000	1	0	0	00	001	000	000	00
And	0110011	111	0000000	1	0	0	00	010	000	000	00
Or	0110011	110	0000000	1	0	0	00	011	000	000	00
Slt	0110011	010	0000000	1	0	0	00	101	000	000	00
Addi	0010011	000	xxxxxxxx	1	0	1	00	000	000	000	00
Xori	0010011	100	xxxxxxxx	1	0	1	00	111	000	000	00
Slti	0010011	010	xxxxxxxx	1	0	1	00	101	000	000	00
Ori	0010011	110	xxxxxxxx	1	0	1	00	001	000	000	00
Sw	0100011	010	xxxxxxxx	0	1	1	10	000	001	000	00
beq	1100011	000	xxxxxxxx	0	0	0	00	001	010	001	00
bneq	1100011	001	xxxxxxxx	0	0	0	00	001	010	010	00
bge	1100011	100	xxxxxxxx	0	0	0	00	001	010	011	00
blt	1100011	101	xxxxxxxx	0	0	0	00	001	010	100	00
lui	0110111	xx	xxxxxxxx	1	0	1	11	100	011	000	00
jal	1101111	xx	xxxxxxxx	1	0	1	11	000	100	000	01
lw	0000011	xx	xxxxxxxx	1	0	1	01	000	000	000	00
jalr	1100111	xx	xxxxxxxx	1	0	1	11	000	000	000	10