Workshop on AMReX: Introduction and Practical Applications

Motivation:

AMReX is a software framework, massively parallel, for block-structured adaptive mesh refinement (AMR) applications, with other features like particle-based simulations, embedded boundaries, etc. targeted for heterogeneous architectures developed under the Exascale computing project for application development. Its abstraction layer ensures immunity to different programming models for heterogeneous architectures on next generation systems. The C-DAC team contributed to the development of AMReX-based phase-field solvers in the MicroSim suite under NSM (DST). MicroSim was developed by the Indian phase-field community. Also, C-DAC is an associate member of High Performance Software Foundation (HPSF) which promotes AMReX as one of the projects for wide adoption, community building, and enabling development efforts. The workshop is an effort for proliferation of AMReX for applications targeting heterogeneous architectures among the scientific community in India.

Speakers:

- Dr. Pradeep Kumar Seshadri, IIT Madras
- Dr. Pratik Das, IIT Delhi
- Dr. Ashish Bhole, IIST Trivandrum
- Prof. Shyamprasad Karagadde, IIT Bombay
- Prof. Snigdha Thakur, IISER Bhopal (TBC)
- Dr. Anando G. Chatterjee, IIT Kanpur (TBC)

Workshop Details:

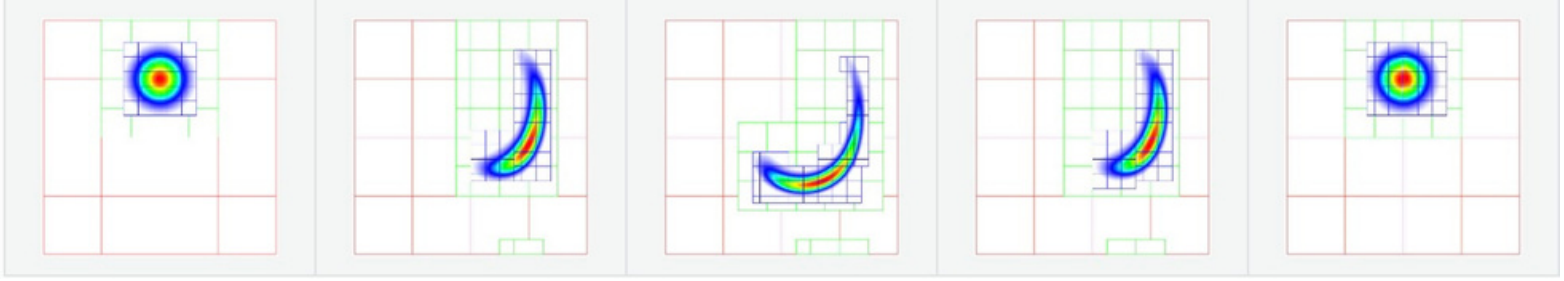
Mode: Offline
Venue: C-DAC Pune
Workshop Dates: 23rd - 24th February 2026
Participant list to be announced by 2nd February 2026

Note: *Please click here to access the workshop announcement page, which includes details on accommodation, speakers, important dates, and the objectives of the workshop. All updates and revised plans will be posted on this page.*

To register scan QR or click here

Last Date for registration: 29th January 2026

Announcement page link: https://sourceforge.net/p/mscc-applications-nsm/discussion/training/thread/39cff744a9/#4cf4



Credits: AMReX Documentation

Have queries? please contact: Aniket Kumar (aniketkumar@cdac.in) / Nasir Attar (nattar@cdac.in)