RR Q = 2 con cola auxiliar para las llegadas post I/O

Job	Llegada	CPU	I/O	0	1	2	3	4	5	6	7	8	9	10	11	12	13
P1	0	4	(R1, 2, 1)	>1	2	R1		3	4<								
P2	2	6	(R2, 3, 1) (R2, 5, 2)			>1	2					3	R2	4	5	R2	R2 (
P3	3	4					^			1	2						
P4	6	5	(R3, 1, 2) (R3, 3, 1)							٧			1	R3	R3	2	3
P5	8	2										>					
RR Q = 2			Queue	1	2	1, 3	2, 4	5	2	4							
			Queue Aux	1	2	4	2	4									

2	13	14	15	16	17	18	19	20	21	22	23	24	25	26	TR	TE
															6	2
	R2	6<													13	7
					3	4<									16	12
2	3	R3	4	5<											11	6
							1	2<							13	11
															11,8	7,6