

CSE 31

Computer Organization

**Lecture 10 – MIPS: Inequalities &
Procedures (1)**



Announcement

- ▶ Lab #4 this week
 - Due next week
- ▶ HW #3 out this Friday (from zyBooks)
 - Due Monday (10/8)
- ▶ Project #1
 - Due Monday (10/22)
 - Don't start late, you won't have time!
- ▶ Reading assignment
 - Chapter 3.1 – 3.7, 3.9 of zyBooks (Reading Assignment #3)
 - Make sure to do the Participation Activities
 - Due Wednesday (10/3)

Announcement

- ▶ Midterm exam Wednesday (10/3, postponed)
 - Lectures 1 – 7
 - HW #1 and #2
 - Closed book
 - 1 sheet of note (8.5" x 11")
 - Sample exam online
 - Review on Friday (9/28) at **1 - 3pm**, SSB 130

Inequalities in MIPS (1/4)

- ▶ Until now, we've only tested equalities (`==` and `!=` in C). General programs need to test `<` and `>` as well.
- ▶ Introduce MIPS Inequality Instruction:
 - “Set on Less Than”
 - Syntax: `slt reg1, reg2, reg3`
 - Meaning: `reg1 = (reg2 < reg3);`

```
if (reg2 < reg3)
    reg1 = 1;
else reg1 = 0;
```

Same thing...

“set” means “change to 1”,
“reset” means “change to 0”.

Inequalities in MIPS (2/4)

- ▶ How do we use this? Compile by hand:

```
if (g < h) goto Less; #g:$s0, h:$s1
```

- ▶ Answer: compiled MIPS code...

```
slt $t5,$s0,$s1 # $t0 = 1 if g<h
```

```
bne $t5,$0,Less # goto Less
```

```
# if $t0!=0
```

Why not:

```
# (if (g<h)) Less:
```

beq \$t5, 1, Less?

- ▶ Register \$0 always contains the value 0, so **bne** and **beq** often use it for comparison after an **slt** instruction.
- ▶ A **slt** → **bne** pair means **if (... < ...) goto...**

Inequalities in MIPS (3/4)

- ▶ Now we can implement $<$,
but how do we implement $>$, \leq and \geq ?
- ▶ We could add 3 more instructions, but:
 - MIPS goal: **Simpler is Better**
- ▶ Can we implement \leq in one or more instructions
using just **slt** and **branches**?
 - What about $>$?
 - What about \geq ?

Inequalities in MIPS (4/4)

```
# a:$s0, b:$s1
slt $t0,$s0,$s1 # $t0 = 1 if a<b
beq $t0,$0,skip # skip if a >= b
    <stuff>      # do if a<b
skip:
```

How about **>** and **<=**?

Two independent variations possible:

Use `slt $t0,$s1,$s0` instead of

`slt $t0,$s0,$s1`

Use `bne` instead of `beq`

Immediates in Inequalities

- ▶ There is also an immediate version of **slt** to test against constants: **slti**
 - Helpful in **for** loops

C **if** (g >= 1) goto Loop

M **Loop:** . . .
| **slti** \$t0,\$s0,1 # \$t0 = 1 if
| # \$s0<1 (g<1)
P **beq** \$t0,\$0,**Loop** # goto Loop
S # if \$t0==0
 # (if (g>=1))

An **slt** → **beq** pair means **if (... ≥ ...) goto...**

What about unsigned numbers?

- ▶ Also **unsigned** inequality instructions:

sltu, sltiu

...which sets result to 1 or 0 depending on unsigned comparisons

- ▶ What is value of \$t0, \$t1?

- ▶ (\$s0 = FFFF FFFA_{hex}, \$s1 = 0000 FFFA_{hex})

slt \$t0, \$s0, \$s1 **1**

sltu \$t1, \$s0, \$s1 **0**

MIPS Signed vs. Unsigned – diff meanings!

- ▶ MIPS terms Signed/Unsigned “overloaded”:
 - Do/Don't sign extend
 - `(lb, lbu)`
 - Do/Don't overflow
 - `(add, addi, sub, mult, div)`
 - `(addu, addiu, subu, multu, divu)`
 - Do signed/unsigned compare
 - `(slt, slti/sltu, sltiu)`

Example: The C Switch Statement (1/3)

- ▶ Choose among four alternatives depending on whether `k` has the value 0, 1, 2 or 3. Compile this C code:

```
switch (k) {  
    case 0: f=i+j; break; /* k=0 */  
    case 1: f=g+h; break; /* k=1 */  
    case 2: f=g-h; break; /* k=2 */  
    case 3: f=i-j; break; /* k=3 */  
}
```

Example: The C Switch Statement (2/3)

- ▶ This is complicated, so **simplify**.
- ▶ Rewrite it as a chain of if-else statements, which we already know how to compile:

```
if(k==0) f=i+j;  
    else if(k==1) f=g+h;  
        else if(k==2) f=g-h;  
            else if(k==3) f=i-j;
```

- ▶ Use this mapping:
 f:\$s0, g:\$s1, h:\$s2,
 i:\$s3, j:\$s4, k:\$s5

Example: The C Switch Statement (3/3)

► Final compiled MIPS code:

```
    bne $s5, $0, L1      # branch k!=0
    add $s0, $s3, $s4    # k==0 so f=i+j
    j   Exit             # end of case so Exit
L1:  addi $t0, $s5, -1    # $t0=k-1
    bne $t0, $0, L2      # branch k!=1
    add $s0, $s1, $s2    # k==1 so f=g+h
    j   Exit             # end of case so Exit
L2:  addi $t0, $s5, -2    # $t0=k-2
    bne $t0, $0, L3      # branch k!=2
    sub $s0, $s1, $s2    # k==2 so f=g-h
    j   Exit             # end of case so Exit
L3:  addi $t0, $s5, -3    # $t0=k-3
    bne $t0, $0, Exit    # branch k!=3
    sub $s0, $s3, $s4    # k==3 so f=i-j
Exit:
```

Always compared with \$0!

Quiz

```
Loop: addi $s0, $s0, -1    # i = i - 1
      slti $t0, $s1, 2    # $t0 = (j < 2)
      beq  $t0, $0, Loop  # goto Loop if $t0 == 0
      slt  $t0, $s1, $s0  # $t0 = (j < i)
      bne  $t0, $0, Loop  # goto Loop if $t0 != 0
```

($\$s0=i$, $\$s1=j$)

What C code properly fills in the blank in loop below?

do {i--;} while(___);

- | | | | |
|-----|------------|--------|------------|
| 1) | $j < 2$ | $\&\&$ | $j < i$ |
| 2) | $j \geq 2$ | $\&\&$ | $j < i$ |
| 3) | $j < 2$ | $\&\&$ | $j \geq i$ |
| 4) | $j \geq 2$ | $\&\&$ | $j \geq i$ |
| 5) | $j > 2$ | $\&\&$ | $j < i$ |
| 6) | $j < 2$ | $ $ | $j < i$ |
| 7) | $j \geq 2$ | $ $ | $j < i$ |
| 8) | $j < 2$ | $ $ | $j \geq i$ |
| 9) | $j \geq 2$ | $ $ | $j \geq i$ |
| 10) | $j > 2$ | $ $ | $j < i$ |

Quiz

```
Loop: addi $s0, $s0, -1    # i = i - 1
      slti $t0, $s1, 2    # $t0 = (j < 2)
      beq  $t0, $0, Loop  # goto Loop if $t0 == 0
      slt  $t0, $s1, $s0  # $t0 = (j < i)
      bne  $t0, $0, Loop  # goto Loop if $t0 != 0
```

($\$s0=i$, $\$s1=j$)

What C code properly fills in the blank in loop below?

do {i--;} while(___);

- | | | | |
|-----|------------|--------|------------|
| 1) | $j < 2$ | $\&\&$ | $j < i$ |
| 2) | $j \geq 2$ | $\&\&$ | $j < i$ |
| 3) | $j < 2$ | $\&\&$ | $j \geq i$ |
| 4) | $j \geq 2$ | $\&\&$ | $j \geq i$ |
| 5) | $j > 2$ | $\&\&$ | $j < i$ |
| 6) | $j < 2$ | $ $ | $j < i$ |
| 7) | $j \geq 2$ | $ $ | $j < i$ |
| 8) | $j < 2$ | $ $ | $j \geq i$ |
| 9) | $j \geq 2$ | $ $ | $j \geq i$ |
| 10) | $j > 2$ | $ $ | $j < i$ |

Summary

- ▶ To help the **conditional branches** make decisions concerning inequalities, we introduce: “Set on Less Than” called

slt, slti, sltu, sltiu

- ▶ One can store and load (signed and unsigned) **bytes** as well as words with **lb, lbu**
- ▶ Unsigned add/sub **don't cause overflow**
- ▶ New MIPS Instructions:

sll, srl, lb, lbu

slt, slti, sltu, sltiu

addu, addiu, subu

C functions

```
main() {  
    int i,j,k,m;  
    ...  
    i = mult(j,k); ...  
    m = mult(i,i); ...  
}
```

What information must
compiler/programmer
keep track of?

Arguments, local variables,
return value, return address

```
/* really dumb mult function */  
int mult (int mcand, int mlier){  
    int product = 0;  
    while (mlier > 0) {  
        product = product + mcand;  
        mlier = mlier -1; }  
    return product;  
}
```

What instructions can
accomplish this?

Function Call Bookkeeping

- ▶ Registers play a major role in keeping track of information for function calls.
- ▶ Register conventions:
 - Return address `$ra`
 - Arguments `$a0, $a1, $a2, $a3`
 - Return value `$v0, $v1`
 - Local variables `$s0, $s1, ... , $s7`
- ▶ The stack is also used; more later.

Instruction Support for Functions (1/6)

```
... sum(a,b); ...    /* a,b:$s0,$s1 */  
}
```

C

```
int sum(int x, int y) {  
    return x+y;  
}
```

address (shown in hexadecimal)

1000

1004

1008

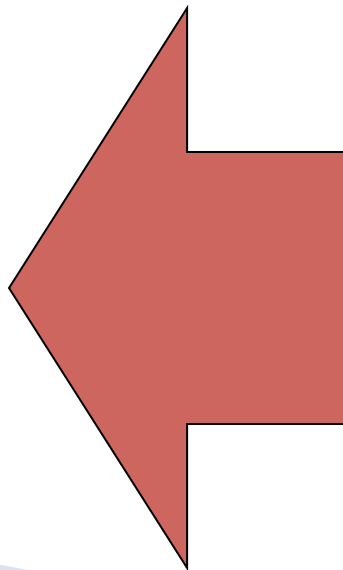
100c

1010

...

2000

2004



In MIPS, all instructions are 4 bytes, and stored in memory just like data. So here we show the addresses of where the programs are stored.

Instruction Support for Functions (2/6)

```
... sum(a,b) ;...    /* a,b:$s0,$s1 */  
}
```

C

```
int sum(int x, int y) {  
    return x+y;  
}
```

address (shown in hexadecimal)

M
I
P
S

1000	add	\$a0,\$s0,\$zero	# x = a
1004	add	\$a1,\$s1,\$zero	# y = b
1008	addi	\$ra,\$zero,1010	#\$ra=1010
100c	j	sum	#jump to sum
1010			
...			
2000	sum:	add \$v0,\$a0,\$a1	
2004	jr	\$ra	# new instruction

Instruction Support for Functions (3/6)


```
... sum(a,b) ;...    /* a,b:$s0,$s1 */  
}
```

C

```
int sum(int x, int y) {  
    return x+y;  
}
```

- Question: Why use `jr` here? Why not use `j`?
- Answer: `sum` might be called by many places, so we can't return to a fixed place. The calling proc to `sum` must be able to say "return here" somehow.

M
I
P
S



```
2000 sum: add $v0,$a0,$a1  
2004 jr      $ra          # new instruction
```

Instruction Support for Functions (4/6)

- ▶ Single instruction to jump and save return address: jump and link (**j**a**l**)

- ▶ Before:

```
1008 addi $ra,$zero,1010 #$ra=1010  
100c j  sum #goto sum
```

- ▶ After:

```
100c jal sum # $ra=1010,goto sum
```

- ▶ Why have a **j**a**l**?

- Make the common case fast: function calls very common.
- Don't have to know where code is in memory with **j**a**l**!

Instruction Support for Functions (5/6)

- ▶ Syntax for **jal** (jump and link) is same as for **j** (jump):

jal label

- ▶ **jal** should really be called **laj** for “link and jump”:
 - Step 1 (link): Save address of next instruction into `$ra`
 - Why next instruction? Why not current one?
 - Step 2 (jump): Jump to the given label

Instruction Support for Functions

- ▶ Syntax for `jr` (jump register):
`jr register`
- ▶ Instead of providing a label to jump to, the `jr` instruction provides a register which contains an address to jump to.
- ▶ Very useful for function calls:
 - `jal` stores return address in register (`$ra`)
 - `jr $ra` jumps back to that address

Nested Procedures (1/2)

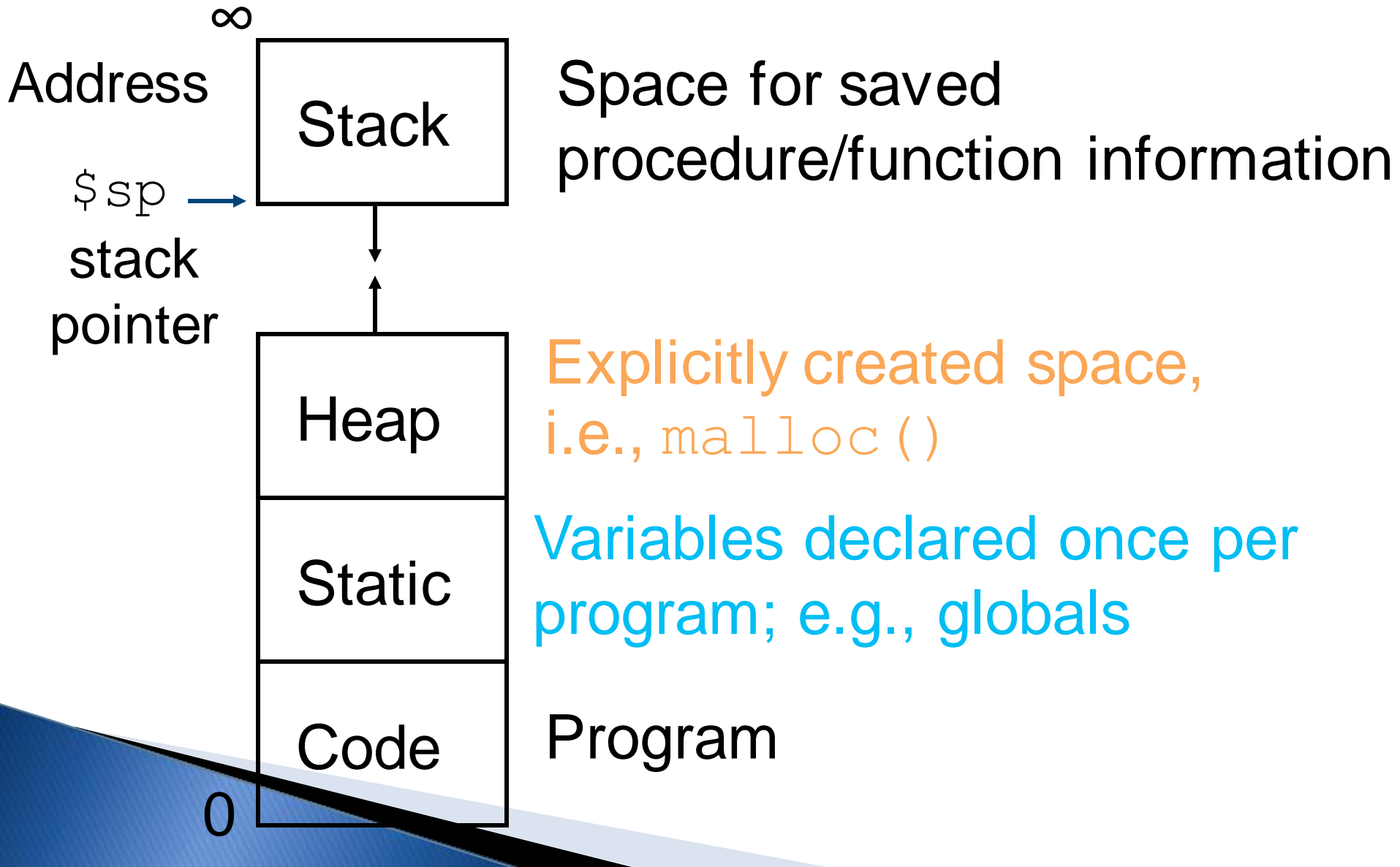
```
int sumSquare(int x, int y) {  
    return mult(x,x)+ y;  
}
```

- ▶ Something called **sumSquare**, now **sumSquare** is calling **mult**.
- ▶ So there's a value in `$ra` that **sumSquare** wants to jump back to, but this will be overwritten by the call to **mult**.
- ▶ Need to save **sumSquare** return address before call to **mult**.
 - How to prevent the return address from being over-written?

Nested Procedures (2/2)

- ▶ In general, may need to save some other info in addition to `$ra`.
- ▶ When a C program is run, there are 3 important memory areas allocated:
 - **Static**: Variables declared once per program, cease to exist only after execution completes. E.g., C globals
 - **Heap**: Variables declared dynamically via `malloc`
 - **Stack**: Space to be used by procedure during execution; this is where we can save register values

C memory Allocation review



Using the Stack (1/2)

- ▶ We have a register **\$sp** which always points to the last used space in the stack (top of stack).
- ▶ To use stack, we decrement this pointer by the amount of space we need and then fill it with info.
- ▶ So, how do we compile this?

```
int sumSquare(int x, int y) {  
    return mult(x,x)+ y;  
}
```

Using the Stack (2/2)

► Hand-compile

```
int sumSquare(int x, int y) {  
    return mult(x,x)+ y; }
```

sumSquare:

```
“push”      addi $sp,$sp,-8      # space on stack  
            sw $ra, 4($sp)       # save ret addr  
            sw $a1, 0($sp)       # save y  
            add $a1,$a0,$zero     # mult(x,x)  
            jal mult             # call mult  
            lw $a1, 0($sp)       # restore y  
            add $v0,$v0,$a1      # mult()+y  
“pop”       lw $ra, 4($sp)       # get ret addr  
            addi $sp,$sp,8       # restore stack  
            jr $ra  
mult:      ...
```

Steps for Making a Procedure Call

1. Save necessary values onto stack.
2. Assign argument(s), if any.
3. **jal** call
4. Restore values from stack.

Basic Structure of a Function

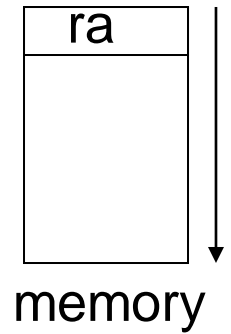
Prologue

```
entry_label:  
addi $sp,$sp, -framesize  
sw $ra, framesize-4($sp)  # save $ra  
save other regs if need be
```

Body ... (call other functions...)

Epilogue

```
restore other regs if need be  
lw $ra, framesize-4($sp)  # restore $ra  
addi $sp,$sp, framesize  
jr $ra
```



Rules for Procedures

- ▶ Called with a **jal** instruction
 - returns with a **jr \$ra**
- ▶ Accepts up to 4 arguments in **\$a0**, **\$a1**, **\$a2** and **\$a3**
- ▶ Return value is always in **\$v0** (and if necessary in **\$v1**)
- ▶ Must follow **register conventions**
 - What are they?

MIPS Registers

The constant 0

Reserved for Assembler

Return Values

Arguments

Temporary

Saved

More Temporary

Used by Kernel

Global Pointer

Stack Pointer

Frame Pointer

Return Address

\$0

\$1

\$2-\$3

\$4-\$7

\$8-\$15

\$16-\$23

\$24-\$25

\$26-27

\$28

\$29

\$30

\$31

\$zero

\$at

\$v0-\$v1

\$a0-\$a3

\$t0-\$t7

\$s0-\$s7

\$t8-\$t9

\$k0-\$k1

\$gp


\$sp

\$fp

\$ra

Use names for registers -- code is clearer!

Other Registers

- ▶ **\$at**: may be used by the assembler at any time; unsafe to use
 - ▶ **\$k0–\$k1**: may be used by the OS at any time; unsafe to use
 - ▶ **\$gp**, **\$fp**: don't worry about them
- 

Register Conventions (1/4)

- ▶ CalleR: the calling function (where you call a function)
- ▶ CalleE: the function being called
- ▶ When callee returns from executing, the caller needs to know which registers may have changed and which are guaranteed to be unchanged.
- ▶ **Register Conventions**: A set of generally accepted rules as to which registers will be unchanged after a procedure call (**jal**) and which may be changed.

Register Conventions (2/4) – saved

- ▶ **\$0: No Change.** Always 0.
- ▶ **\$s0-\$s7: Restore if you change.** Very important, that's why they're called saved registers. If the callee changes these in any way, it must restore the original values **before returning**. i.e. callee's job to restore.
- ▶ **\$sp: Restore if you change.** The stack pointer must point to the same place before and after the `jal` call, or else the caller won't be able to restore values from the stack.
- ▶ HINT -- All saved registers start with **S**!

Register Conventions (3/4) – volatile

- ▶ **\$ra: Can Change.**
 - The jal call itself will change this register. Caller needs to save on stack before next call (nested call).
- ▶ **\$v0-\$v1: Can Change.**
 - These will contain the new returned values.
- ▶ **\$a0-\$a3: Can change.**
 - These are volatile argument registers. Caller needs to save if they are needed after the call.
- ▶ **\$t0-\$t9: Can change.**
 - That's why they're called temporary: any procedure may change them at any time. Caller needs to save if they'll need them afterwards.

Register Conventions (4/4)

- ▶ What do these conventions mean?
 - If function **R** calls function **E**, then function **R** must save any temporary registers that it may be using onto the stack **before making** a **jal** call.
 - Function **E** must save any **S** (saved) registers it intends to use before garbling up their values. It must restore any modified **S** registers **before returning** back to **R**
- ▶ Remember: **caller/callee** need to save only **temporary/saved** registers **they are using**, not all registers.

Summary

- ▶ Functions called with **jal**, return with **jr \$ra**.
- ▶ The stack is your friend: Use it to save anything you need. Just leave it the way you found it!
- ▶ Instructions we know so far...
 - Arithmetic: **add, addi, sub, addu, addiu, subu**
 - Memory: **lw, sw, lb, sb, lbu**
 - Decision: **beq, bne, slt, slti, sltu, sltiu**
 - Unconditional Branches (Jumps): **j, jal, jr**
- ▶ Registers we know so far
 - All of them!
 - There are CONVENTIONS when calling procedures!