CSE 31 HW #6

Fill in the following table (assume 1 valid bit and 1 dirty bit):

Address Bits	Cache Size	Block Size	Associativity	Tag Bits	Index Bits	Offset Bits	Bits Per Row
16	4KB	4B	1	4	10	2	38
16	16KB	8B	2	3	10	3	12
16	32KB	8B	4	3	10	3	267
16	2KB	4B	2	6	8	2	74
32	8KB	8B	1024	29	0	3	67613
32	32KB	16B	2	18	10	4	278
32	64KB	16B	1	16	12	4	146
32	512KB	32B	4	17	10	5	1049
64	512KB	256B	2	46	10	8	4146
64	2MB	256B	4	26	30	8	283
64	2MB	128B	1	43	14	7	1069
64	512KB	128B	1	256	14	6	162
64	4MB	32B	2	46	16	5	79