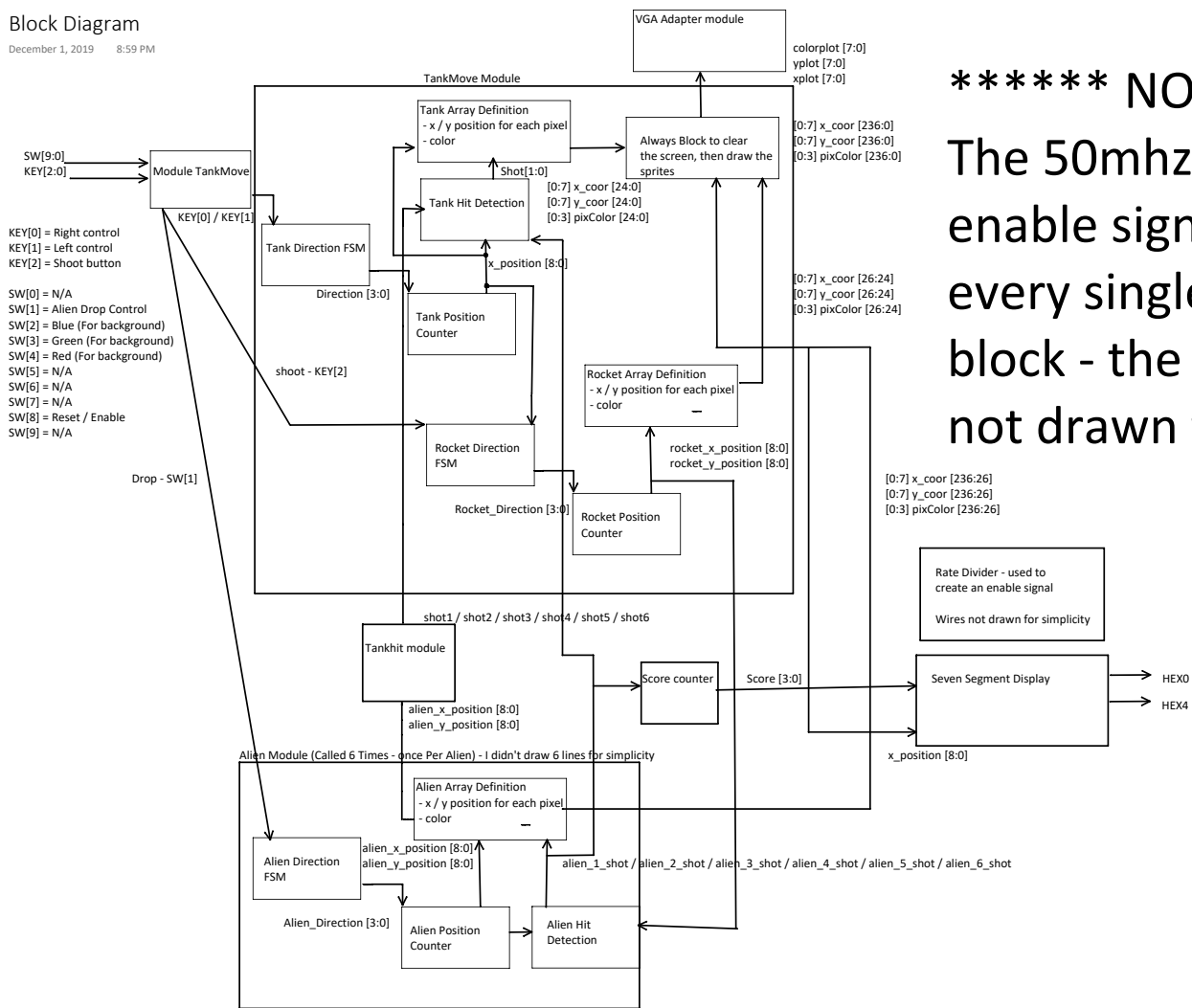


## Block Diagram

December 1, 2019 8:59 PM



\*\*\*\*\* NOTE \*\*\*\*\*

The 50mhz clock and enable signal go to every single always block - the wires are not drawn for simplicity