

Members in Attendance

- Felix Moss
- Annika Boyd
- Nathan Truong
- Eisa ALsharifi

Agenda

- Any updates?
- Plans for wednesday?

Notes

- Begin breadboard building on Wednesday
 - Use huge resistor on signal input for reducing the sine wave
 - Set voltage to low value established in LTSpice
- People have midterms so not much progress has been made, no big deal though
- Felix has to download and start messing around with AutoCAD and Fusion 360
 - Also has to make weekly progress report