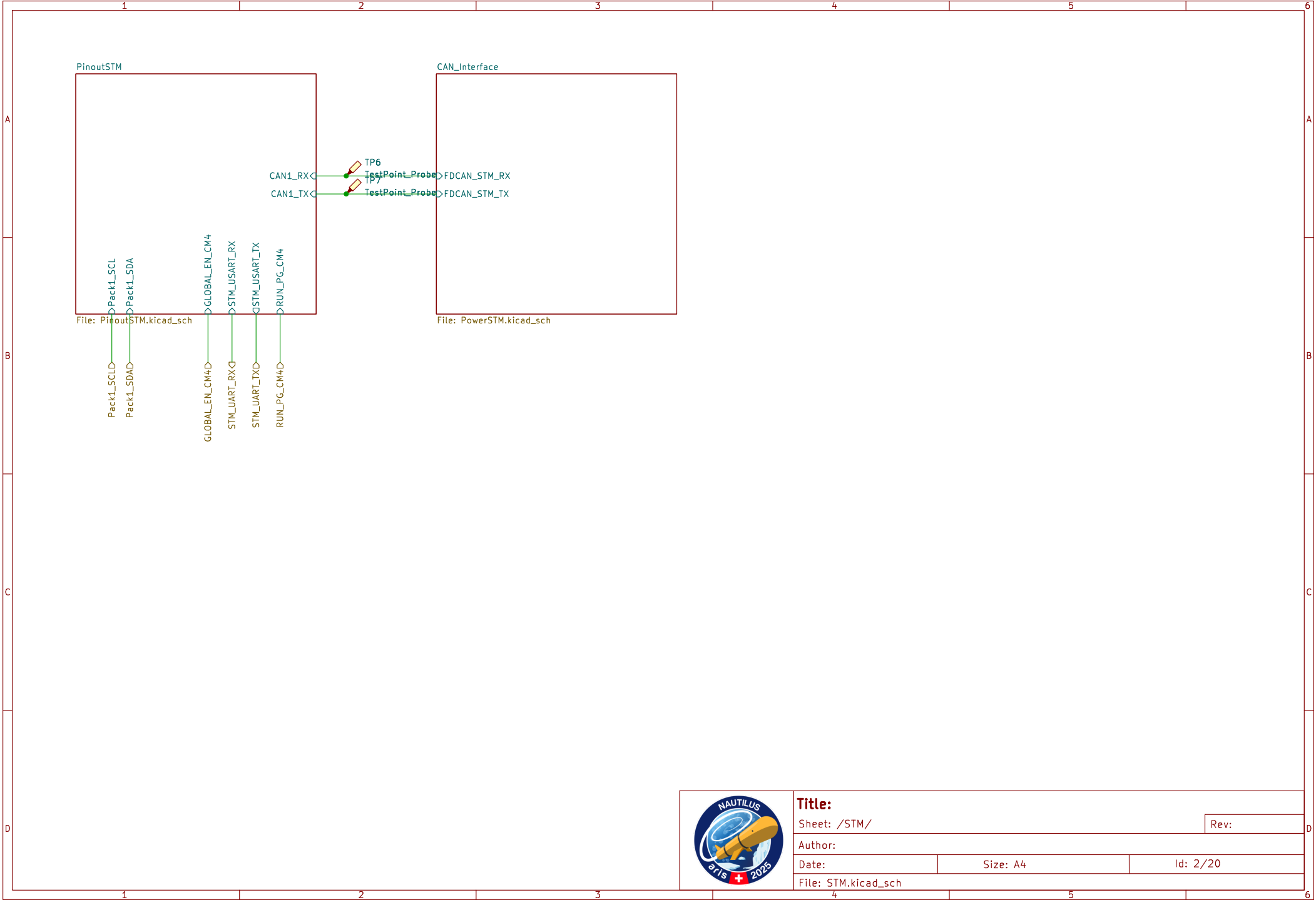
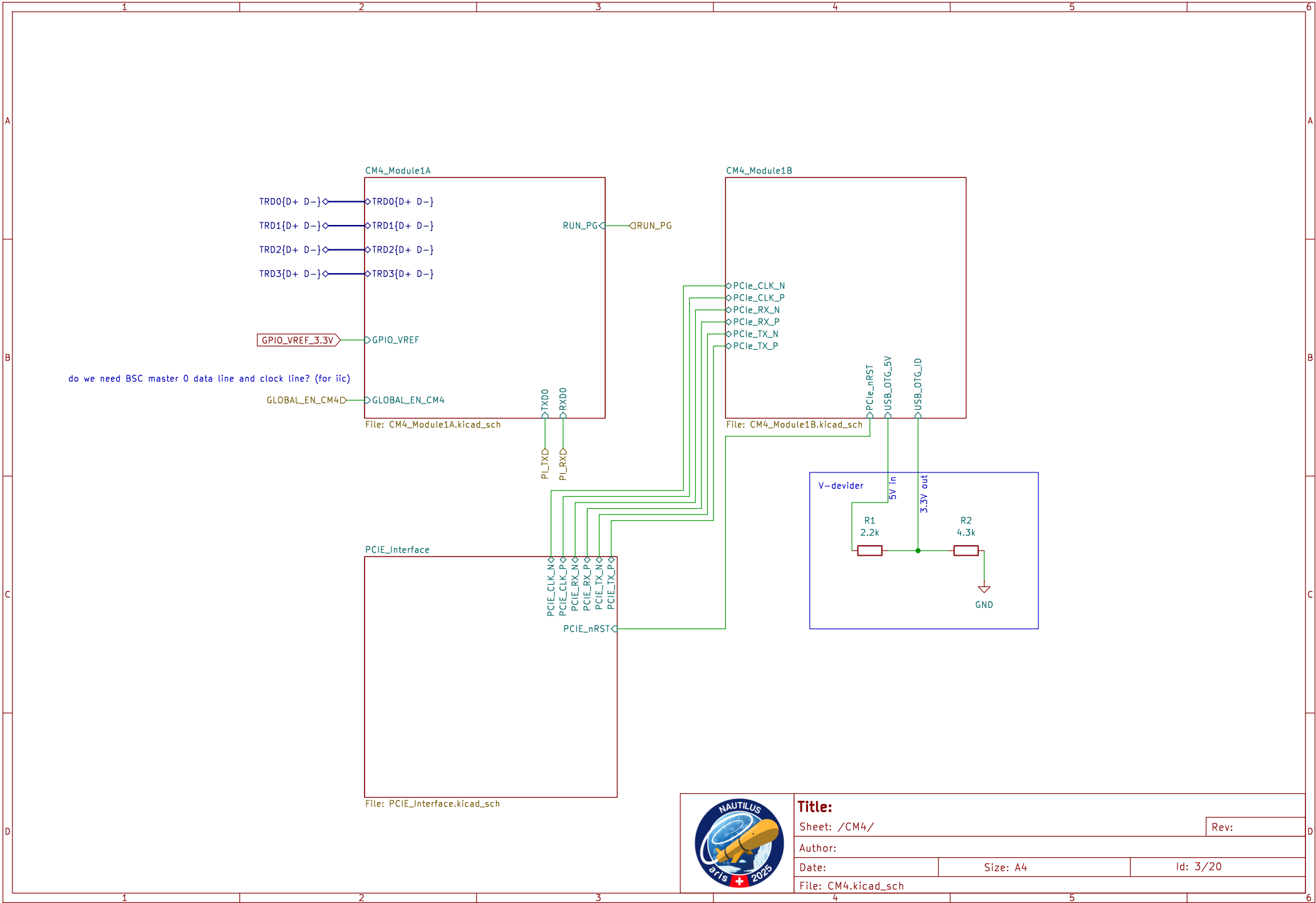
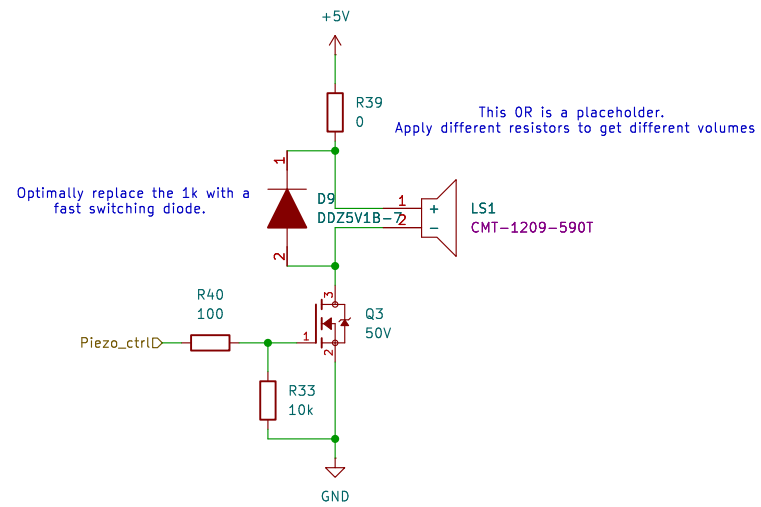
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Not in the PCB yet as we dont know if we actually need it.



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Author:

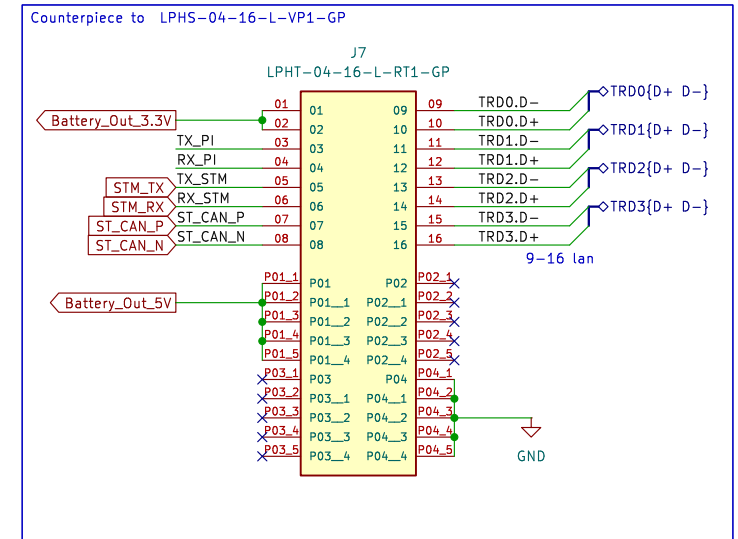
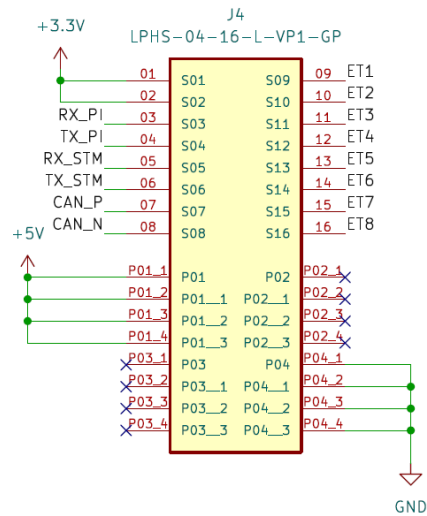
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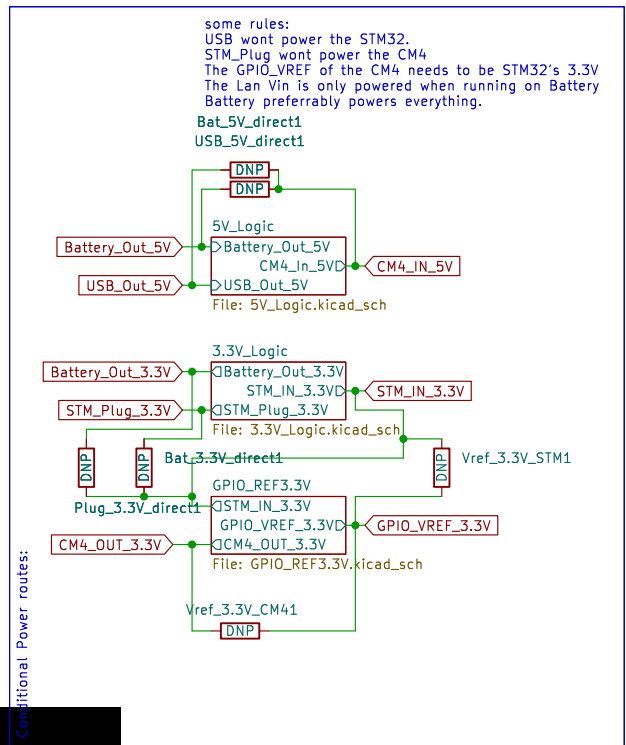
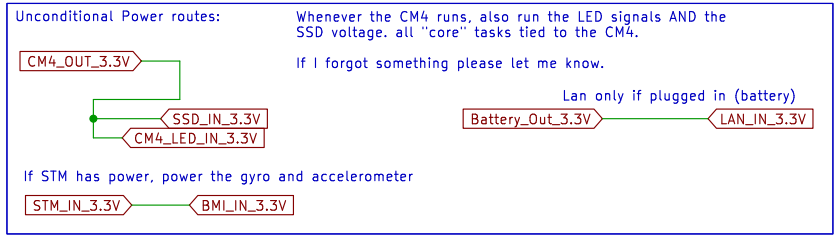
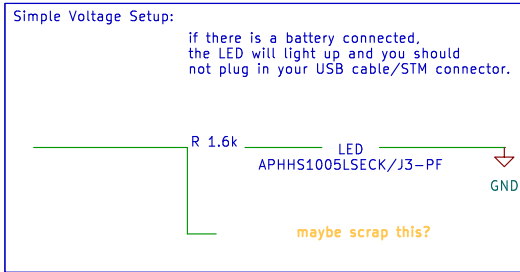
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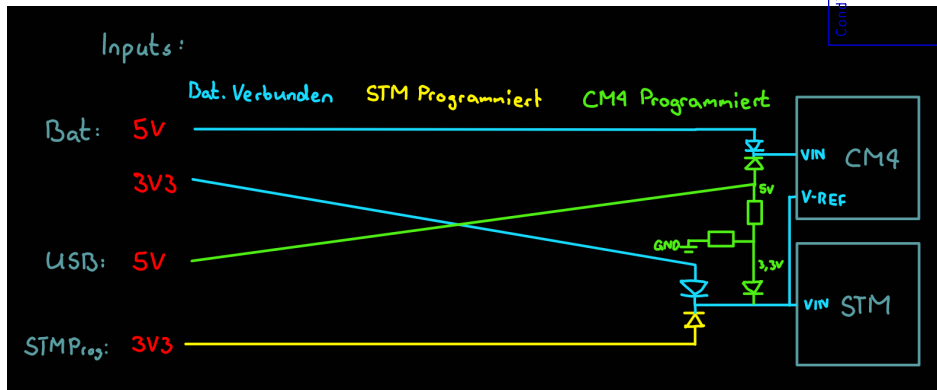
Mainboard



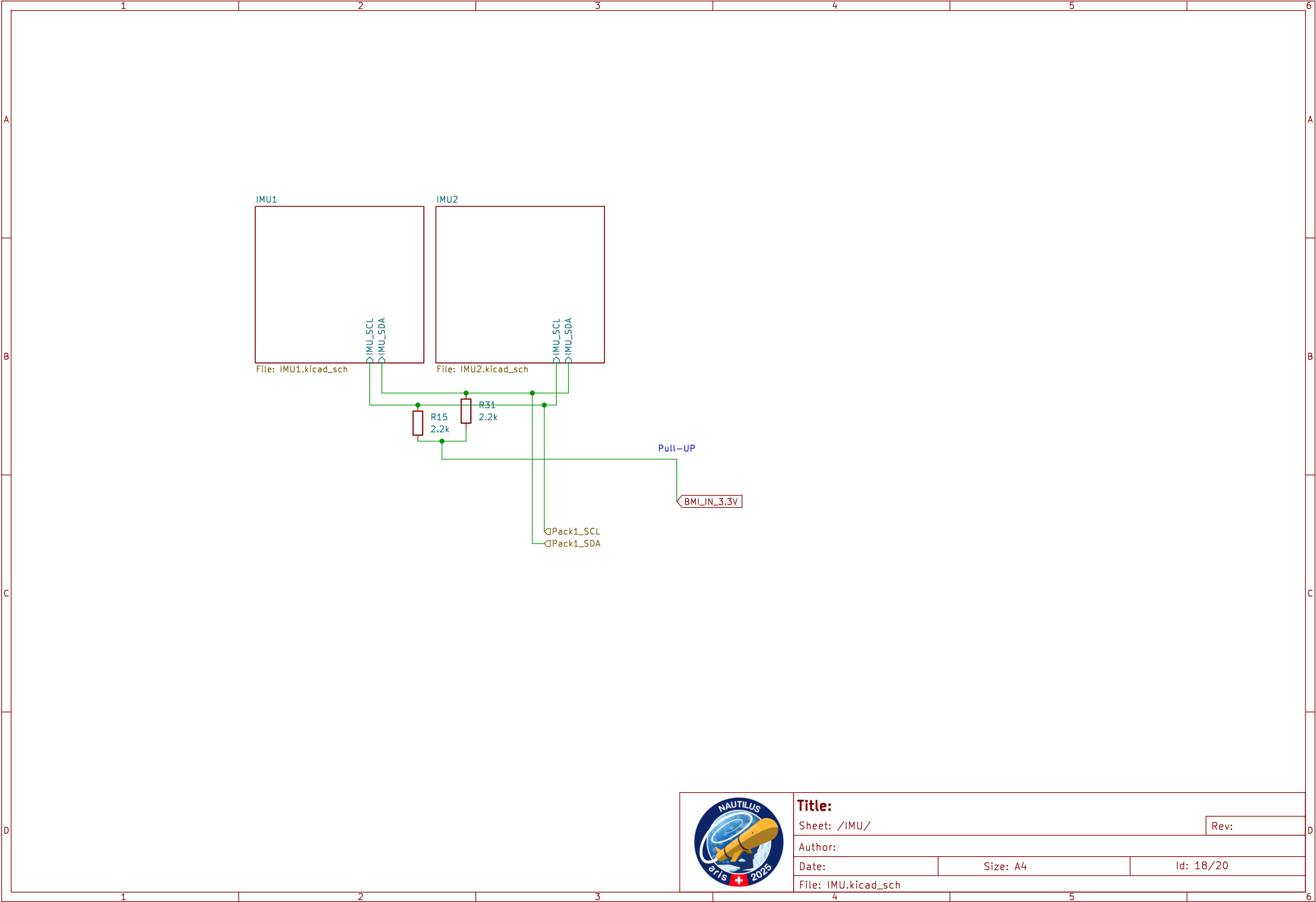
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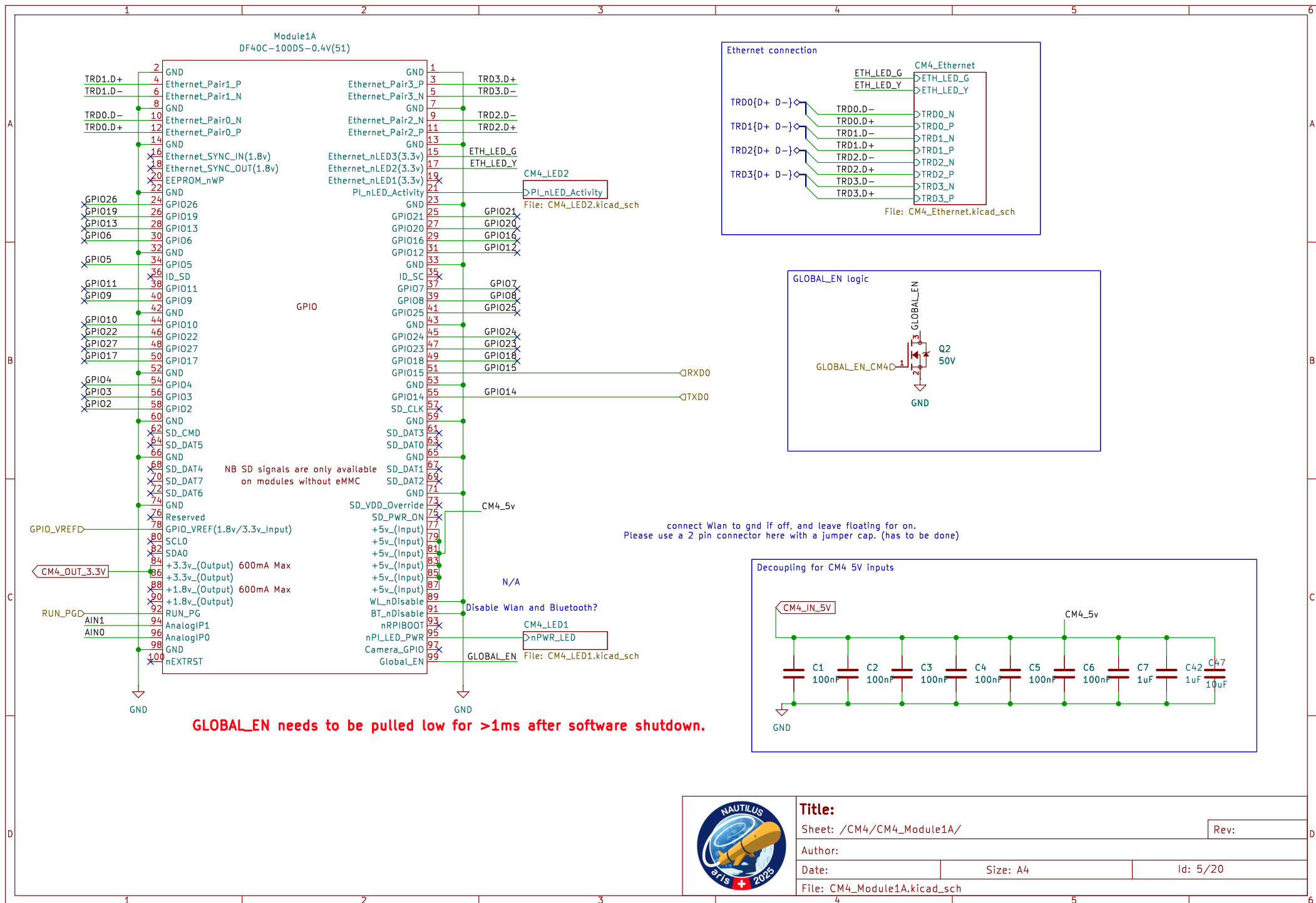
Wont work... diodes are not perfect...

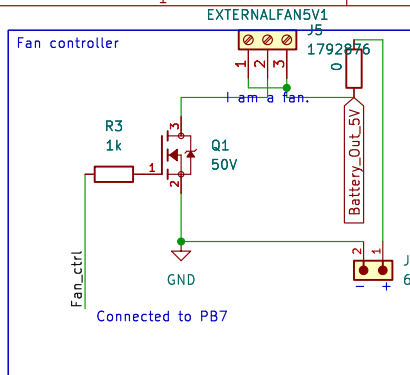


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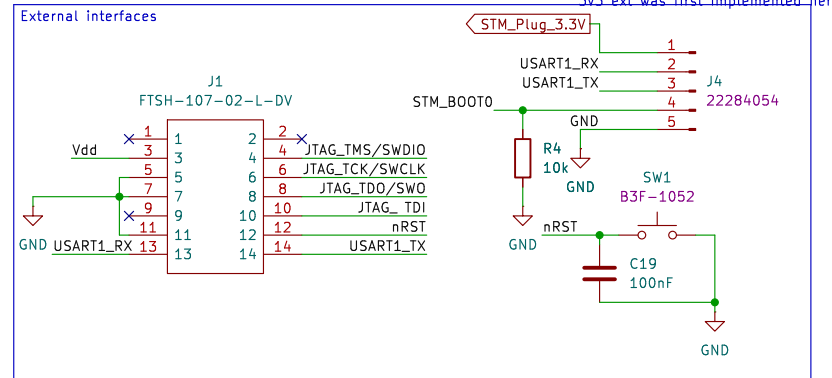
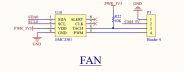


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fan only active when battery is connected.
Mby add an external power pin to externally power the fan for testing.



logic handled in power section

Vdd STM_IN_3.3V

Vss GND

STDC14 for STLINK-V3MINI (STM32 JTAG/SWD and VCP)

Figure 11. CN5 STDC14 connector (Top view)

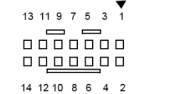


Table 4. CN5 STDC14 connector pinout

STDC14 Pin #	ARM10 Pin #	Pin description	Type
1	-	Reserved ⁽¹⁾	-
2	-	Reserved ⁽¹⁾	-
3	1	T_VCC ⁽²⁾	I
4	2	T_JTMS/T_SWCLK	I/O
5	3	GND	S
6	4	T_JCLK/T_SWCLK	O
7	5	GND	S
8	6	T_JTDO/T_SWO ⁽³⁾	I
9	7	T_JCLK	O
10	8	T_JTDI/NC ⁽⁴⁾	O
11	9	GNDDetect	O
12	10	T_NRST	O
13	-	T_VCP_RX	O
14	-	T_VCP_TX	I

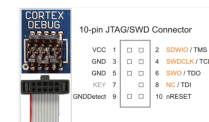
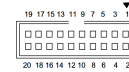


Figure 10. JTAG debugging flat ribbon layout



Title:

Sheet: /STM/PinoutSTM/

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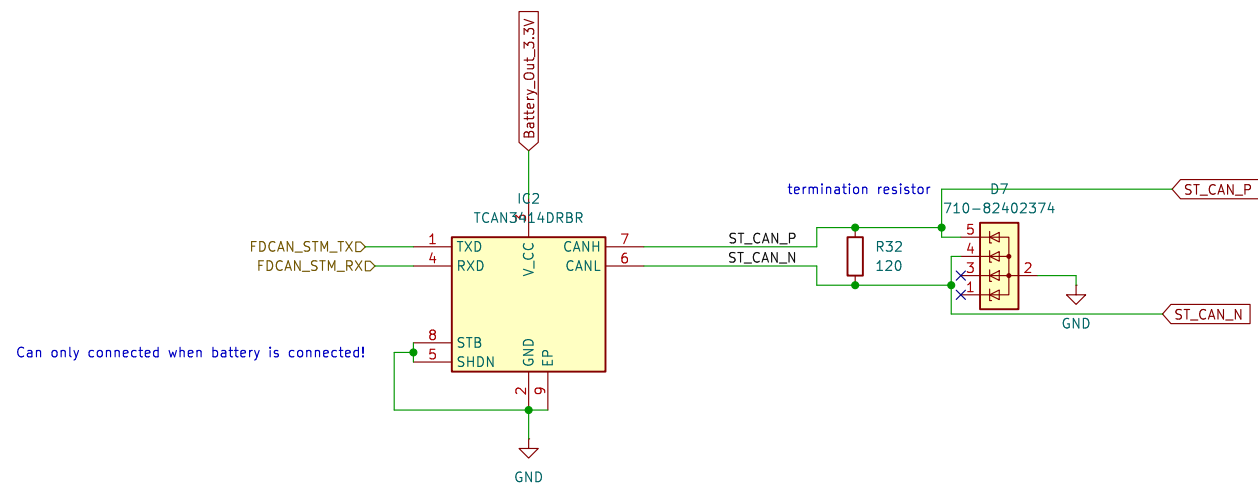
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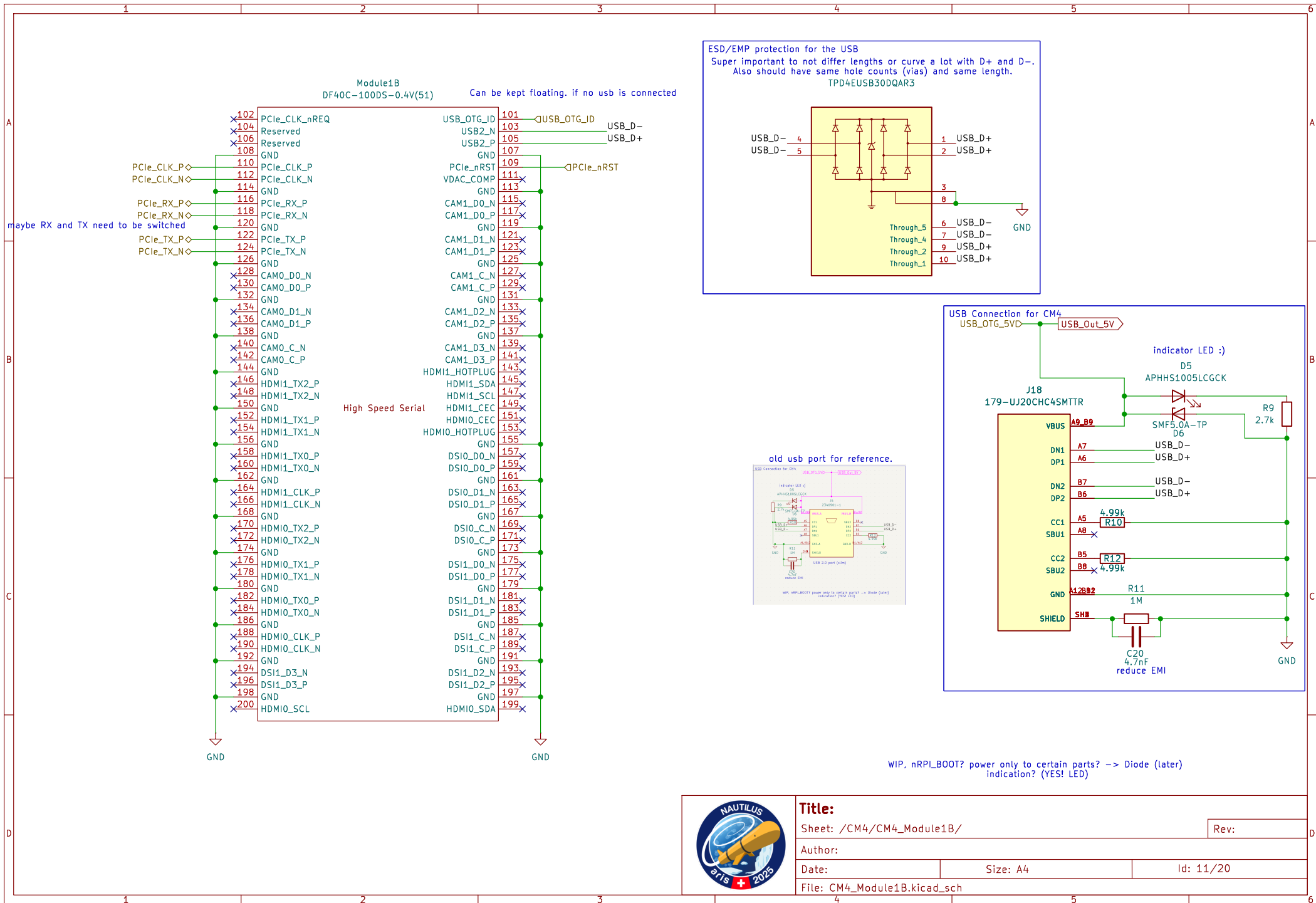
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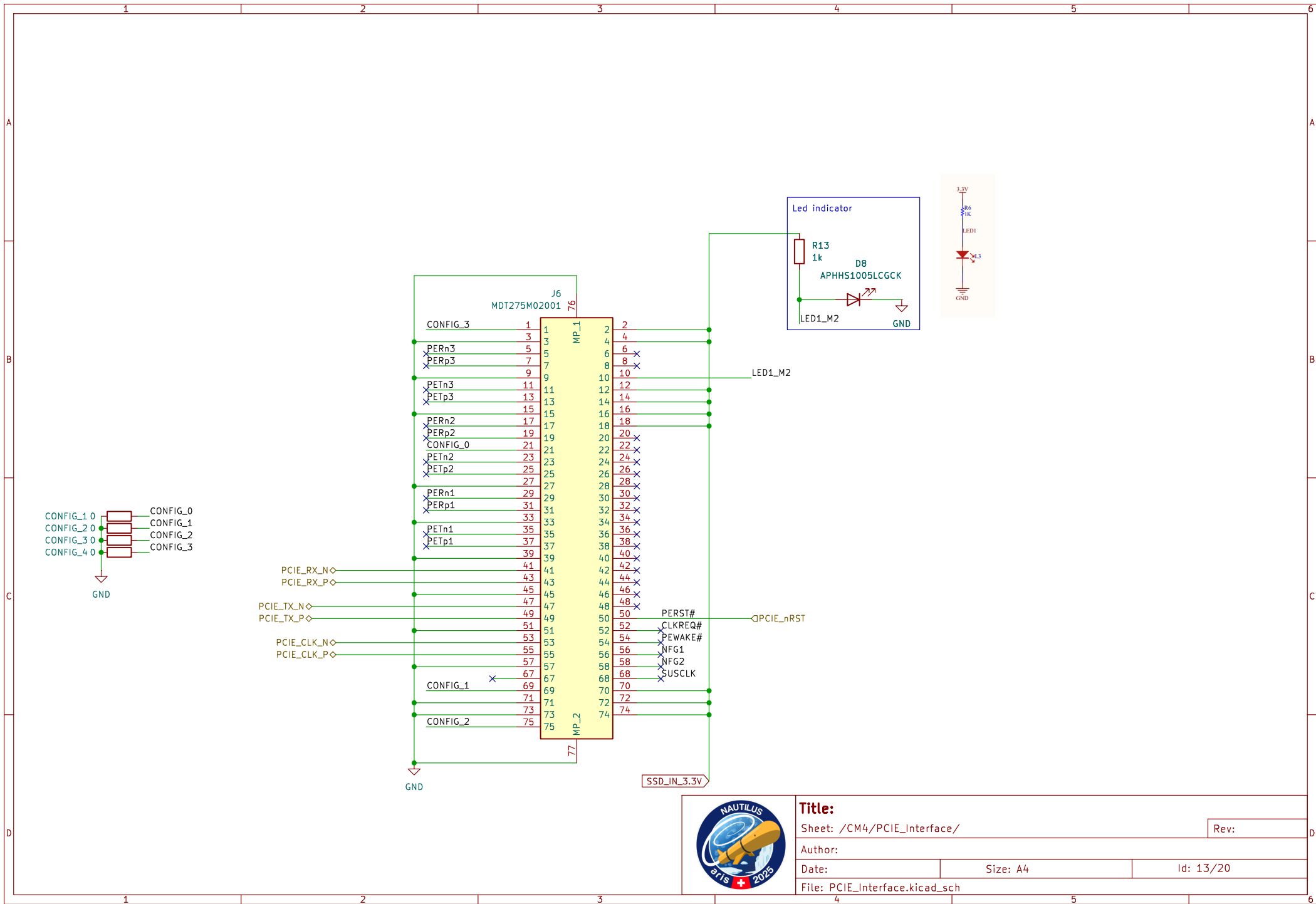
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Title:		
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Date:	Size: A4	Id: 13/20
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for questions about wiring etc please consult the datasheet...
<https://www.ti.com/lit/ds/symlink/tps2120.pdf?ts=1761678178328>

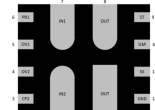
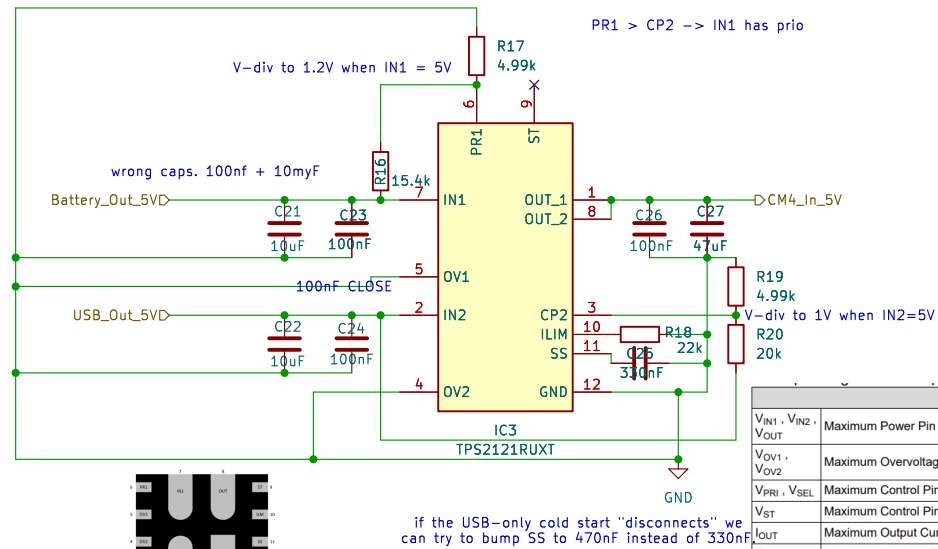


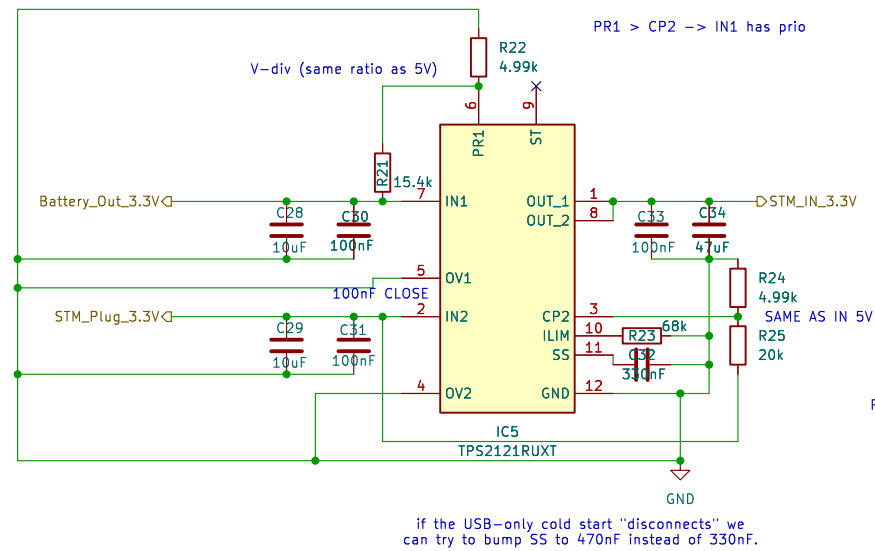
Figure 6-2. TPS2121 (RUX) Package 12-Pin VQFN-HR Bottom View

NAME	PIN		IO	DESCRIPTION
	TPS2120	TPS2121		
IN1	B1, B2, C1	7	I	Power Input for Source 1
IN2	B3, B4, C4	2	I	Power Input for Source 2
OUT	C2, C3, D1, D2, D3, D4	1, 8	O	Power Output
ST	E1	9	O	Status output indicating which channel is selected. Connect to GND if not required.
ILIM	E2	10	O	Output Current Limiting for both channels.
ISL	E3	11	O	Adjusts Input Settling Delay Time and Output Soft Start Time
GND	E4	12	—	Device Ground
PRI	A1	6	I	Enables Priority Operation. Connect to IN1 to set switchover voltage. Connect to GND if not required.
OV1	A2	5	I	Active Low Enable Supervisor for IN1 Overvoltage Protection. Connect to GND if not required.
OV2	A3	4	I	Active Low Enable Supervisor for IN2 Overvoltage Protection. Connect to GND if not required.
SEL	A4	—	I	Active Low Enable for IN1. Allows GPIO to override priority operation and manually select IN2. TPS2120 only.
CP2	—	3	I	Enables Comparator Operation and is compared to PRI to set switchover voltage. Connect to GND if not required. TPS2121 only.

		Pins	MIN	MAX	UNIT
V _{IN1} , V _{IN2} , V _{OUT}	Maximum Power Pin Voltage	IN1, IN2, OUT	-0.3	24	V
V _{OV1} , V _{OV2}	Maximum Overvoltage Pin Voltage	OV1, OV2	-0.3	6	V
V _{PRI} , V _{SEL}	Maximum Control Pin Voltage	PRI, SEL	-0.3	6	V
V _{ST}	Maximum Control Pin Voltage	ST	-0.3	6	V
I _{OUT}	Maximum Output Current	OUT	Internally Limited		
T _{J, MAX}	Maximum Junction Temperature		Internally Limited		
T _{STG}	Storage temperature		-65	150	°C



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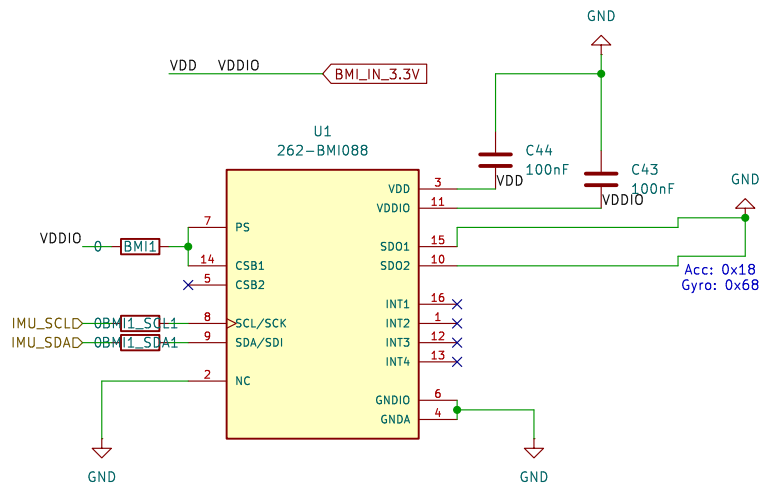
[R] = [kOhm]	22 kΩ	→ -4.55 A
65.2 / R*0.861	39 kΩ	→ -2.78 A
	50 kΩ	→ -2.25 A
	56 kΩ	→ -2.04 A
	68 kΩ	→ -1.72 A
	100 kΩ	→ -1.24 A



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PS -> 3.3V IIC
CSB1 -> 3.3V NOT SPI

PB8 on STM
PB9 on STM



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Date:

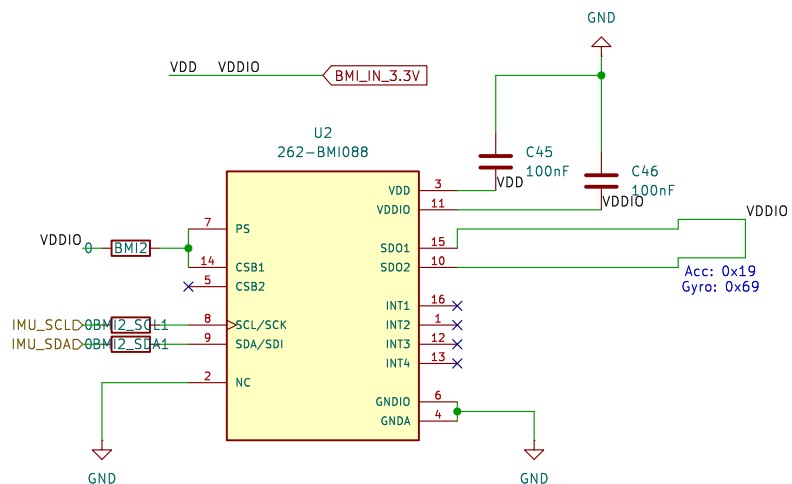
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PS -> 3.3V IIC
CSB1 -> 3.3V NOT SPI

PB8 on STM
PB9 on STM



Accelerometer
SD01 pin pulled to GND (0x18)
SD01 pin pulled to VDDIO: (0x19)

Gyroscope:
SD02 pin pulled to GND: (0x68)
SD02 pin pulled to VDDIO: (0x69)



Title:

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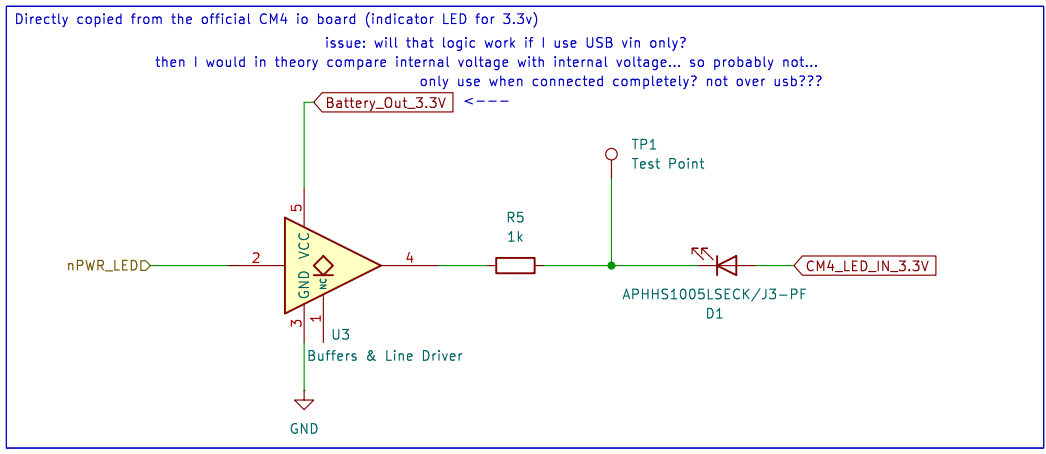
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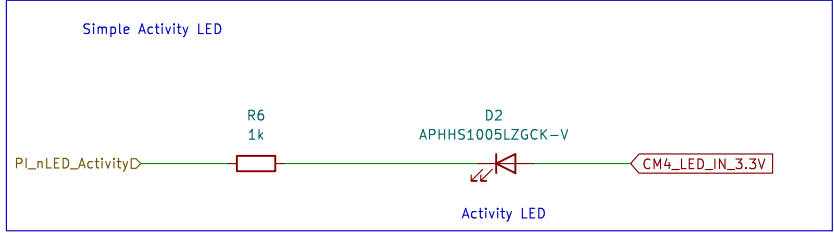
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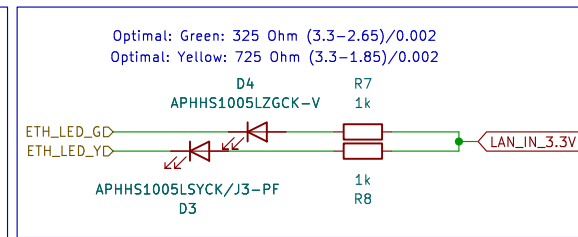
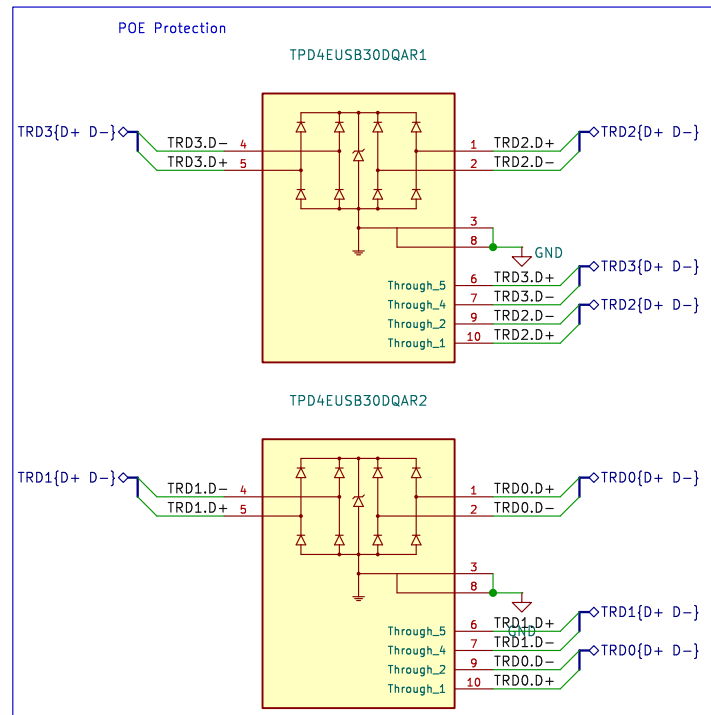
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