

Title

Sheet: 4

Author:

Date:

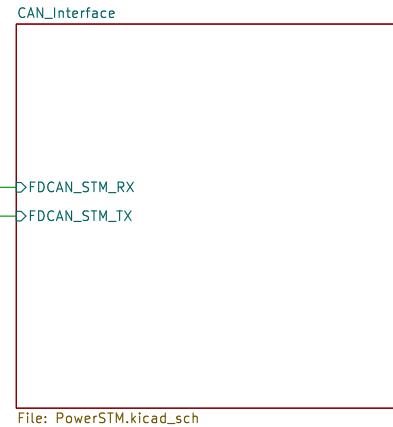
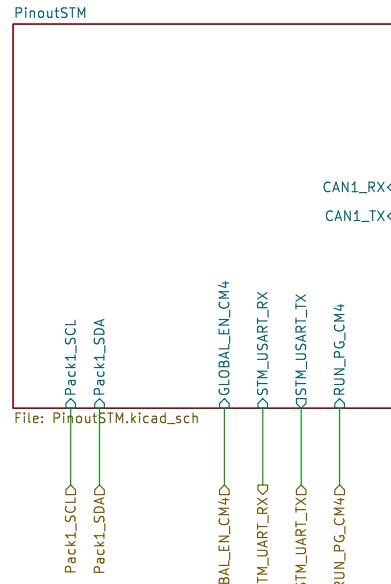
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Rev:

Id: 1/20

10 of 10

1 2 3 4 5 6



Title:

Sheet: /STM/

Rev:

Author:

Date:

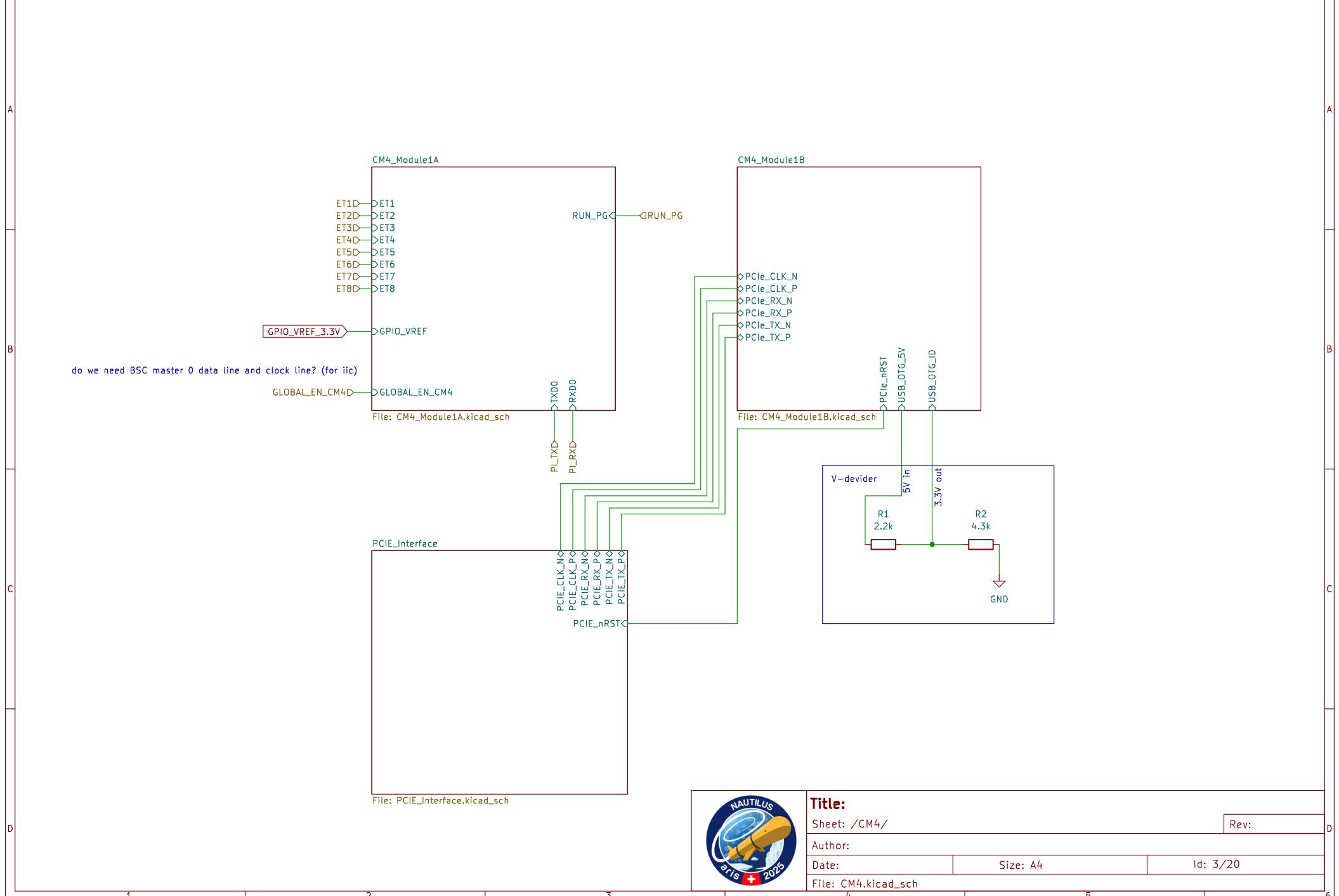
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A

A

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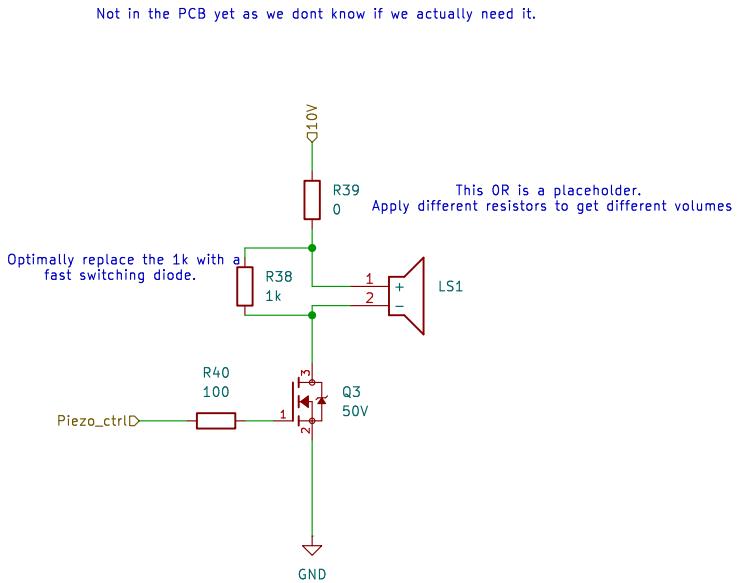
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Rev:

Author:

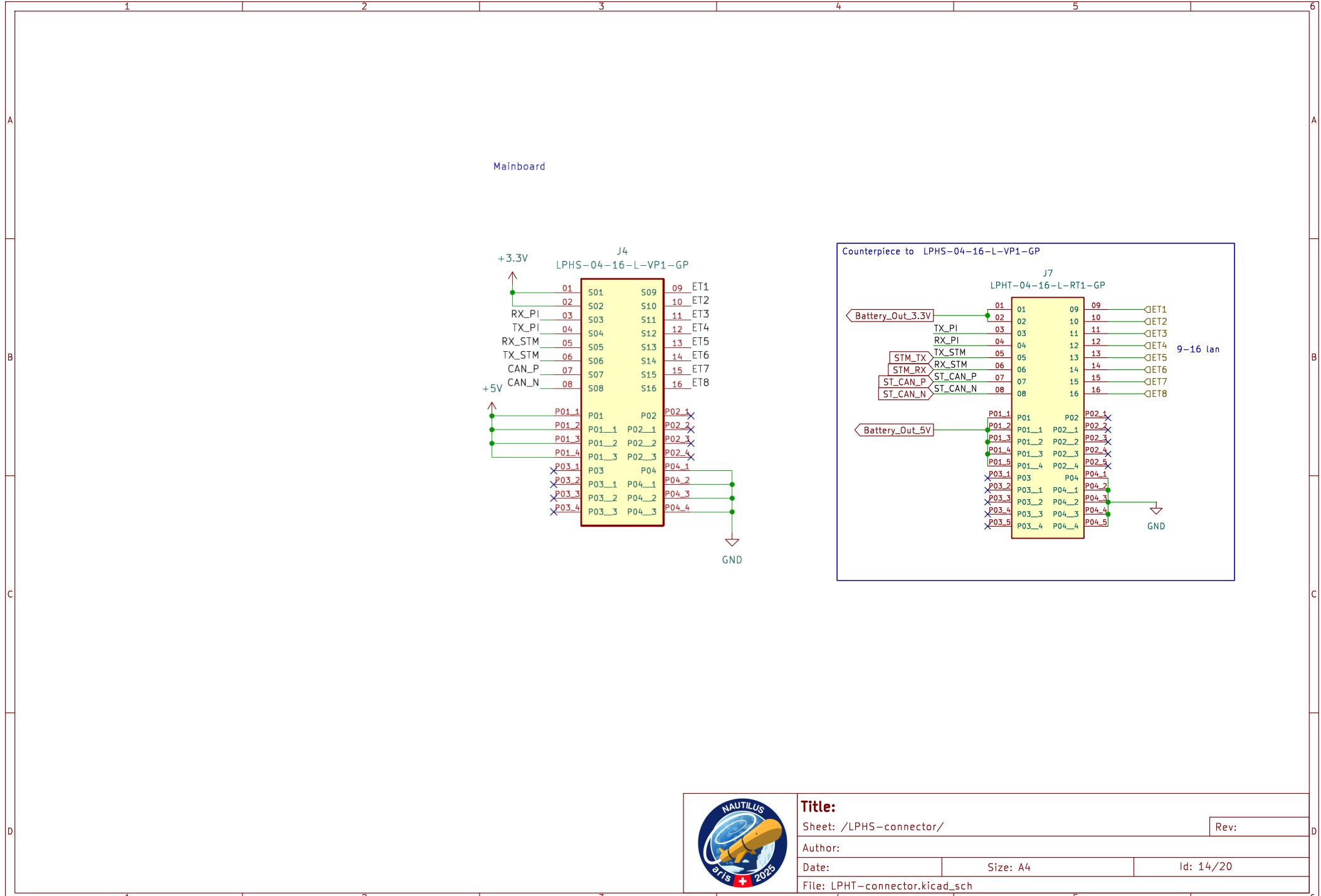
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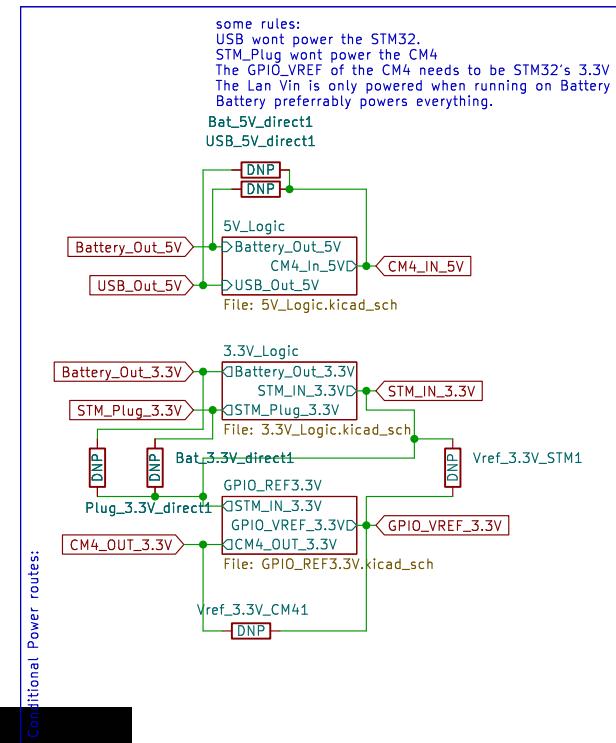
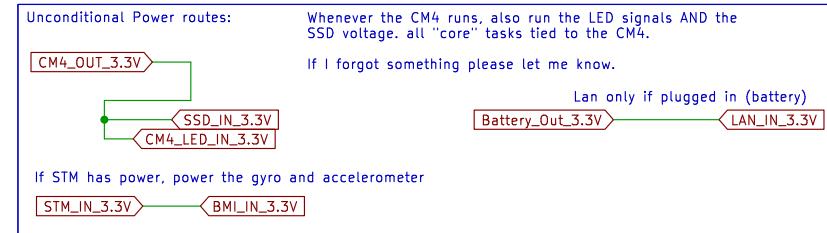
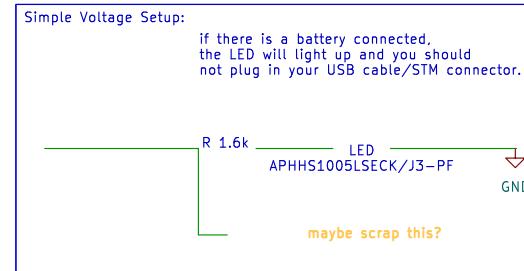
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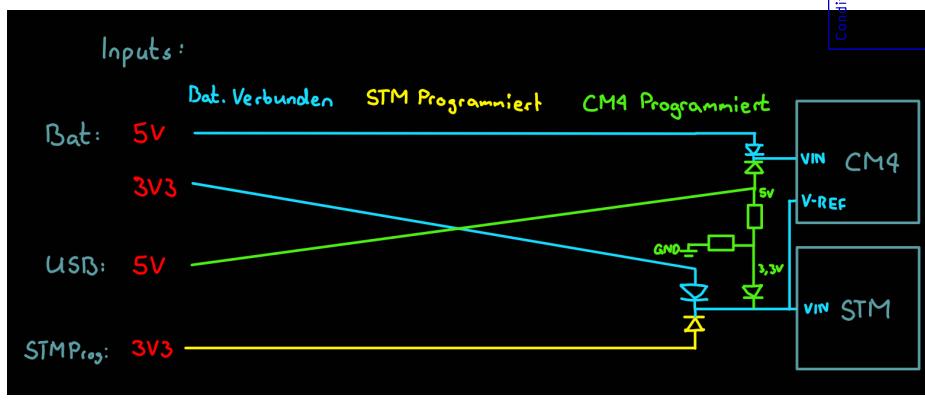
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File: Piezzo.kicad_sch





Wont work... diodes are not perfect...



Title:

Sheet: /Power_logic/

Author:

Date:

Size: A4

Rev:

Id: 14/20

File: Power_logic.kicad_sch

A

A

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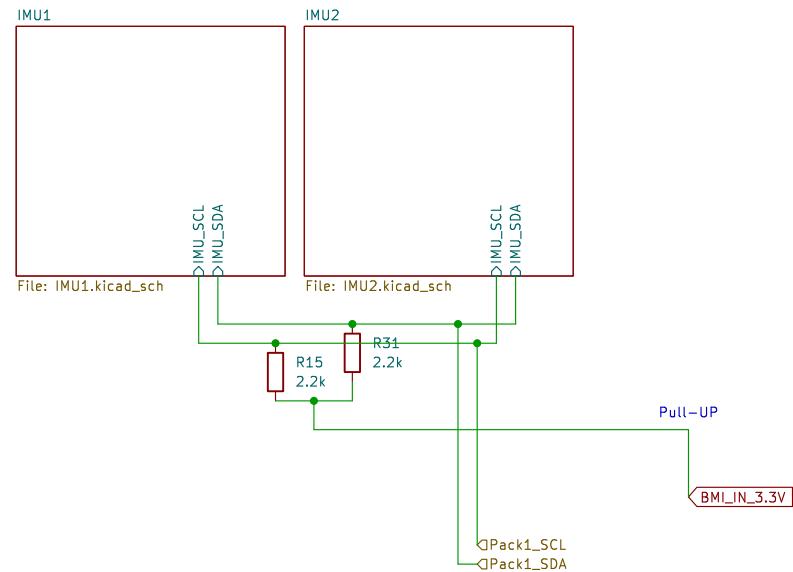
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D

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Rev:

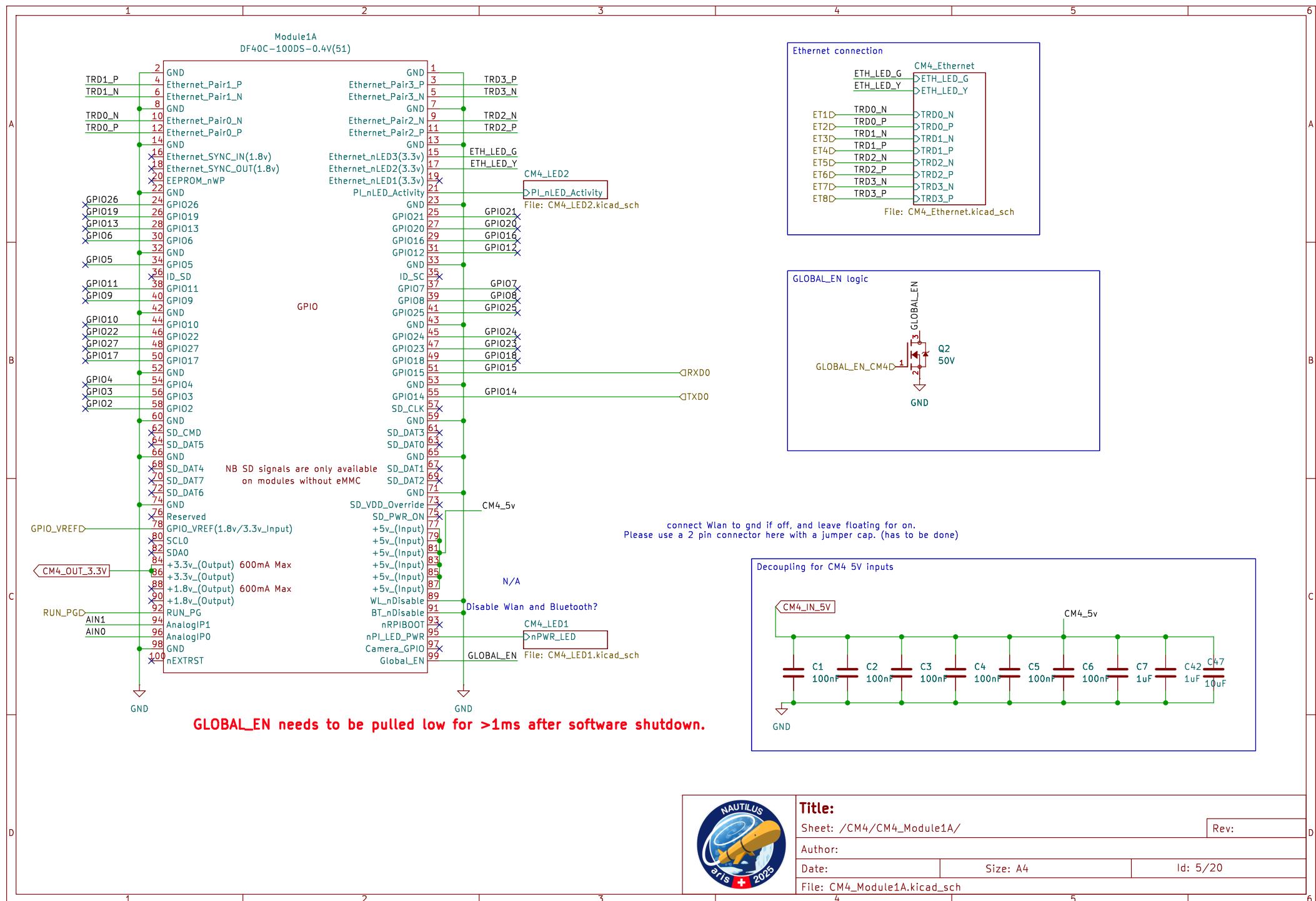
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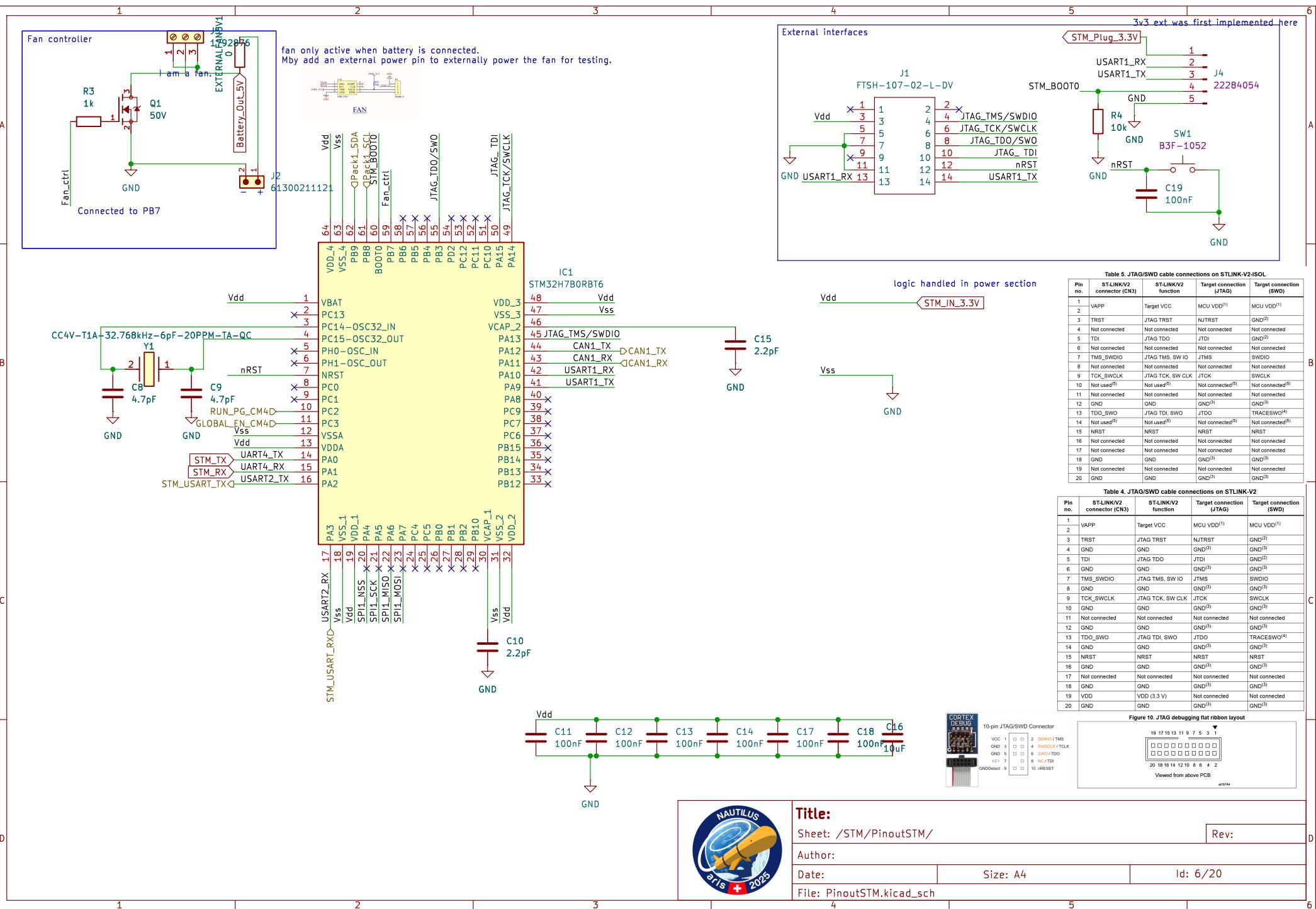
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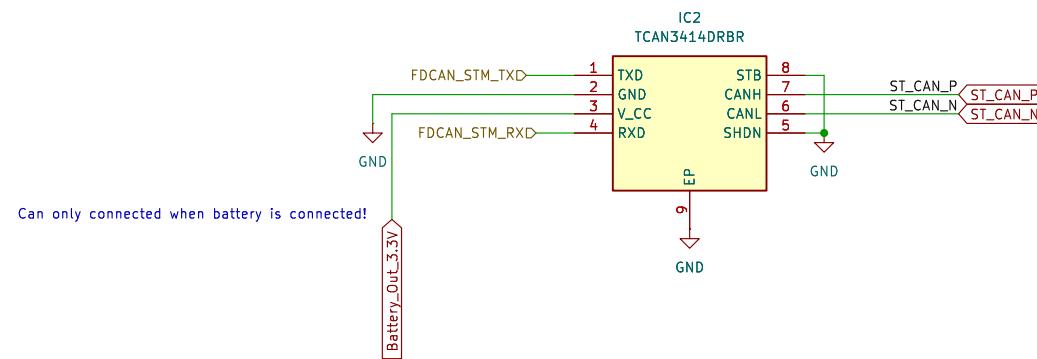
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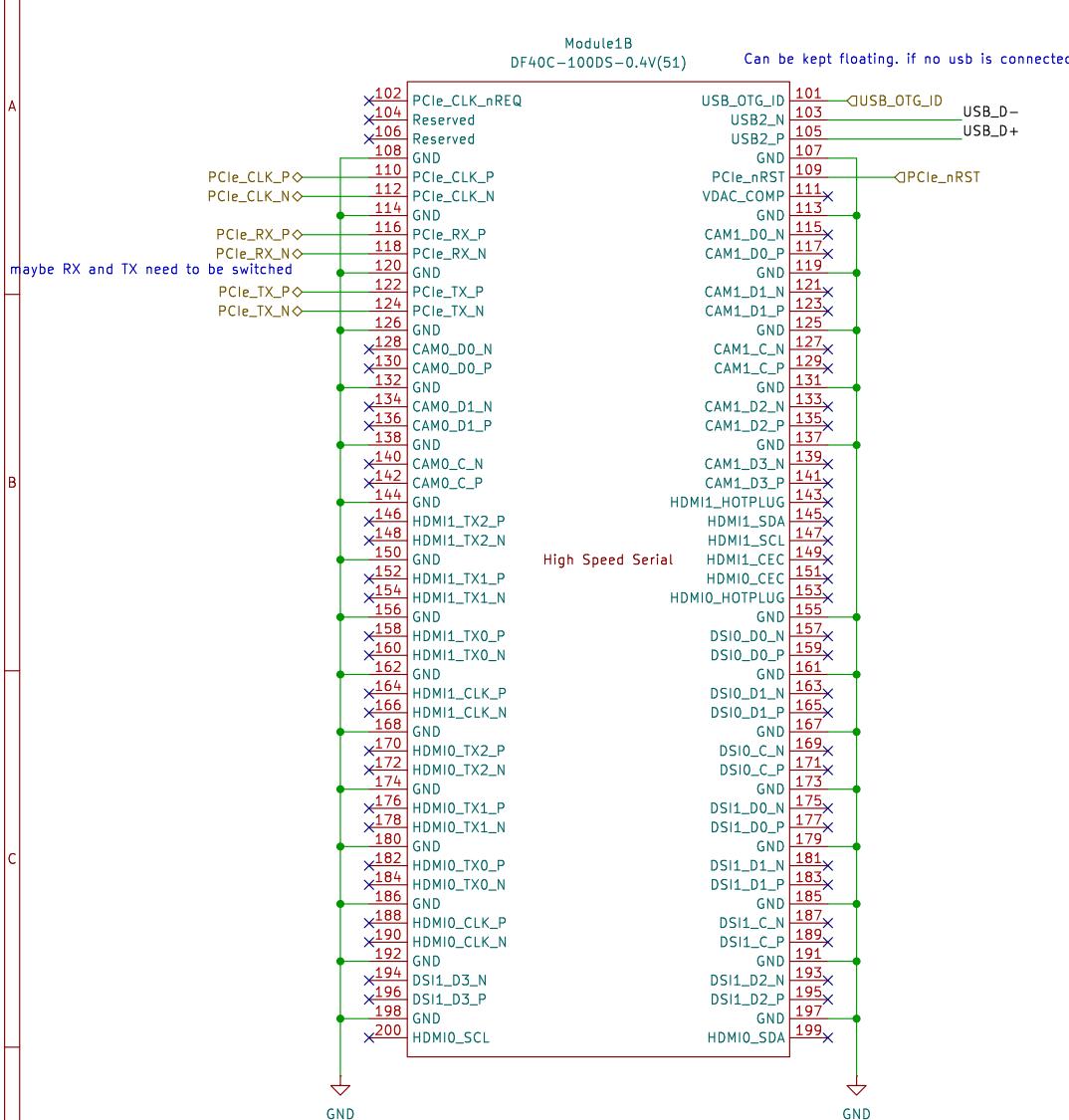
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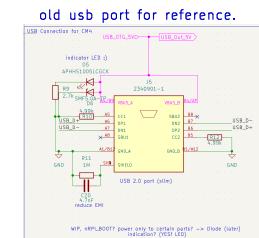
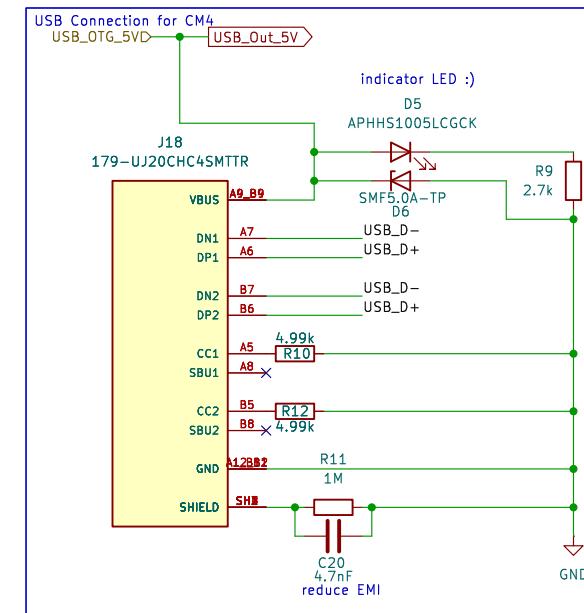
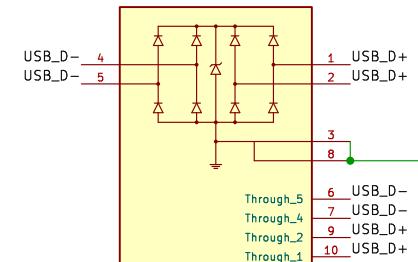
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1 2 3 4 5 6



ESD/EMP protection for the USB
Super important to not differ lengths or curve a lot with D+ and D-.
Also should have same hole counts (vias) and same length.

TPD4EUSB30DQAR3



WIP, nRPI_BOOT? power only to certain parts? → Diode (later)
indication? (YES! LED)



Title:

Sheet: /CM4/CM4_Module1B/

Rev:

Author:

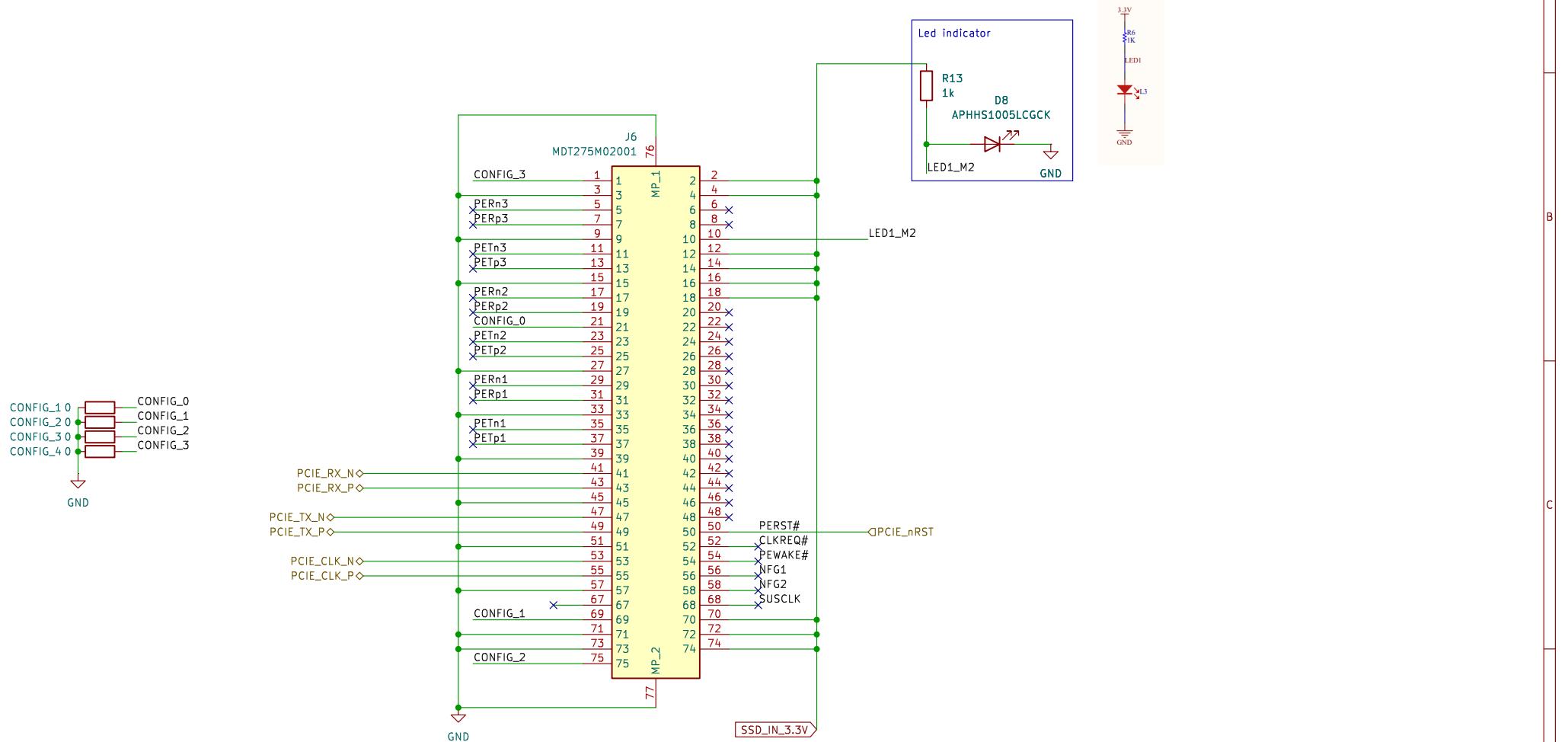
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File: CM4_Module1B.kicad_sch

1 2 3 4 5 6



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Author:

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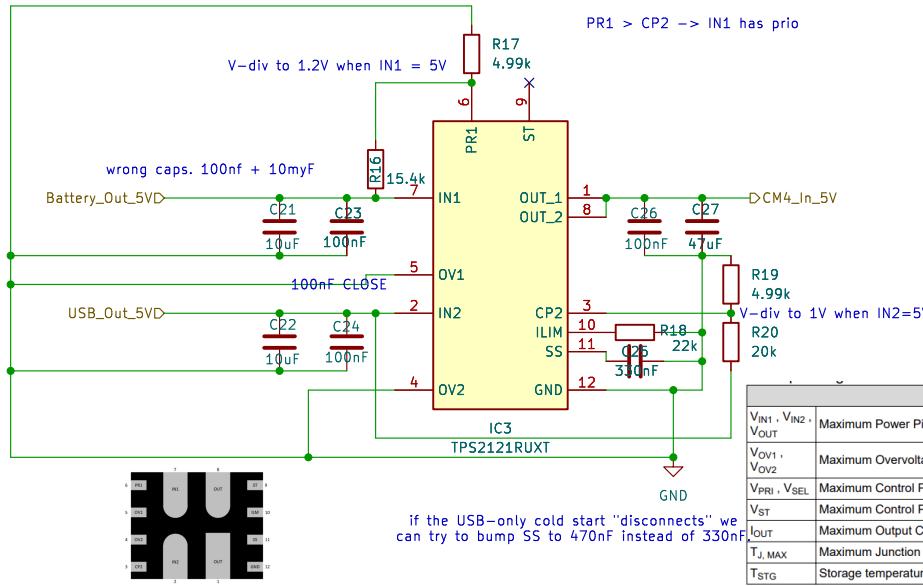
Review

Id: 13/20

A

A

for questions about wiring etc please consult the datasheet..
<https://www.ti.com/lit/ds/symlink/tps2120.pdf?ts=1761678178328>



	Pins	MIN	MAX	UNIT
V _{IN1} , V _{IN2} , V _{OUT}	IN1, IN2, OUT	-0.3	24	V
V _{OV1} , V _{OV2}	OV1, OV2	-0.3	6	V
V _{PRI} , V _{SEL}	PRI, SEL	-0.3	6	V
V _{ST}	ST	-0.3	6	V
I _{OUT}	OUT			Internally Limited
T _{J, MAX}				Internally Limited
T _{STG}		-65	150	°C

Figure 6-2. TPS2121 (RUX) Package 12-Pin VQFN-HR Bottom View

Pin Functions		
NAME	TPS2120	TPS2121
IN1	B1, B2, C1	I Power Input for Source 1
IN2	B3, B4, C4	I Power Input for Source 2
OUT	C2, C3, D1, D2	I Power Output
ST	E1	0 Status output indicating which channel is selected. Connect to GND if not required.
ILIM	E2	0 Output Current Limiting for both channels.
SS	E3	0 Adjusts Input Setting Delay Time and Output Soft Start Time
GND	E4	—
PR1	A1	— Enables Priority Operation. Connect to IN1 to set switchover voltage. Connect to GND if not required.
OV1	A2	— Active Low Enable Supervisor for IN1 Overvoltage Protection. Connect to GND if not required.
OV2	A3	— Active Low Enable Supervisor for IN2 Overvoltage Protection. Connect to GND if not required.
SEL	A4	— Active Low Enable for IN1. Allows GPIO to override priority operation and manually select IN2. TPS2120 only.
CP2	—	3 I Enables Comparator Operation and is compared to PR1 to set switchover voltage. Connect to GND if not required. TPS2121 only.

**Title:**

Sheet: /Power_logic/5V_Logic/

Rev:

Author:

Date:

Size: A4

Id: 15/20

File: 5V_Logic.kicad_sch

A

A

B

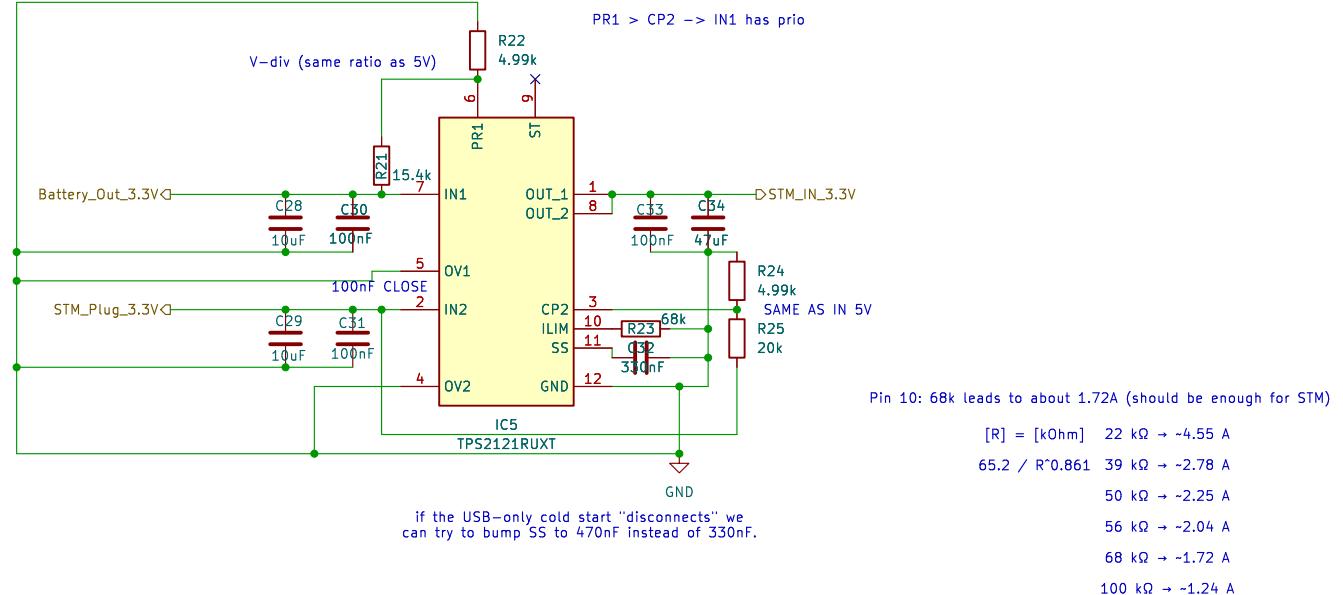
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**Title:**

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Rev:

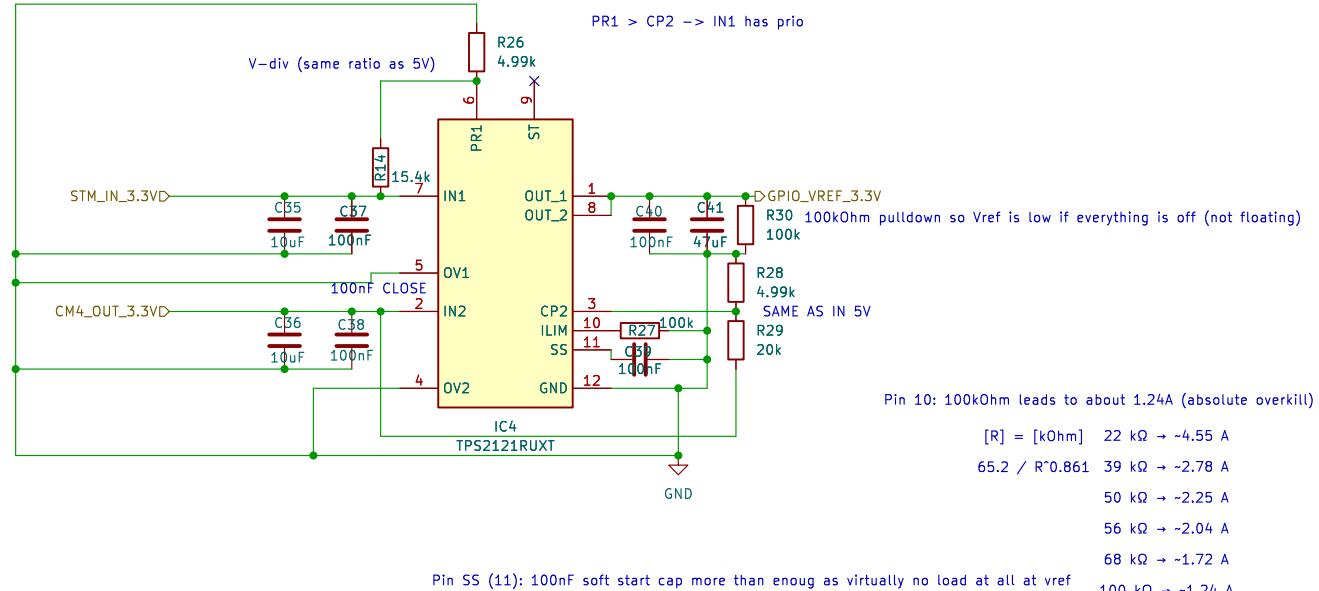
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Date:

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Title

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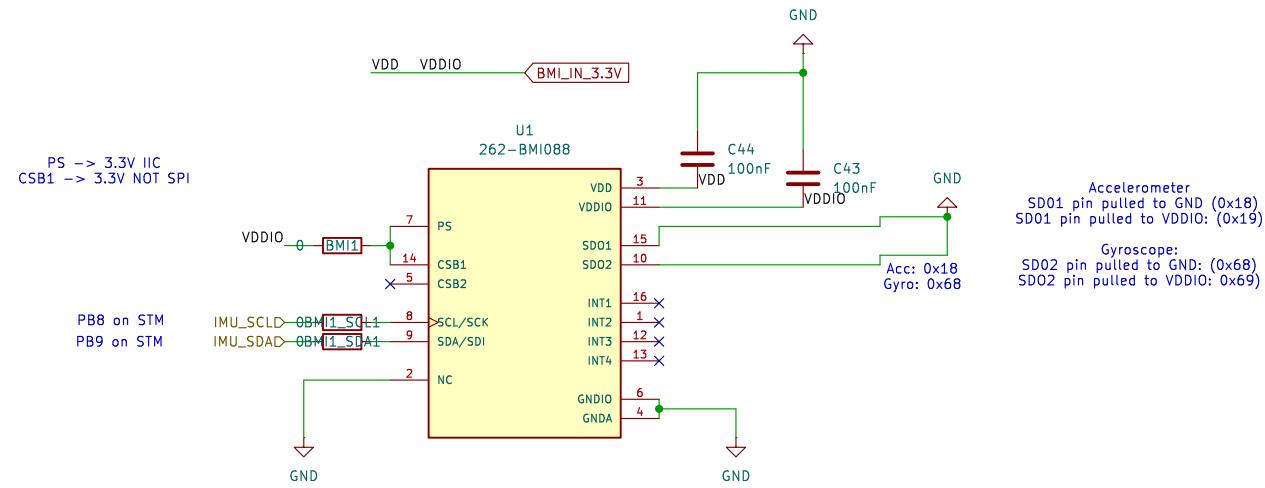
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Author:

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Id: 17/20



Title

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Author:

Date:

File: IMU1.kicad_sch

Rev:

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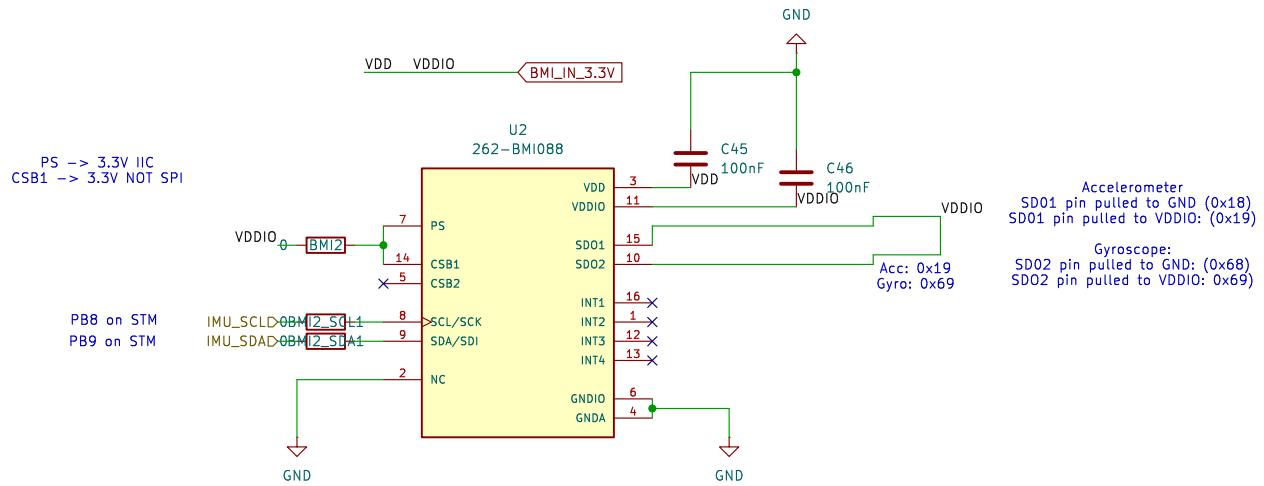
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**Title:**

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Rev:

Author:

Date:

Size: A4

Id: 20/20

File: IMU2.kicad_sch

A

A

B

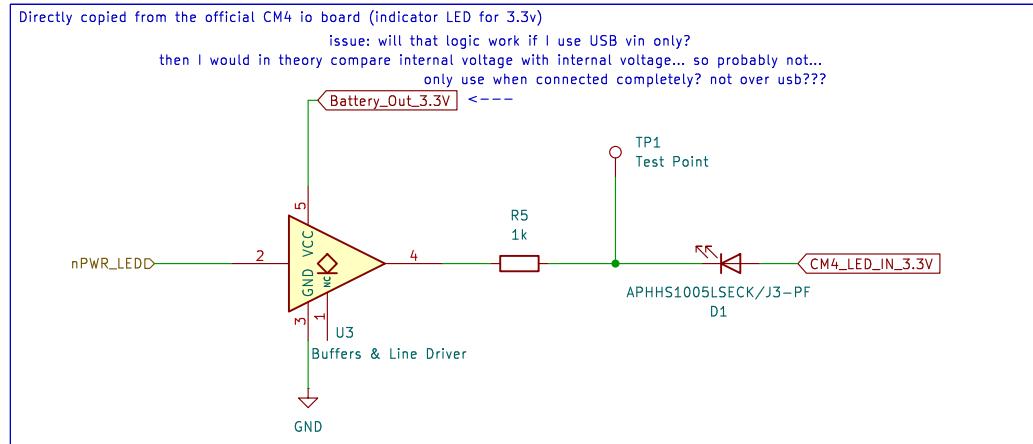
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Rev:

Author:

Date:

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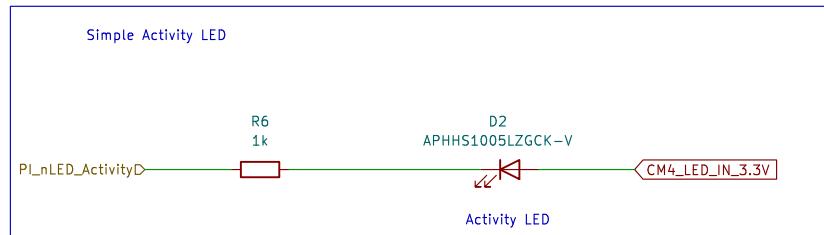
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1 2 3 4 5 6

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Title:

Sheet: /CM4/CM4_Module1A/CM4_LED2/

Rev:

Author:

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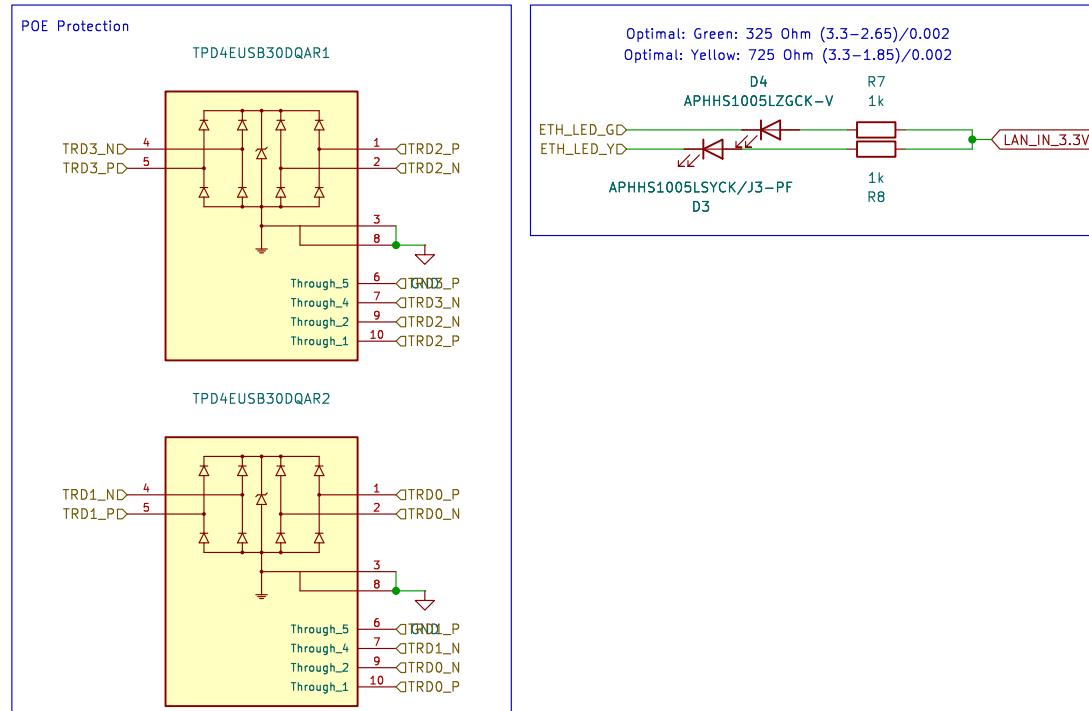
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1 2 3 4 5 6

A

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**Title:**

Sheet: /CM4/CM4_Module1A/CM4_Ethernet/

Rev:

Author:

Date:

Size: A4

Id: 10/20

File: CM4_Ethernet.kicad_sch