NAVEEN BHARATHI P

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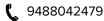○ naveenelan github.com/Naveenelan   ☎ 9488042479

EDUCATION

Kongu Engineering College - B.E., Electronics and Communication Engineering	7.49 CGPA	2021 - 2025
Little Flower Matriculation Higher Secondary School - HSC	82.6 %	2019 - 2021
Little Flower Matriculation School - SSLC	76.7 %	2018 - 2019

PROJECTS

Smart Yoga Mat - SIH'24 | Embedded
Developed a Smart yoga mat integrated with Smartwatch and the velostat material connected with Arduino to monitor the yoga.

Obstacles Detection Blind Stick | Embedded
Using an ultrasonic sensor and Arduino Mega, also incorporated GPS navigation functions into the stick.

FPGA Implementation of RO-PUF in NLFSR | VLSI
Designed an FPGA that was used to protect the IC from the cloning process by the generation of a Random Number Generator.

Ayush Web Portal - SIH'23 | Web development
Developed a web portal using React.js for the front end and MongoDB for the back end.

AREA OF INTEREST

- Digital Electronics
- VLSI Design
- Embedded Design

SKILLS

- Languages   - Verilog, C, Java
- Tools   - Cadence Virtuoso, MATLAB, Model Sim, Proteus, Xilinx Vivado
- DataBase   - MySQL
- Web development - HTML, CSS, Git-hub
- Full Stack - Basics

CERTIFICATES

Basics Machine Learning   - Completed this course in IBM Cognitive Class.

COURSES

Semiconductor Life Cycle	- KEC
Functional Verification in VLSI	- KEC
Basics Machine Learning	- IBM

ACHIEVEMENTS

2nd runner-up	Topic of FPGA on RO-PUF - Domain of VLSI	CT-UG - KEC | 3rd year
3rd runner-up	Smart blind stick	IDEATHON - KEC | 2nd year

RESPONSIBILITIES

Chairman of IETE Student Forum for the year	2024-2025
Joint secretary of ECE Association for the year	2023-2024
Executive member of Self Development Club for the year	2022-2024

LANGUAGES

Tamil	English	Japanese - Beginner

SOFT SKILLS

Team worker	Leadership	Adaptability