



Karnatak Law Society's
Vishwanathrao Deshpande Institute of Technology,
Haliyal - 581 329



Department of Electronics and Communication Engg.




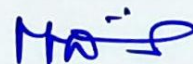
Course Completion Certificate

This is to certify that,

Naveen U. Badiger, bearing USN: **2VD19EC405**

has successfully completed the Add-On Course entitled
"Design of VLSI Systems and Verification of Digital Circuits Using Simulation Tool"
in 7th Semester for the Academic Year: 2021-2022 with score of 10/10.


Prof. Plasin Dias
Staff In-charge


Dr. Mahendra M. Dixit
HoD


Dr. V. A. Kulkarni
Principal