



Certificate no: UC-44b8867f-cba6-4965-a0a9-22f475225415

Certificate url: ude.my/UC-44b8867f-cba6-4965-a0a9-22f475225415

Reference Number: 0004

CERTIFICATE OF COMPLETION

Verilog HDL: VLSI Hardware Design Comprehensive Masterclass

Instructors **Shepherd Tutorials**

Naveen Badiger

Date **Feb. 26, 2023**

Length **12.5 total hours**