#5, C/o U. S. Badiger, Abhinav Nagar, Anand Nagar road, Hubli +917019346318

https://github.com/Naveenub/resume/blob/main/Resume.pdf badigernaveen2@gmail.com

NAVEEN U BADIGER

OBJECTIVETo pursue a challenging career in your organization which would be

mutually beneficial in terms of learning experience and contribution to

Organizational growth.

PROFILE Proficient in designing from scratch, testing FPGA based digital systems and embedded systems SUMMARY with SOC, Verilog design sources, Verilog test bench sources. Possess excellent communication skills and ready to work in a challenging environment.

SKILLS &KNOWLEDGE OF SEMICONDUCTOR/VLSI DESIGN

ABILITIESFundamentals Of Digital And Analog Circuits, Design Methodologies, Design Flow, Hardware Description Languages, Etc.

INDUSTRY KNOWLEDGE

Analog Circuit Design, Digital Electronics, Microprocessors,

Network Theory, Control System, Process Control Etc.

PROGRAMMING LANGUAGES

C; Verilog, Python, Matlab, Assembly Language, Embedded C

IDEs

Xilinx Vivado, NI Multisim, Octave, Cadence Virtuoso, Keil Uvision, LabVIEW

INTERPERSONAL SKILLS

Communication, Empathy, Flexibility, Leadership, Patience, Self-learning

PROBLEM-SOLVING SKILLS

Attention To Detail, Collaboration, Patience, Research

COMMUNICATION SKILLS

Active Listing, Interpersonal Communication, Verbal/Nonverbal Communication, Written Communication

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EDUCATIODIPLOMA

NINSTITUTE NAME:- K H Kabbur Institute Of Technology, Vidyagiri, Dharwad

BOARD:- Department Of Technical Education Bangalore

FIELD OF STUDY:- Electronics And Communication

YEAR OF COMPLETED:- Dec 2018

PERCENTAGE SECURED:- 63.52%

BACHELOR'S OF ENGINEERING

INSTITUTE NAME:- Kls's Vishwanath Rao Deshpande Institute Of Technology, Haliyal

BOARD:- Visvesvaraya Technological University

FIELD OF STUDY: - Electronics And Communication

YEAR OF COMPLETED: - Aug 2022

PERCENTAGE SECURED: - 69.2% & CGPA SCORED: - 7.27/10

INTERNSHI INSTITUTE NAME:- Haegl Technologies Pvt Ltd, Dharwad

P AREA: - Artificial Intelligence And Internet Of Things

TIME PERIOD: - 01st September 2021 To 30th September 2021

LINK:- https://github.com/naveenub/resume/blob/main/img 20220315 101240.jpg

WORK Technical Support Associate,

HISTORY ZKTeco International Technical Support Center, Bangalore, Karnataka

Period:- Oct 2022 - Present

CERTIFICA 1) Design of VLSI System and Verification of Digital Circuits using Simulation Tool

TES Link:-

https://github.com/Naveenub/resume/blob/main/7th%20sem%20addon%20certificate.pdf

UDEMY 1) Verilog for an FPGA Engineer with Xilinx Vivado Design Suite CERTIFICA Link:-

TES

https://github.com/Naveenub/resume/blob/main/7th%20sem%20addon%20certificate.pdf

2) Verilog HDL VLSI Hardware Design Comprehensive Masterclass.pdf

Link:-

https://github.com/Naveenub/Udemy-

 $\underline{Certificates/blob/main/Verilog\%20HDL\%20VLSI\%20Hardware\%20Design\%20Comprehensive\%20Masterclass.pdf}$

3) FPGA Turbo Series-Communication Protocol

Link:-

https://github.com/Naveenub/Udemy-Certificates/blob/main/FPGA%20Turbo%20Series-Communication%20Protocol.pdf

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PROJECTS 1. Blinking LED using 555 timers

- This is a simple circuit designed to explain the working and use of a 555 timer IC. This circuit is designed using a low power consumption output device, a red LED.
- There are many applications of 555 timers, generally used in Lamp Dimmer, Wiper Speed control, Timer Switch, Variable duty cycle fixed frequency oscillator, PWM Modulation etc.

2. A stable Multivibrator (Mini-Project @ Diploma)

- The Astable Multivibrator is another type of cross-coupled transistor switching circuit that has NO stable output states as it changes from one state to the other all the time.
- The Astable circuit consists of two switching transistors, a cross coupled feedback network, and two time delay capacitors which allows oscillation between the two states with no external triggering to produce the change in state.

3. Smart Train System (Final Year Project @ Diploma)

- In the present days the field of electronics has taken a lot of developments and the security services in the presence and the absence of manpower.
- The main aim of this application is to save energy in railway platforms. There is a provision for sensing the day/night using LDR.
- In the night times one of the ac appliances is in ON position and whenever the train is approaching the station, and then remaining appliances will switch on automatically according to the embedded application designed.
- This project is intended to design a system that supports wireless communication-using RF. The data will be transmitted over the free space (Channel), is received by another device.

4. Heart Beat Sensor using Arduino (Mini-Project @ Engineering)

- Heartbeat sensor is an electronic device that is used to measure the heart rate i.e. speed of the heartbeat.
- Monitoring body temperature, heart rate, and blood pressure are the basic things that we do in order to keep us healthy.
- In order to measure body temperature, we use thermometers and a sphygmomanometer to monitor the arterial pressure or blood pressure.
- Heart rate can be monitored in two ways: one way is to manually check the pulse either at wrists or neck and the other way is to use a heartbeat sensor.

5. IOT based Industry Security Automation using Raspberry Pi

(Final year Project @ Engineering)

- To control and monitor of different activities focused by Present innovations in technology. To reach the human needs these are increasingly emerging.
- To monitor and assess the conditions in case of exceeding the prescribed level of parameters (e.g., CO, Smoke, temperature and Humidity levels) an efficient environmental monitoring system is needed.

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- In an environment when an object equipped with sensor devices, then in this case
 microcontroller and various software application becomes a self-defending, Selfmonitoring and self-controlling environment and it is also called as smart environment.
- The embedded system enables the user to remotely access the various parameters and store the data in cloud and this system is an integration of sensor devices with wireless communication.

COMMUNI Excellent written and verbal communication skills, Confident and professional speaking CA abilities, Empathic listener and persuasive speaker.

TION

LEADERSH Collaborating effectively, cultivating team orientation among staff/Colleagues, Decision-IP Making, Drawing consensus on group goals, Strategic planning.

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