

#5, C/o U. S. Badiger
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Cell No:- 7019346318
<https://github.com/Naveenub/resume/blob/main/Resume.pdf>
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NAVEEN

OBJECTIVE

To pursue a challenging career in your organization which would be mutually beneficial in terms of learning experience and contribution to organizational growth.

SKILLS & ABILITIES

Technical Knowledge:-

Analog and Digital IC Design, CMOS design with delay, Semiconductor Design, Digital Electronics, Network Theory, Networking Concepts.

Proficient In:-

CADENCE, LABVIEW, MATLAB, ARM Keil Uvision, EAGLE, LINUX

Programming Languages:-

Assembly language, Verilog language, C/C++ language, PLC Programming, Python Programming.

Personal Skill Set:-

Communication, Problem solving, Teamwork, Managing Time, Co-ordinating, Creativity, Adaptability, Computer literacy.

EXPERIENCE

FRESHER

EDUCATION

DIPLOMA ----- JSS'S K. H. KABBUR INSTITUTE OF TECHNOLOGY, VIDYAGIRI, DHARWAD

Branch: - Electronics and Communication

Year of Passing: - 2019

Percentage secured: - 63.52%

ENGINEERING ----- KLS'S VISHWANATHRAO DESHPANDE INSTITUTE OF TECHNOLOGY, HALIYAL

Branch: - Electronics and Communication

Year of Passing: - 2022

Currently studying Final Year

CGPA scored: - 7.12(till 7th Semester)

INTERNSHIP ----- HA EGL TECHNOLOGIES PVT LTD, DHARWAD

Area: - Artificial Intelligence and Internet of Things

Time Period: - 01st September 2021 to 30th September 2021

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COMMUNICATION

Excellent written and verbal communication skills, Confident and professional speaking abilities, Empathic listener and persuasive speaker.

PROJECTS

- Blinking LED using 555 timers
 - A stable Multivibrator (Mini-Project @ Diploma)
 - Smart Train System (Final Year Project @ Diploma)
 - Heart Beat Sensor using Arduino (Mini-Project @ Engineering)
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INTERESTS

- Doing small mini projects and Simulations on small circuits
 - Reading scientific and space books
 - Playing in-door games (like Carrom) and few outside games (like cricket, Volleyball, Tennis)
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CERTIFICATES

https://github.com/Naveenub/resume/blob/main/IMG_20220315_101026.jpg