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<https://github.com/Naveenub/resume/blob/main/Resume.pdf>  
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## NAVEEN U BADIGER

**OBJECTIVE** To pursue a challenging career in your organization which would be mutually beneficial in terms of learning experience and contribution to Organizational growth.

**PROFILE SUMMARY** Proficient in designing from scratch, testing FPGA based digital systems and embedded systems with SOC, Verilog design sources, Verilog test bench sources. Possess excellent communication skills and ready to work in a challenging environment.

### SKILLS & ABILITIES

#### **KNOWLEDGE OF SEMICONDUCTOR/VLSI DESIGN**

Fundamentals Of Digital And Analog Circuits, Design Methodologies, Design Flow, Hardware Description Languages, Etc.

#### **INDUSTRY KNOWLEDGE**

Analog Circuit Design, Digital Electronics, Microprocessors, Network Theory, Control System, Process Control Etc.

#### **PROGRAMMING LANGUAGES**

C; Verilog, Python, Matlab, Assembly Language, Embedded C

#### **IDEs**

Xilinx Vivado, NI Multisim, Octave, Cadence Virtuoso, KeilUvision, LabVIEW

#### **PRODUCT SKILLS**

Jira Management, Linux, Xshell.

### EDUCATION

#### **DIPLOMA**

INSTITUTE NAME:- K H Kabbur Institute Of Technology, Vidyagiri, Dharwad

FIELD OF STUDY:- Electronics And Communication

PERIOD OF STUDY:- July 2015 to Dec 2018

PERCENTAGE SECURED:- 73.52%

#### **BACHELOR'S OF ENGINEERING**

INSTITUTE NAME:- Kls's Vishwanath Rao Deshpande Institute Of Technology, Haliyal

FIELD OF STUDY: - Electronics And Communication

PERIOD OF STUDY: - July 2019 to July 2022

PERCENTAGE SECURED: - 71.2% & CGPA SCORED: - 7.52/10

INTERNSHIP	<p>Haegl Technologies Pvt Ltd, Dharwad</p> <p><u>Specialization:-</u> Artificial Intelligence And Internet Of Things</p> <p><u>TIME PERIOD:-</u> 01st September 2021 To 30th September 2021</p>
WORK HISTORY	<p><b><u>Technical Associate Engineer</u></b></p> <p>ZKTeco International Technical Support Center, Bangalore, Karnataka</p> <p><u>Time Period:-</u> Oct 2022 – Present</p>
ACADEMIC CERTIFICATES	<p><b>Design of VLSI System and Verification of Digital Circuits using Cadence Virtuoso Simulation Tool</b></p>
CERTIFICATES	<p><b><u>Udemy Certifications</u></b></p> <ol style="list-style-type: none"> <li>1. Verilog for An FPGA Engineer With Xilinx Vivado Design Suite</li> <li>2. Verilog HDL VLSI Hardware Design Comprehensive Masterclass</li> <li>3. FPGA Turbo Series-Communication Protocol</li> </ol> <p><b><u>Coursera Certifications</u></b></p> <ol style="list-style-type: none"> <li>1. Introduction To Internet Of Things</li> <li>2. Digital Electronics Circuits</li> <li>3. VLSI Physical Design With Timing Analysis (Ongoing)</li> <li>4. CMOS Digital VLSI Design (Ongoing)</li> </ol>
PROJECTS	<ol style="list-style-type: none"> <li>1. Blinking LED using 555 timers</li> <li>2. A stable Multivibrator (Mini-Project @ Diploma)</li> <li>3. Smart Train System (Final Year Project @ Diploma)</li> <li>4. Heart Beat Sensor using Arduino (Mini-Project @ Engineering)</li> <li>5. IOT based Industry Security Automation using Raspberry Pi (Final year Project @ Engineering)</li> </ol>
COMMUNICATION	<p>Excellent written and verbal communication skills, Confident and professional speaking abilities, Empathic listener and persuasive speaker.</p>
LEADERSHIP	<p>Collaborating effectively, cultivating team orientation among staff/Colleagues, Decision-Making, Drawing consensus on group goals, Strategic planning.</p>