Important Questions

Class: 4 B.Tech

Subject: **COMPUTER SYSTEM AND ITS ARCHITECTURE**

Q.1 Explain micro instruction.

Q.2 Construct Von-Neumann block diagram of computer organization.

Q.3 List Flynn's classifications of computer architecture.

Q.4 Create a common bus system using multiplexer in computer architecture.

Q.5 Evaluate instruction cycle in computer organization.

Q.6 Analyze PSW.

Q.7 Differentiate between RISC vs. CISC

Q.8 Analyze various addressing modes.

Q.9 Compare isolated I/O and memory mapped I/O.

Q.10 Discuss strobe method and handshaking methods for data transfer.

Q.11 Categorize various types of I/O ports.

Q.12 Explain cache memory. Analyze its role in improving the performance of a computer system.

Q.13 Compare SRAM and DRAM.

Q.12 Critically evaluate virtual memory.

Q.13 Explain I/O interfacing.

Q.14 Evaluate interrupt cycle in computer architecture.

Q.15 Describe parallel processing.

Q.16 Describe pipelining.

Q.17 Explain Booth multiplication algorithm with an example.

Q.18 Evaluate various page replacement algorithms.

Q.19 Explain memory hierarchy with it's advantage.

Q.20 Explain flash memory with it's advantages.