







# Naveen A

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## Education


2023 – 2027 **B.E - Electronics Engineering (VLSI Design & Technology),**  
Chennai, India *Rajalakshmi Institute of Technology ; CGPA: 8.79* 


## Experience


01/2025 – 06/2025 **Spinacle Technologies Private Limited, Research and Development Intern**   
Chennai, India


- Developed IoT sensor-based systems using Raspberry pi 5, ESP32 for real-time data monitoring of Gut Health and Integrated Azure Cloud for secure storage and management of data.


## Projects

11/2025 – 12/2025 **Mento Band, Predictive AI Wearable for Sports Injury Prevention**   
An IoT-based dual-wearable system that monitors key bio signals and applies AI-driven analytics for real-time injury prediction and athletic performance optimization.


05/2025 – 06/2025 **Sky-Fi, ESP-Based Paper plane**   
Developing a low-cost ESP8266/ESP32-based plane for real-time with wireless connectivity, sensors, and camera support.


04/2025 – 05/2025 **PiStream, App-Controlled Live Streaming and Audio Analysis System**   
Designed for smart surveillance and IoT applications, the system combines real-time multimedia capture with cloud-based data processing, all controlled through a mobile interface


04/2024 – 05/2025 **Digital Circuits Simulations, CMOS-Based Digital Logic Circuit Simulations**   
Designed and simulated basic and sequential circuits using CMOS, including logic gates, SR latch, and a 4-bit memory unit built with flip-flops to explore digital logic design.

02/2025 – 04/2025 **Muscle-Sync, Real-Time EMG-Based Control System**   
A real-time control system that uses the Electromyography (EMG) Signal to translate Muscle Activity into the game commands. Combining signal processing and AI for accurate, hands-free interaction, enabling immersive and intuitive gameplay.

## Achievements & Leadership





16/04/2025 **Best Overall Performer Award, Rajalakshmi Institute of Technology** 

11/03/2025 **Conducted PCB Workshop, Rajalakshmi Institute of Technology** 

26/08/2025 **Participation – UPS Technology Day, UPS Technology Groups** 

## Technical Skills & Achievements

### Certificates

Digital Circuits (NPTEL)   
Microprocessor and Microcontroller (NPTEL)   
System design through Verilog (NPTEL)   
Embedded for Beginner (NIELIT) 






### Area of Expertise

IoT , Embedded System , Digital Electronics , PCB Design ,  
Microprocessor and Microcontroller , 3D modeling

### Software

C, C++, Python, LT Spice, EasyEDA , Verilog

### Hackathon & Expo

Smart India Hackathon  (Finalist)  
Lam Research talent Challenge   
Smart Innovators Hackathon   
Circuit Design   
Techgium   
Young Innovators Project Expo 