CHANAKYA UNIVERSITY

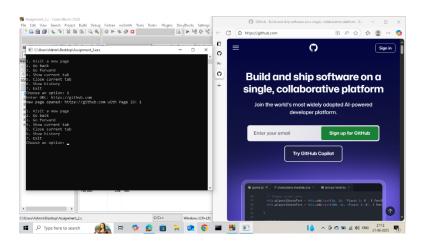
Data Structures and Algorithms

NAME: Navyashree R

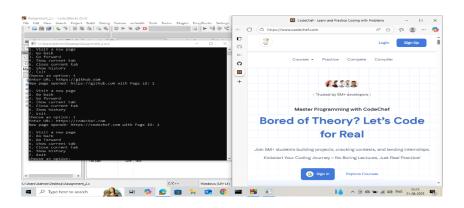
Branch: Electronics Engineering (VLSI and Embeded Systems)

Reg no: 24UG00449

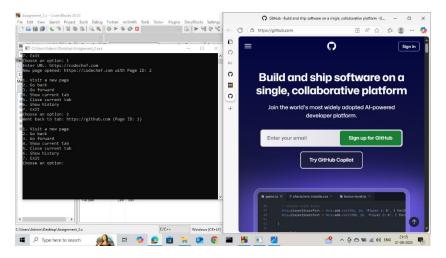
Ouput 1:



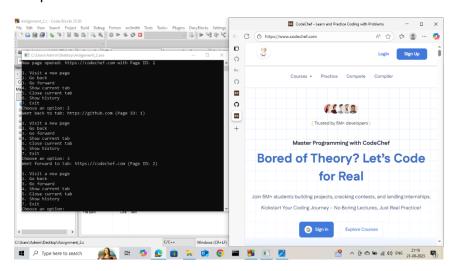
Output 2:



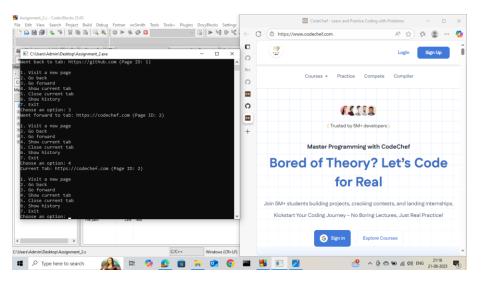
Output 3:



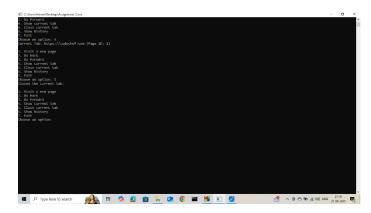
Output 4:



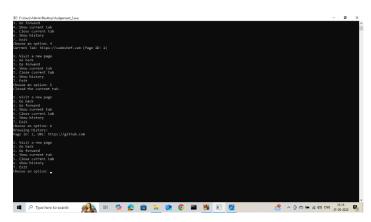
Output 5:



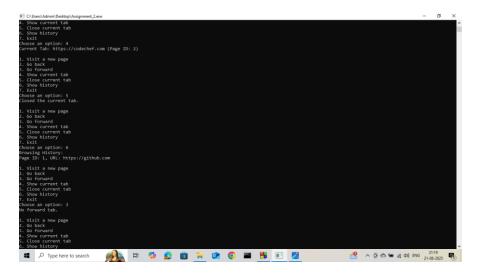
Output 6:



Output 7:



Output 8:



Output 9:

