

# Computer Architecture

Unit	Course Topics	Hours
1	<b>Introduction to Computer Architecture:</b> Basic organization, instruction cycle, Von Neumann architecture, performance measures (MIPS, CPI, throughput)	4L
2	<b>Data Representation &amp; Arithmetic:</b> Number systems, integer & floating-point representation (IEEE 754), addition, subtraction, multiplication, division, ALU design	6L
3	<b>Instruction Set Architecture (ISA):</b> Types of instructions, addressing modes, RISC vs CISC, instruction formats, stack, accumulator & register-based architectures	6L
4	<b>Processor Architecture:</b> CPU organization, control unit (hardwired & microprogrammed), pipelining, pipeline hazards, superscalar and VLIW architectures	6L
5	<b>Memory System:</b> Memory hierarchy, cache memory (mapping, replacement, write policies), virtual memory, main memory, secondary storage	5L
6	<b>I/O Organization:</b> I/O techniques (interrupts, DMA, polling), bus architecture, I/O devices, peripherals, data transfer modes	4L
7	<b>Advanced Computer Architectures:</b> Multiprocessors, multicores, parallel processing, GPU basics, SISD/MISD/SIMD/MIMD models	4L