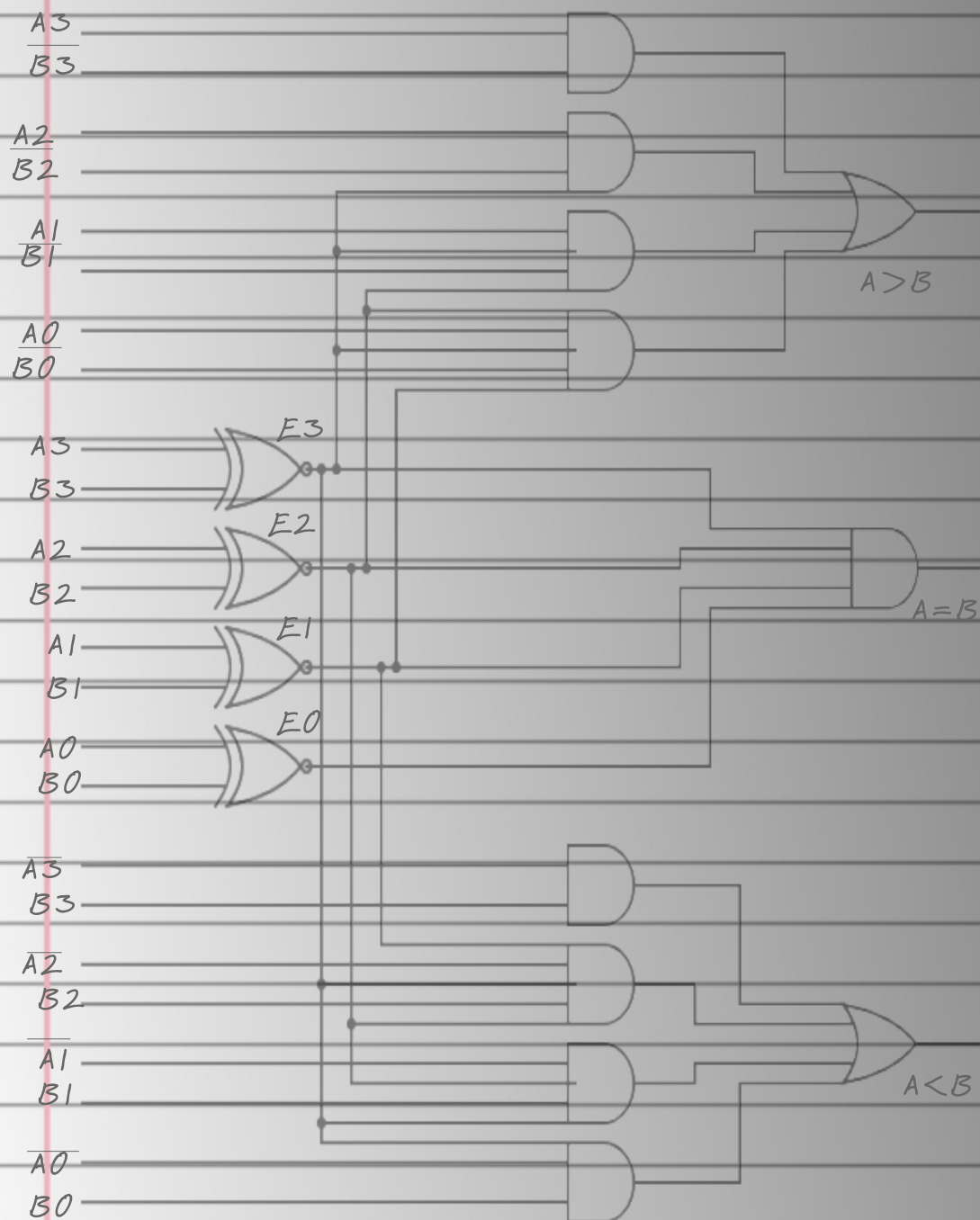


Practical-5

Design a 4 bit magnitude comparator using combinational circuits and truth table.



$A = A_3 A_2 A_1 A_0$

$B = B_3 B_2 B_1 B_0$

Truth Table :

Comparing Inputs				Output		
A_3B_3	A_2B_2	A_1B_1	A_0B_0	$A > B$	$A < B$	$A = B$
$A_3 > B_3$	x	x	x	1	0	0
$A_3 < B_3$	x	x	x	0	1	0
$A_3 = B_3$	$A_2 > B_2$	x	x	1	0	0
$A_3 = B_3$	$A_2 < B_2$	x	x	0	1	0
$A_3 = B_3$	$A_2 = B_2$	$A_1 > B_1$	x	1	0	0
$A_3 = B_3$	$A_2 = B_2$	$A_1 < B_1$	x	0	1	0
$A_3 = B_3$	$A_2 = B_2$	$A_1 = B_1$	$A_0 > B_0$	1	0	0
$A_3 = B_3$	$A_2 = B_2$	$A_1 = B_1$	$A_0 < B_0$	0	1	0