

**Logisim:** 4-bit Binary Multiplication Unit

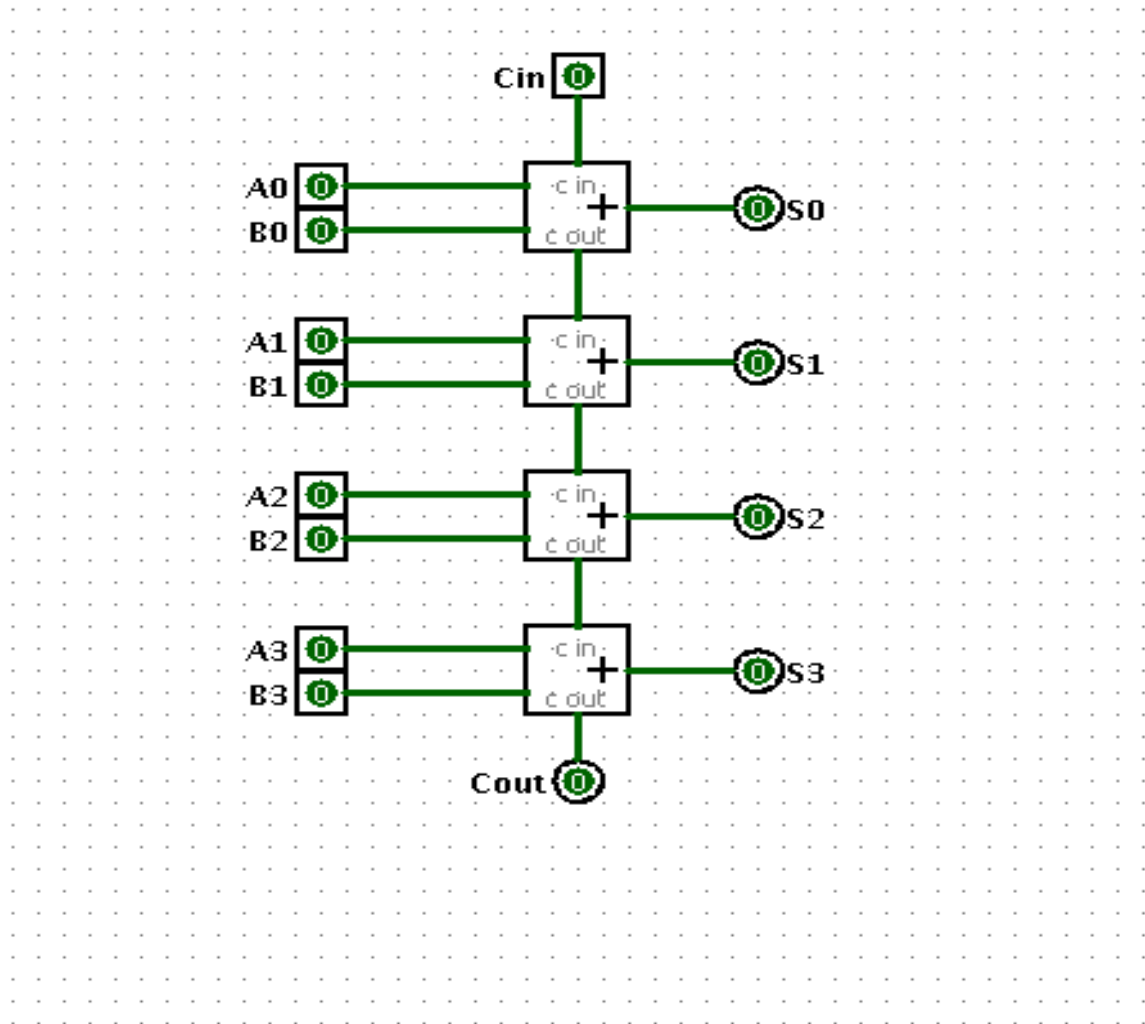


Figure: 4 Bit Binary Adder

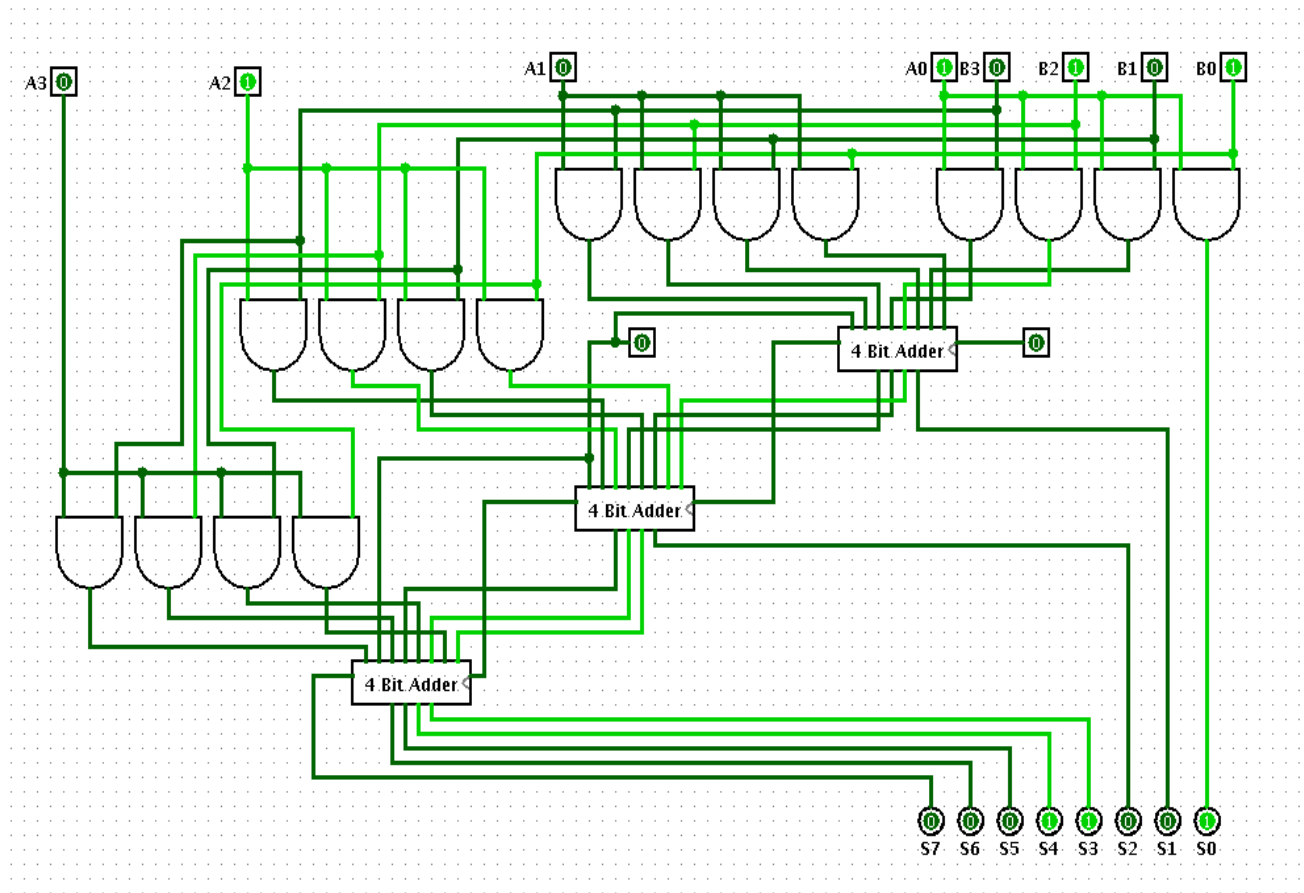


Figure: 4 Bit Binary Multiplication Unit

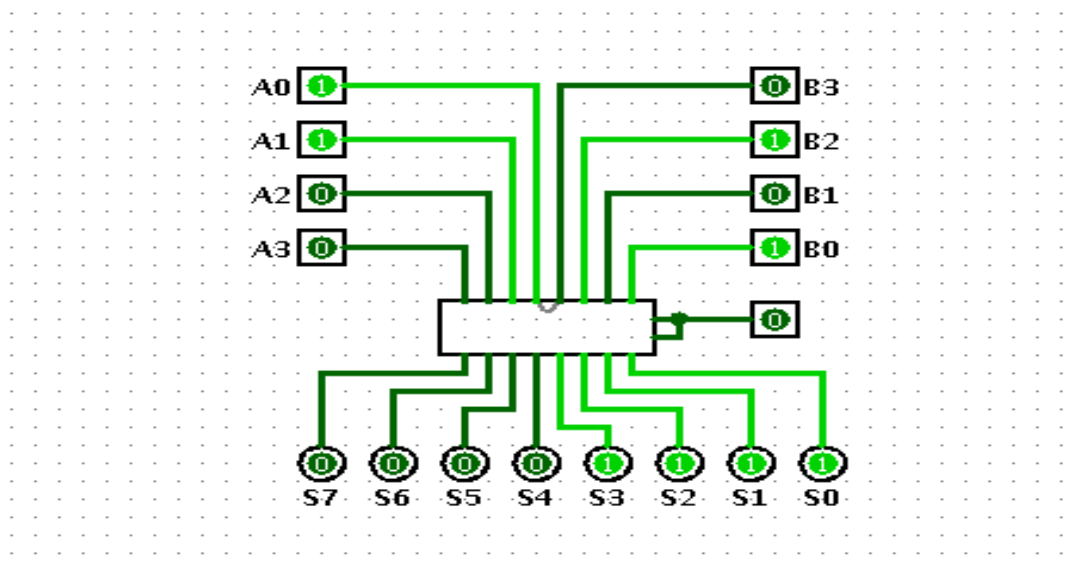


Figure: 4 Bit Binary Multiplication Unit (Main Circuit)