|  |  |
| --- | --- |
| Memory address of RAM | DATA in that address |
| 0000 | 0000 1000 (load the data of address 1000) |
| 0001 | 0001 1001 (add the data of address 1001 with accumulator’s data) |
| 0010 | 0010 1010 (subtract the data of address 1010 from accumulator’s data) |
| 0011 | 0011 0000 (show output; data will go to output regi from accu) |
| 0100 | 1111 0000 (HALT) |
| 0101 |  |
| 0110 |  |
| 0111 |  |
| 1000 | 0000 0110 (in decimal 6; will be loaded to accu) |
| 1001 | 0000 0001 (in decimal 1; will go to B regi and be added with 6; hence will become 7 and will be stored at accu) |
| 1010 | 0000 0100 (in decimal 4; will go to B regi and be subtracted from 7; hence will become 3 and will be stored at accu)) |
| 1011 |  |
| 1100 |  |
| 1101 |  |
| 1110 |  |
| 1111 |  |