FPGA In Reliability and Safety Critical Application

Neaz Mahmud
Electronic Engineering
Hochschule Hamm Lippstadt
Lippstadt , Germany
Neaz.mahmud@stud.hshl.de

***Abstract*—**

[**?**], [1]–[6]

References

[1] Francisco Cardells-Tormo, Javier Valls-Coquillat, Vicenc Almenar-Terre, and Vicente Torres-Carot. Efficient fpga-based qpsk demodulation loops: Application to the dvb standard. In *International Conference on Field Programmable Logic and Applications*. Springer, 2002.

[2] Henrik Christopherson, Wayne Pickell, Adrian Koller, Suresh Kannan, and Eric Johnson. Small adaptive flight control systems for uavs using fpga/dsp technology. In *AIAA 3rd" Unmanned Unlimited" Technical Conference, Workshop and Exhibit*, page 6556, 2004.

[3] Radek Dobias and Hana Kubatova. Fpga based design of the railway's interlocking equipments. In *Euromicro Symposium on Digital System Design, 2004. DSD 2004.*, pages 467–473. IEEE, 2004.

[4] Antonino Mazzeo, Luigi Romano, Giacinto Paolo Saggese, and Nicola Mazzocca. Fpga-based implementation of a serial rsa processor. In *2003 Design, Automation and Test in Europe Conference and Exhibition*. IEEE, 2003.

[5] Jingke She and Jin Jiang. Application of fpga to shutdown system no. 1 in candu. In *6th American Nuclear Society International Topical Meeting on NPIC&HMIT, April, Knoxville, Tennessee, USA*, 2009.

[6] Luca Sterpone and Massimo Violante. Analysis of the robustness of the tmr architecture in sram-based fpgas. *IEEE Transactions on Nuclear Science*, 52(5):1545–1549, 2005.