

1	2	3	4	5	6	7	8																					
A	<div>Designator [01] - COVER PAGE.SchDoc</div> <div>Designator [02] - BLOCK DIAGRAM.SchDoc</div> <div>Designator [03] - 28PINS.SCHEMATIC.SchDoc</div> <div>Designator [04] - REVISION HISTORY.SchDoc</div>							A																				
B								B																				
C								C																				
D	<div><div>NOTES</div><div>Mark Not Fitted Components as NF</div><div>DRAFT - Very early stage of schematic, ignore details.</div><div>PRELIMINARY - Close to final schematic.</div><div>CHECKED - There should not be any mistakes. Tell the engineer if you find one.</div><div>RELEASED - A board with this schematic has been sent to production.</div></div> <div><table><tr><td colspan="4">http://www.fedevel.com/academy</td></tr><tr><td colspan="2">Title:</td><td colspan="2">Variant: [No Variations]</td></tr><tr><td colspan="2">Page Contents:</td><td>28Pins_Project_V111 Project.SchDoc</td><td>Checked by</td></tr><tr><td>Size:</td><td colspan="2">DWG NO</td><td>Revision:</td></tr><tr><td>Date:</td><td>2022-02-04</td><td>Designed by www.fedevel.com</td><td>Sheet 1 of 4</td></tr></table></div>							http://www.fedevel.com/academy				Title:		Variant: [No Variations]		Page Contents:		28Pins_Project_V111 Project.SchDoc	Checked by	Size:	DWG NO		Revision:	Date:	2022-02-04	Designed by www.fedevel.com	Sheet 1 of 4	D
http://www.fedevel.com/academy																												
Title:		Variant: [No Variations]																										
Page Contents:		28Pins_Project_V111 Project.SchDoc	Checked by																									
Size:	DWG NO		Revision:																									
Date:	2022-02-04	Designed by www.fedevel.com	Sheet 1 of 4																									
1	2	3	4	5	6	7	8																					

2022-02-04

Variant: [No Variations]

RELEASED 04-02-2022

Page	Index	Page	Index	Page	Index	Page	Index
1	COVER PAGE	11		21		31	
2	BLOCK DIAGRAM	12		22		32	
3	28PINS SCHEMATIC	13		23		33	
4	REVISION HISTORY	14		24		34	
5		15		25		35	
6		16		26		36	
7		17		27		37	
8		18		28		38	
9		19		29		39	
10		20		30		40	

Cannot open file 28Pins3D.png. File does not exist.

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational
design notes.

DESIGN NOTE:
Example text for cautionary
design notes.

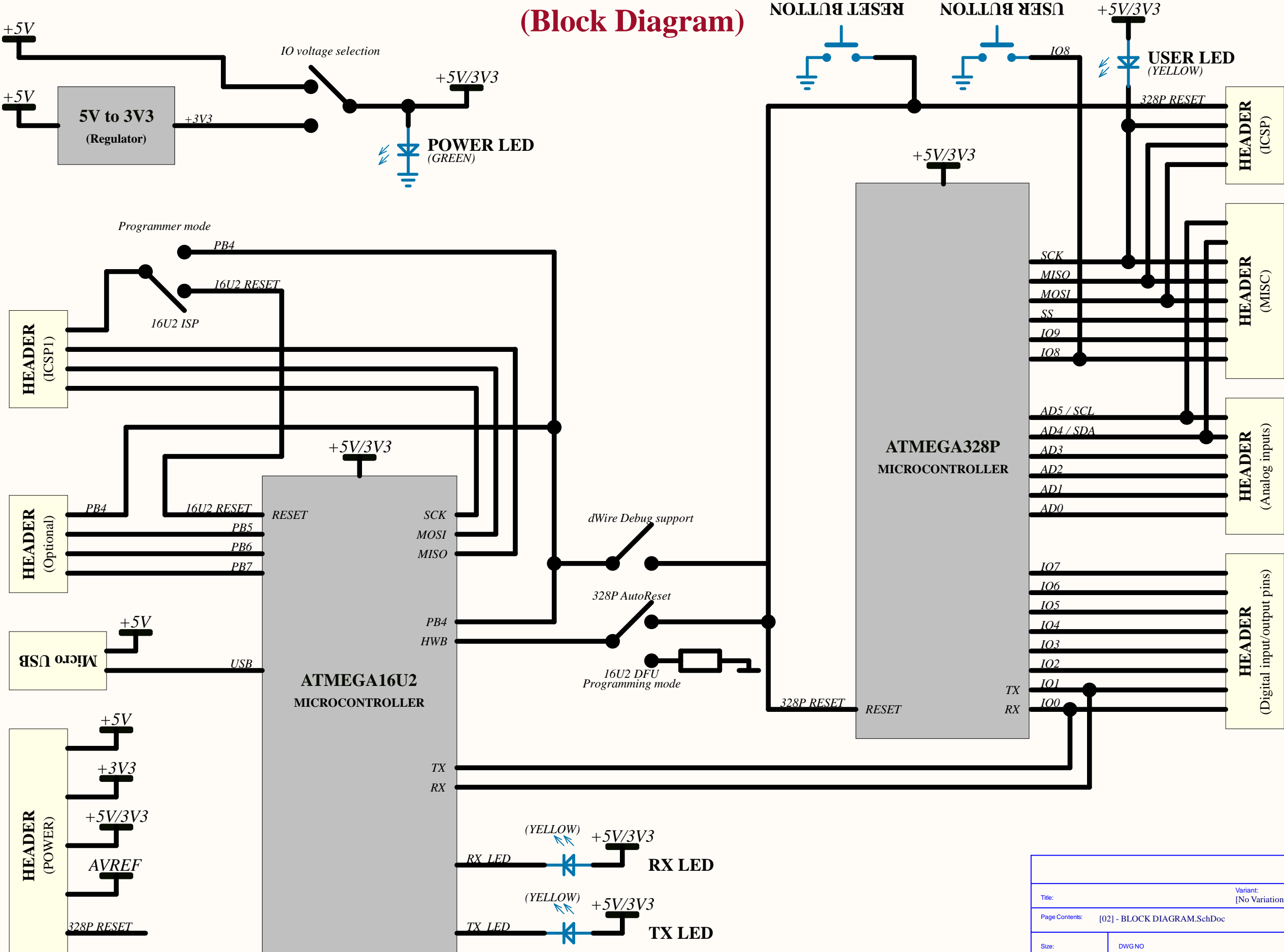
DESIGN NOTE:
Example text for debug notes.

DESIGN NOTE:
Example text for critical
design notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.

Title:		Variant: [No Variations]	
Page Contents:		[01] - COVER PAGE.SchDoc	
Checked by			
Size:	DWG NO	Revision:	
Date: 2022-02-04		Sheet 2 of 4	

(Block Diagram)



Title:			
Variant:			
[No Variations]			
Page Contents:			
[02] - BLOCK DIAGRAM.SchDoc			
Size:		Revision:	
DWG NO			
Date:			Sheet 3 of 4
2022-02-04			

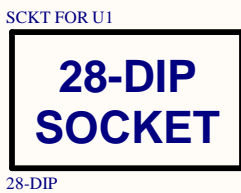
1	2	3	4	5	6	7	8	
REVISION HISTORY								A
								B
								C
								D
1	2	3	4	5	6	7	8	

http://www.fedevel.com/academy			
Title: [No Variations]			
Page Contents: [04] - REVISION HISTORY.SchDoc		Checked by	
Size:	DWG NO		Revision:
Date:	2022-02-04		Sheet 4 of 4

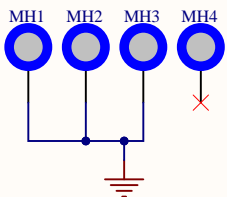
ARDUINO SCHEMATIC

By Neel Bullywon

DIP SOCKET



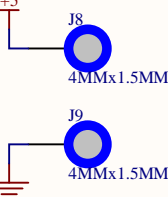
MOUNTING HOLES



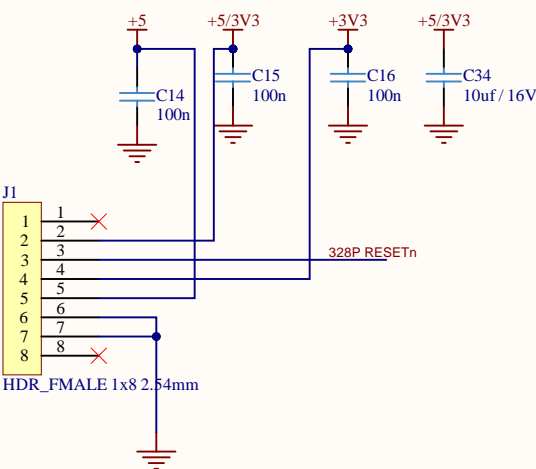
FIDUCIALS



POWER PADS

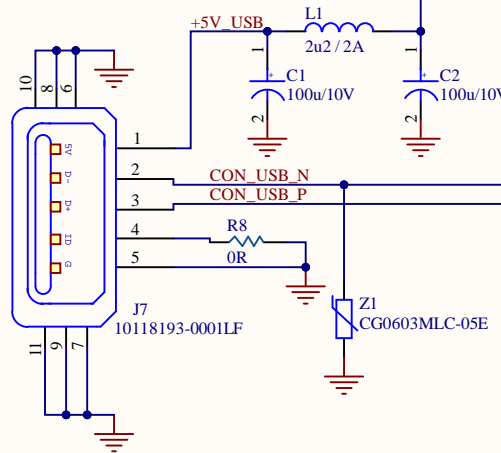


POWER

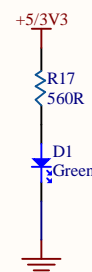


Title		
Size	Number	Revision
A2		
Date:	2-04-2022	Sheet of
File:	C:\Users\...[03] - 28PINS SCHEMATIC.SchDoc	By:

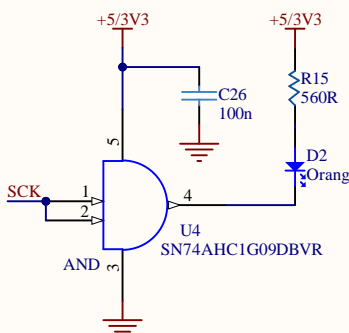
MICRO USB



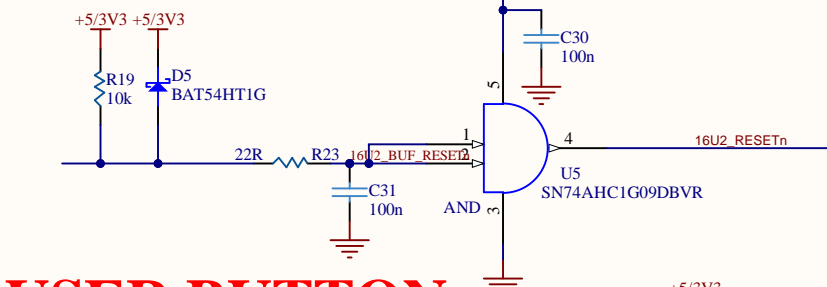
POWER LED



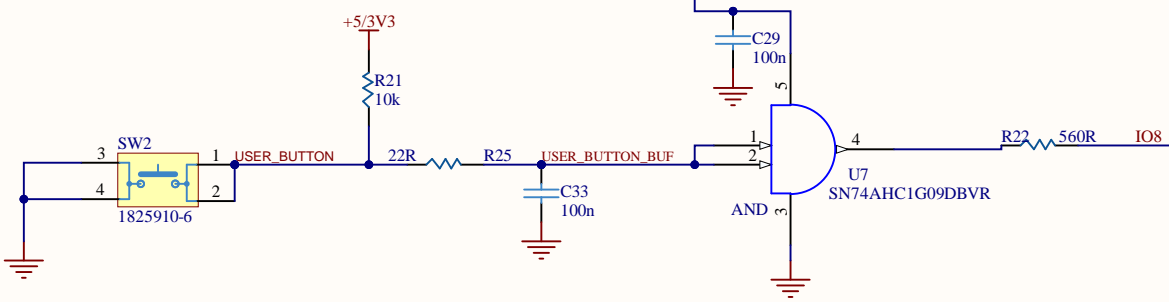
USER LED



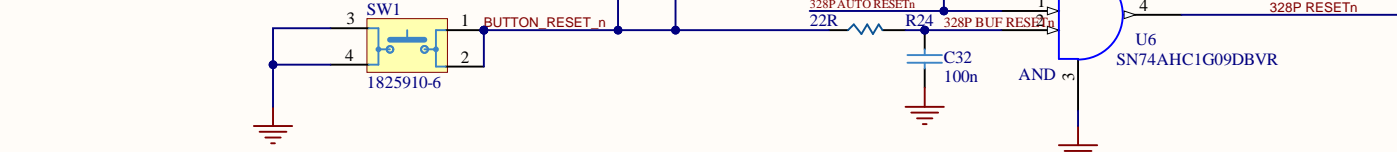
RESET (16U2)



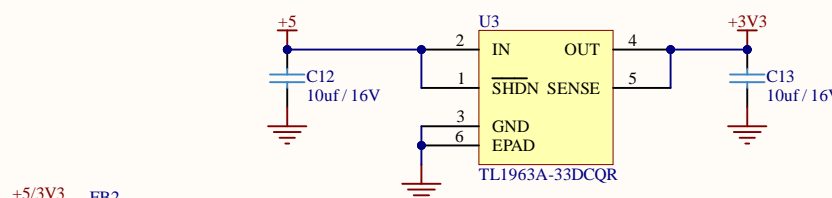
USER BUTTON



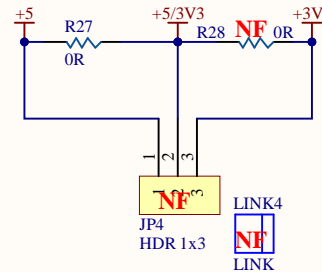
RESET (328P)



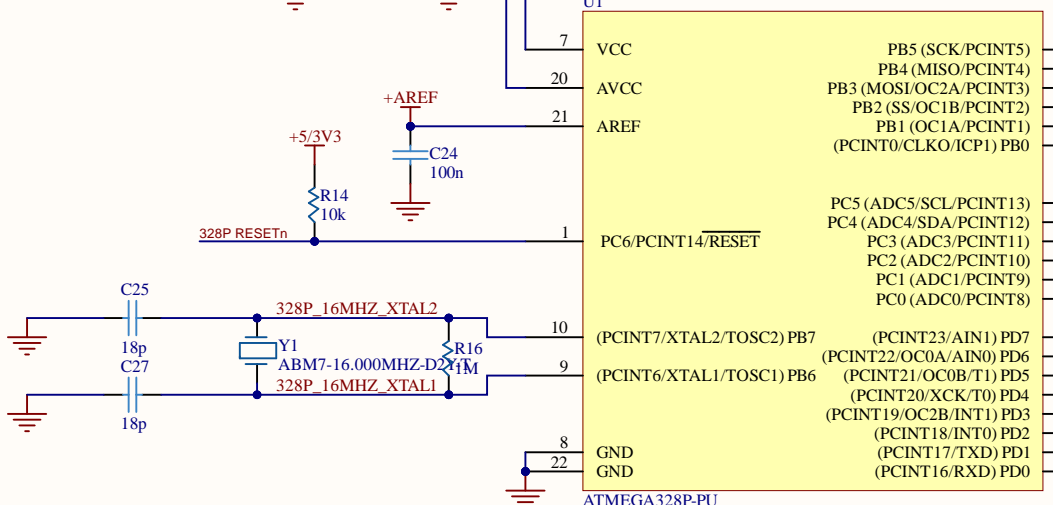
3V3 LDO



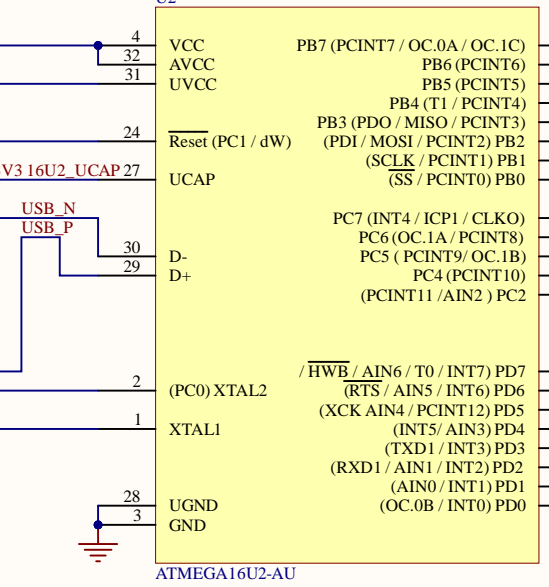
Power Selection



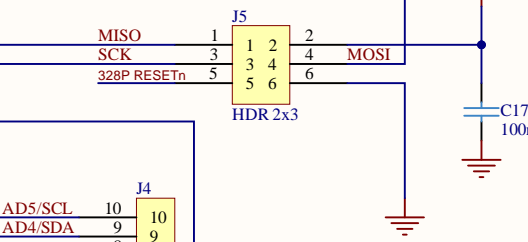
328P



16U2



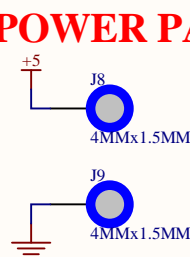
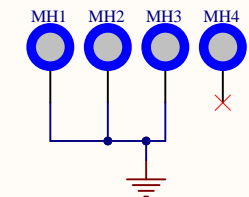
ICSP



IOH

AD

IOL



POWER

