ASSIGNMENT – 5

PERFORMANCE PROFILING WITH PERF

REPORT

AIM:-

• To analyze the performance of two matrix multiplication algorithms [Naïve multiplication and Blocked multiplication] in terms of instructions, cache references, cache misses/miss-rate, L1-dcache-misses/miss-rate, L1-icache-misses/miss-rate using PERF.

TOOL:- PERF

PROCEDURE:-

- First, we design two different algorithms for matrix multiplication i) Naïve multiplication ii) Blocked multiplication.
- In Naïve method, we do the matrix multication as we do on paper.
- In Blocked method, we use the concept of divide and conquer where we divide the matrix into matrices of required block size and multiply blockwise.
- The implementation of both algorithms is done in "template.cpp".

- Now, for testing, we generate input square matrices of any size containing random values in "gen_input.cpp".
- This can be done using commands:-

```
$g++gen_input.cpp
```

\$./a.out 128 input128

• Now, "template.cpp" can be run by:-

```
$ g++ template.cpp
```

\$./a.out block_size inputfile outputfile

Note:- Here, blocksize is '0' means we are using naïve algorithm or else the blocked algorithm of given blocksize.

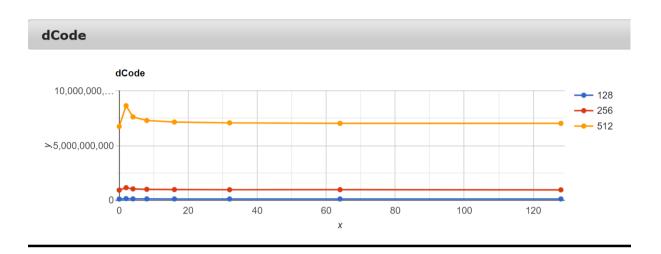
- As the output file is ready, we can now use perf for analyzing the performance by using the command:-
- "sudo perf stat -e instructions, cachereferences, cache-misses, L1-dcache-loads, L1dcache-load-misses, L1-icache-loads, L1-icacheload-misses, LLC-loads, LLC-load-misses -r runs ./a.out block_size inputfile outputfile"
- Here, this command displays the required stats only and calibrated as an average of the given number of runs.

OBSERVATIONS:-

• We analyze the performance of both algorithms for blocksizes '128', '256', '512'.

• Note that the result data sheets are attached at the end for reference.

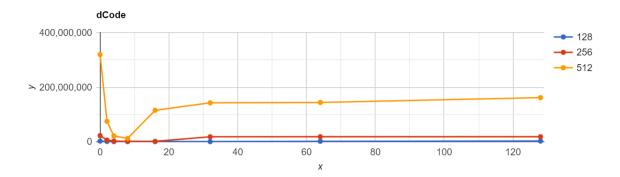
1.) INSTRUCTIONS:-



- From the graph, it is clear that the number of instructions for blocked algorithm is always greater than or equal to the naïve algorithm.
- Moreover, the instructions number decreases as the block size increases.
- This observation is consistent for all three sizes.

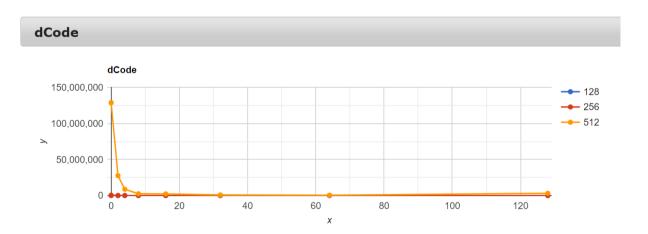
2.) CACHE REFERENCES:-

dCode



- From the graph, it is clear that the cache references for blocked algorithm are less than that of the naïve algorithm.
- Moreover, the number decreases till a certain block size and then increases thereafter.
- Here, the number decreases till blocksize '16' for matrix sizes '128', '256' but for matrix size '512', the number decreases till blocksize '8' and then increases.

3.) CACHE MISSES:-

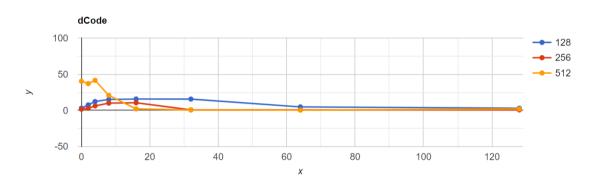


 From the graph, it is clear that the cache misses for blocked algorithm are less than that of the naïve algorithm.

- Moreover, the number decreases as the block size increases (with very - very minor zizag trend).
- Here, this observation is much clearly evident in matrix size – '512' whereas some flaws are seen in matrix size – '128'.

4.) CACHE MISS RATE :-





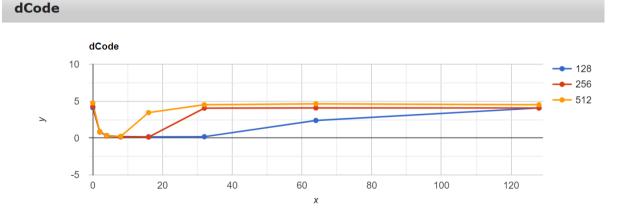
- From the graph, for matrix sizes '128', '256'
 the cache miss rate for blocked algorithm is
 greater than that of the naïve alogorithm and the
 cache miss rate decreases as the block size
 increases.
- o For matrix size − '512' the cache miss rate follows a zigzag trend which is a bit unpredictable. The values drop dramatically at block sizes − '16' to '128'.

5.) L1 DCACHE LOAD MISSES :-

dCode 150,000,000 100,000,000 50,000,000 20 40 60 80 100 120

- o From the graph it is clear that the L1-dcacheload-misses for blocked algorithm are less than or equal to that of the naïve algorithm.
- Moreover, the number decreases till a certain block size and then increases thereafter.
- Here, the number decreases till blocksize '16' for matrix sizes '128', '256' but for matrix size '512', the number decreases till blocksize '8' and then increases.

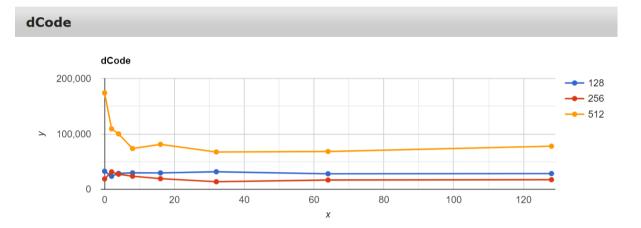
6.) L1 DCACHE MISS RATE :-



 From the graph it is clear that the L1-dcacheload-misses for blocked algorithm are less than or equal to that of the naïve algorithm.

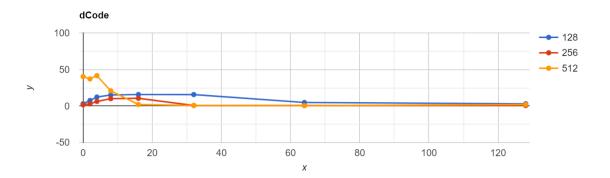
- Moreover, the number decreases till a certain block size and then increases thereafter.
- Here, the number decreases till blocksize '16' for matrix sizes '128', '256' but for matrix size '512', the number decreases till blocksize '8' and then increases.

7.) L1 ICACHE LOAD MISSES :-



- From the graph L1-cache-load-misses for blocked algorithm are less than that of naïve algorithm which is clearly evident in block size – '128', '512'.
- But this trend is inconsistent to block size '256'
 which is a bit unpredictable.

8.) L1 ICACHE MISS RATE :-



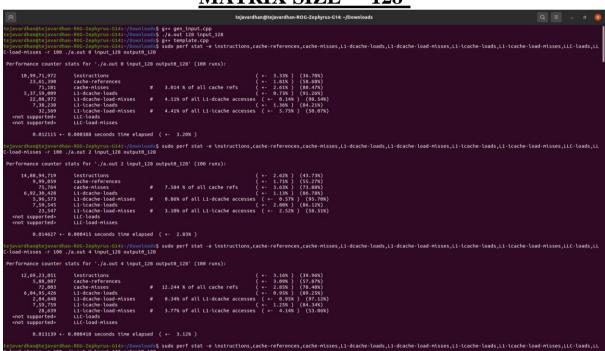
 In this graph, there is no such perfect trend that is being followed.

RESULT:-

- Blocked algorithm is more efficient than the naïve algorithm due to the added advantage of it using the 'PRINCIPLE OF LOCALITY'.
- Here, even though the number of instructions are more for blocked algorithm than the naïve algorithm, many other metrics are quite optimal for blocked algorithm.
- The ideal block size for matrix sizes '128', '256' is '16' and for matrix size '512' is '8' especially due to lower L1-dcache-misses.
- Overall, the ideal block size is '16' considering all metrics of all three sizes.

RESULT DATA SHEETS:-

MATRIX SIZE - '128'



```
tejavardhan@tejavardhan-ROG-Zephyrus-G14: ~/Downloads
  javardhamitejavardham-RDC-Zephyrus-C14:-/Downloads5 sudo perf stat -e instructions,cache-references,cache-misses,L1-dcache-loads,L1-dcache-load-misses,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcac
  erformance counter stats for './a.out 8 input 128 output0 128' (100 runs):
    12,42,10,842 instructions (+- 3.04%) (40.86%)
4,97,600 cache-references (+- 3.69%) (58.31%)
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1,10,77,334 Li-tcache-loaden-tisses (+- 0.25%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.48%) (96.
                                               0.013191 +- 0.000425 seconds time elapsed ( +- 3.22% )
    javardhangtejavardhan-ROG-Zephyrus-ci4:-/Rownloads$ sudo perf stat -e instructions,cache-references,cache-nisses,Li-dcache-loads,Li-dcache-load-nisses,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcac
    11,59,63,245 Instructions (+- 3.23%) (40.21%) (47,6,018 cache-references (+- 4.30%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) (58.94%) 
                                               0.012867 +- 0.000427 seconds time elapsed ( +- 3.32% )
    erformance counter stats for './a.out 64 input 128 output0 128' (100 runs):
       <not supported> LLC-loads
<not supported> LLC-load-misses
  javardhangtejavardhan-noc-Zephyrus-cisz-/Rouminadis sudo perf stat -e instructions,cache-references,cache-nisses,Li-dcache-loads,Li-dcache-load-nisses,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcac
    javardhanqtejavardhan-ROC-Zephyrus-G14:-/Rownloads$ sudo perf stat -e instructions,cache-references,cache-misses,L1-dcache-loads,L1-dcache-load-misses,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcac
    0.014091 +- 0.000494 seconds time elapsed ( +- 3.50% )
  javardhan@tejavardhan-ROG-Zephyrus-G14:-/Downloads$ sudo perf stat -e instructions,cache-references,cache-misses,L1-dcache-loads,L1-dcache-load-misses,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcac
                               rmance counter stats for './a.out 128 input 128 output0 128' (100 runs):
                          11,85,67,121 instructions (+ 3.43%) (39.69%)
24,08,020 cache-references (+ 1.71%) (57.29%)
73,566 cache-reses (+ 1.71%) (57.29%)
5,47,24,340 il-d-cache-load-nisses (+ 2.85%) (67.67%)
22,25,379 il-d-cache-load-nisses (+ 0.28%) (89.14%)
5,87,40,657 il-t-cache-load-nisses (+ 0.28%) (97.69%) (89.14%)
11,t-cache-load-nisses (+ 2.18%) (65.37%)
                                               0.013006 +- 0.000405 seconds time elapsed ( +- 3.11% )
```

MATRIX SIZE - '256'

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tejavardhan@tejavardhan-ROG-Zephyrus-G14: ~/Downloads
  javardhamitejavardham-ROC-Zephyrus-Ci4:-/Bownloads$ sudo perf stat -e instructions,cache-references,cache-misses,Li-dcache-loads,Li-dcache-load-misses,Li-lcache-loads,Li-lcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcac
      erformance counter stats for './a.out 8 input 256 output0 256' (100 runs):
                   99,76,67,088 instructions (+- 0.26%) (66.44%)
13,50,563 cache-references (+- 1.75%) (69.88%)
13,53,769 cache-nisses # 10.011 % of all cache refs (+- 1.75%) (72.55%)
41,35,770,991 L1-dcache-load-nisses # 0.15% of all L1-dcache accesses (+- 0.24%) (74.75%)
52,67,701 L1-dcache-load-nisses # 0.15% of all L1-dcache accesses (+- 1.92%) (75.75%)
12,41,255 L1-dcache-loads # 0.15% of all L1-dcache accesses (+- 3.21%) (73.75%)
of supported- L1-clacke-load-nisses # 1.16% of all L1-tcache accesses (+- 6.11%) (67.44%)
of supported- L1-clacke-load-nisses
                                           0.066888 +- 0.000458 seconds time elapsed ( +- 0.68% )
Javardhandtejavardhan-ROG_Zephyrus-Gisz-/Downloadis sudo perf stat -e instructions,cache-references,cache-misses,Li-dcache-loads,Li-dcache-load-misses,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-load-misses,LiC-loads,Li
                              rmance counter stats for './a.out 16 input_256 output0_256' (100 runs):
    javardhangtejavardhan-noc-Zephyrus-cisz-/Downloads$ sudo perf stat -e instructions,cache-references,cache-nisses,Li-dcache-loads,Li-dcache-load-nisses,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcac
    96,03,47,822 Instructions (+- 0.26%) (68.00%)
1,81,27,807 cache-references
1,81,27,807 cache-nisses # 0.731 % of all cache refs (+- 0.121%) (71.34%)
39,74,29,219 L1-dcache-loads (+- 1.21%) (71.34%)
1,51,09,493 L1-tcache-loads (+- 0.26%) (74.42%)
13,038 L1-tcache-loads (+- 2.81%) (71.64%)
30,74,29,219 L1-dcache-loads (+- 0.26%) (74.42%)
13,038 L1-tcache-loads (+- 2.81%) (71.64%)
4.05% of all L1-tcache accesses (+- 11.45%) (67.16%)
  javardhan@tejavardhan-ROG-Zephyrus-Gi4:-/Ownitoads$ sudo perf stat -e instructions,cache-references,cache-nisses,L1-dcache-loads,L1-dcache-load-nisses,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-load-nisses,LLC-loads,LL0-loads,LL0-loads,LL0-loads,LL0-loads,LL0-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lc
                              rmance counter stats for './a.out 64 input 256 output0 256' (100 runs)
      <not supported> LLC-loads
<not supported> LLC-load-misses
  javardhangtejavardhan-ROG-Zephyrus-Gidz-/Bownloads$ sudo perf stat -e instructions,cache-references,cache-nisses,L1-dcache-loads,L1-dcache-load-nisses,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcache-loads,L1-lcac
erformance counter stats for './a.out 64 input_256 output0_256' (100 runs):
    0.063673 +- 0.000367 seconds time elapsed ( +- 0.58% )
    lavardhamstejavardham-855-Zephyros-615:-/Downloads5 sudo perf stat -e instructions,cache-references,cache-misses,Li-dcache-loads,Li-dcache-load-misses,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcac
                                  mance counter stats for './a.out 128 input_256 output0_256' (100 runs):
    96,06,89,081 Instructions (+- 0.32%) (67.24%)
1,83,66,080 cache-references
1,20,190 cache-nisses # 0.654 % of all cache ref (+- 0.71%) (79.94%)
39,45,63,336 L1-dcache-load-nisses
1,06,51,447 L1-dcache-load-nisses
1,56,144 L1-dcache-load-nisses
1,56,144 L1-dcache-load-nisses
1,10,50,144 L1-dcache-accesses (+- 0.27%) (66.93%)
1,10,50,144 L1-dcache-accesses (+- 9.02%) (66.93%)
1,10,50,144 L1-dcache-load-nisses
1,10,50,144 L1-dcache-load-nisses
1,10,50,144 L1-dcache-accesses (+- 9.02%) (66.93%)
  javardhangtejavardhan-RGG-Zephyrus-Gis:-/Gownloads$ sudo perf stat -e instructions,cache-references,cache-misses,Li-dcache-loads,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-dcache-load-misses,Li-
                                               nce counter stats for './a.out 256 input_256 output0_256' (100 runs):
    94,87,80,103 Instructions (+- 0.23%) (68.23%)
2,30,19,105 cache-references
3,37,7,49 cache-nisses # 1.640 % of all cache refs (+- 2.20%) (72.23%)
39,51,19,734 L1-dcache-loads | # 1.460 % of all cache refs (+- 9.02%) (73.29%)
1,61,20,331 L1-dcache-loads | # 4.08% of all L1-dcache accesses (+- 0.17%) (74.08%)
10,673 L1-tcache-loads | # 4.08% of all L1-tcache accesses (+- 1.27%) (72.23%)
10,673 L1-tcache-load-nisses | # 0.66% of all L1-tcache accesses (+- 1.20.2%) (66.76%)
which supporteds | L1-cloads | L1-cloads | # 0.66% of all L1-tcache accesses (+- 1.20.2%) (66.76%)
```

MATRIX SIZE - '512'

```
:$ g++ gen_input.cpp
$ ./a.out 512 input.512
$5 g++ template.cpp
«$ sudo perf stat -e instructions,cache-references,cache-misses,l1-dcache-loads,l1-dcache-load-misses,l1-icache-load-misses,l1C-loads,l1
                                                                                       unter stats for './a.out 0 input 512 output0 512' (100 runs):
                                                                                                                                   Instructions (+- 0.85%) (71.18%) cache-references (+- 0.12%) (71.28%) cache-references (+- 0.12%) (71.32%) cache-rioseds (+- 0.12%) (71.28%) (1.14cache-loads) (+- 0.85%) (71.55%) (1.14cache-loads) (+- 0.85%) (71.55%) (1.14cache-loads) (+- 1.22%) (71.55%) (1.14cache-loads-misses (+- 1.23%) (71.56%) (1.14cache-loads-misses (+- 1.23%) (71.26%) LIC-loads-misses
  invaridismitts juvaridism. RDC. Zaphyrou. C184-/Downloads5 sudo perf stat -e instructions, cache-references, cache-misses, L1-dcache-loads, L1-dcache-load-misses, L1-tcache-loads, L1-tcache-loa
                                       .00.100.602 Instructions (+-0.09%) (71.01%)
.47.42.275 cache-references (+-0.30%) (71.20%)
.76,74,003 cache-nisses # 37.026 % of all cache refs (+-1.09%) (71.40%)
.45,23,986 L1-dcache-loads
.1-dcache-loads (+-0.03%) (71.61%)
.25,39,378 L1-tcache-loads
.25,39,378 L1-tcache-loads
.26,39,378 L1-tcache-loads
.26,39,378 L1-tcache-loads
.26,39,378 L1-tcache-loads
.27,99,241 L1-tcache-loads
.27,99,241 L1-tcache-load-nisses
.27,99,241 L1-tcache-load-nisses
.28,39,378 L1-tcache-load-nisses
.28,39,378 L1-tcache-load-nisses
.29,000 file L1-tcache-load-nisses
.20,000 file L1-tcache
  javardhangtejavardhan-ROG-Zephyrus-C14:-/Downloads$ sudo perf stat -e instructions,cache-references,cache-misses,L1-dcache-loads,L1-dcache-load-misses,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcache-loads,L1-tcac
                                mance counter stats for './a.out 4 input_512 output0_512' (100 runs):
          7,61,69,61,655 Instructions (+- 0.03%) (71.16%)
2,11,44,183 cache-references (+- 0.12%) (71.26%)
87,90,652 cache-insees # 41.575 % of all cache refs (+- 0.14%) (71.28%)
1,10,03,422 L1-dcache-load-nisses # 0.33% of all L1-dcache accesses (+- 0.14%) (71.28%)
1,10,03,422 L1-dcache-load-nisses # 0.33% of all L1-dcache accesses (+- 0.14%) (71.28%)
1,10,051 L1-tcache-load-nisses # 0.33% of all L1-dcache accesses (+- 0.15%) (71.48%)
1,00,051 L1-tcache-load-nisses # 1.02% of all L1-tcache accesses (+- 1.15%) (71.48%)
1,10,051 L1-tcache-load-nisses # 1.02% of all L1-tcache accesses (+- 1.15%) (71.48%)
1,10,051 L1-tcache-load-nisses # 1.02% of all L1-tcache accesses (+- 1.15%) (71.48%)
                                           0.500510 +- 0.000406 seconds time elapsed ( +- 0.08% )
  javardhan@tejavardhan-RDG_Zephyrus-Gisi-/Downloads$ sudo perf stat -e instructions,cache-references,cache-nisses,Li-dcache-loads,Li-dcache-load-nisses,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcac
    0.465395 +- 0.000406 seconds time elapsed ( +- 0.09% )
        rformance counter stats for './a.out 16 input 512 output0 512' (100 runs):
          0.48103 +- 0.00104 seconds time elapsed ( +- 0.22% )
  javardhandtejavardhan-ROC_Zephyrus-cisz-/Dounioads$ sudo perf stat -e instructions,cache-references,cache-nisses,Li-dcache-loads,Li-dcache-load-nisses,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcac
    devardhammitejavardham-NDG_Egphyros-Gisz-/DownloadsS sudo perf stat -e instructions,cache-references,cache-misses,Li-dcache-loads,Li-dcache-load-misses,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tca
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davardhanqtejavardhan-NGC-Zephyrus-Cidi--/Boumbands sudo perf stat -e instructions,cache-references,cache-misses,L1-dcache-loads,L1-dcache-load-misses,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcac
        javardhan@tejavardhan=ROG_Zephyrus-G14:-/Downlaads$ sudo perf stat -e instructions,cache-references,cache-misses,L1-dcache-loads,L1-dcache-load-misses,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcache-loads,L1-dcac
                                      mance counter stats for './a.out 128 input_512 output0_512' (100 runs):
      0.48076 +- 0.00101 seconds time elapsed ( +- 0.21% )
    javardhangtejavardhan-NGG-Zejhyrus-Cist-/Downloads S sudo perf stat -e instructions,cache-references,cache-nisses,Li-dcache-loads,Li-dcache-load-nisses,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tca
    erformance counter stats for './a.out 256 input 512 output0 512' (100 runs):
    0.55461 +- 0.00110 seconds time elapsed ( +- 0.20% )
                                              0.452450 +- 0.000426 seconds time elapsed ( +- 0.09% )
  javardhandtejavardhan-RDG-Zephyrus-Gids-/Demaloads$ sudo perf stat -e instructions,cache-references,cache-misses,Li-dcache-loads,Li-dcache-load-misses,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcache-loads,Li-lcac
        rformance counter stats for './a.out 128 input_512 output0_512' (100 runs):
    7,02,49,81,755 instructions (+-0.07%) (71.01%)
10,15,04,900 cache-references (+-0.07%) (71.01%)
32,77,1871 cache-misses # 2.025 % of all cache refs (+-0.07%) (71.05%)
3,03,19,15,055 il-id-cache-loads
13,08,19,078 il-id-cache-loads
5,24,478 il-it-cache-loads
5,74,478 il-it-cache-loads
11-it-cache-load-misses # 4.51% of all il-id-cache accesses (+-0.14%) (71.69%)
77,041 il-it-cache-load-misses # 1.31% of all il-id-cache accesses (+-2.47%) (71.14%
cnot supportedb
il-iolad-misses # 1.31% of all il-id-cache accesses (+-2.47%) (71.14%
      7,81,52,24,075 Instructions (+- 0.69%) (70.99%) 25,75,38,876 cache-references (+- 0.12%) (71.14%) 3,01,92,49,222 (1.1-dcache-load-nisses # 20.855 % of all cache refs (+- 1.65%) (71.34%) 1,15,77,1891 (1.1-dcache-load-nisses # 4.50% of all L1-dcache accesses (+- 0.11%) (71.69%) 1,15,75,551 (1.1-cache-load-nisses # 4.50% of all L1-dcache accesses (+- 3.11%) (71.29%) (71.79%) (1.1-cache-load-nisses | 1.07% of all L1-tcache accesses (+- 3.51%) (71.24%) (1.004-nisses | 1.004-nisses | 1.0
  javardhangtejavardhan-NOG-Zephyrus-Gisz-/Downtoads$ sudo perf stat -e instructions,cache-references,cache-nisses,Li-dcache-loads,Li-dcache-load-nisses,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcache-loads,Li-tcac
                             rmance counter stats for './a.out 512 input_512 output0_512' (100 runs):
```