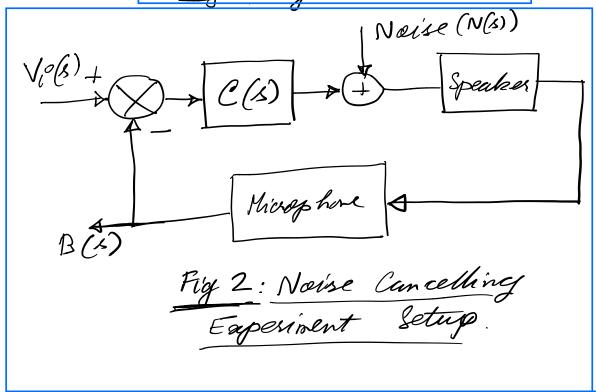
FE344-EDL Assignment (Submit by 20/4/21)

Problem: Design an analog compensation circuit for a noise concelling headphone.

Setup: A(b)
Speaker

B(b)
Microphore

Fig1: System ID



Data: In Fig 1, the Bode plot for the transfer function B(s) is given in the A(s) attached Excel file.
Specs: 1) In Fig 2, the t.f. [B(s)] should have at least [N(s)] attemporation at 100 Hz.
20 dB attenuation at $100 Hz$. 27 In Fig 2 , the tf . $\left[\frac{B(s)}{V_1^{\circ}(s)}\right]$ should have 0 dB gain over the widest possible freq. range. the widest possible gain area
the widest possible freq. range. 3) \[\begin{aligned} \B(\sigma) & \text{should be stable} \\ \V_1^2(\sigma) & \text{adeq wate} \\ \virta & \text{adeq wate} \]
Problem: Design the transfer f= C(s) to neet the above specs. 2) Design an analog chet

implementation of C(s) and verify that the Bode plots of the designed analog clet. matches those of the designed matches those of the designed C(s), using your favourite clet. simulator (e.g. SPICE)