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Polling Collision Indication

This Specification Bulletin clarifies PCD collision indication in case of PCDs supporting Proprietary Technologies

Applicability

This Specification Bulletin applies to:

- *EMV Level 1 Specifications for Payment Systems, EMV Contactless Interface Specification, Version 3.0 – February 2018.*

Related Documents

- *None*

Description

This specification bulletin clarifies in requirement 9.2.1.7 that PCDs are responsible for suppressing the unintentional collision indication that occurs in the polling sequence for proprietary technologies.

Specification Change

Change Requirement 9.2.1.7 as follows:

Requirements 9.2: Polling

PCD	
…	
9.2.1.7	If the PCD supports other technologies, then the PCD shall reset the Operating Field (as defined in section 3.2.6) before continuing with 9.2.1.3 (symbol 20).
PCDs that implement other technologies are responsible for suppressing any unintentional collision indications. These may occur when PICCs support the other technology alongside another supported technology and are due to the same PICC responding both before and after the RESET (symbol 20).	

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