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Clarification on Setting TSI bit for CDA (Mode 1 and 2) Transaction

Applicability

This Specification Bulletin applies to:

- *EMV Integrated Circuit Card Specifications for Payment Systems Book 3 - Application Specification, Version 4.3 November 2011*

Related Documents

None

Effective Date

Immediate

Description

EMV provides four CDA options, Mode 1, 2, 3, and 4. Depending on the option chosen, CDA can be performed at 1st Generate AC, at 2nd Generate AC, or at both.

This bulletin clarifies that when CDA is performed at 1st Generate AC (Mode 1 or Mode 2), 'Offline data authentication was performed' bit in the TSI shall be set to 1 after the 1st Generate AC.

Specification Changes

In Section 10.3, 'Description:', after the paragraph 'If the records read… … bit in the TVR', insert following:

> When CDA is performed at 1st Generate AC (CDA Mode 1 or 2), terminal shall set 'Offline data authentication was performed' bit in the TSI to 1 immediately after the 1st Generate AC.

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