```
!nvcc --version
!pip install git+https://github.com/afnan47/cuda.git
%load ext nvcc plugin
     nvcc: NVIDIA (R) Cuda compiler driver
     Copyright (c) 2005-2023 NVIDIA Corporation
     Built on Tue Aug 15 22:02:13 PDT 2023
     Cuda compilation tools, release 12.2, V12.2.140
     Build cuda 12.2.r12.2/compiler.33191640 0
     Collecting git+<a href="https://github.com/afnan47/cuda.git">https://github.com/afnan47/cuda.git</a>
       Cloning <a href="https://github.com/afnan47/cuda.git">https://github.com/afnan47/cuda.git</a> to /tmp/pip-reg-build-un13y4o
       Running command git clone --filter=blob:none --quiet <a href="https://github.com/a">https://github.com/a</a>
       Resolved <a href="https://github.com/afnan47/cuda.git">https://github.com/afnan47/cuda.git</a> to commit aac710a35f52bb78ab
       Preparing metadata (setup.py) ... done
     Building wheels for collected packages: NVCCPlugin
       Building wheel for NVCCPlugin (setup.py) ... done
       Created wheel for NVCCPlugin: filename=NVCCPlugin-0.0.2-py3-none-any.whl
       Stored in directory: /tmp/pip-ephem-wheel-cache-37ng9loz/wheels/aa/f3/44/
     Successfully built NVCCPlugin
     Installing collected packages: NVCCPlugin
     Successfully installed NVCCPlugin-0.0.2
     created output directory at /content/src
     Out bin /content/result.out
%%writefile add.cu
#include <iostream>
#include <cuda runtime.h>
#include <cstdlib>
using namespace std;
 global void add(int* A, int* B, int* C, int size) {
    int tid = blockIdx.x * blockDim.x + threadIdx.x;
    if (tid < size) {</pre>
         C[tid] = A[tid] + B[tid];
}
int main() {
    int N;
    cout << "Enter the size of the vectors: ";
    cin >> N;
    int* A, * B, * C;
    size t vectorBytes = N * sizeof(int);
    A = new int[N];
    B = new int[N];
    C = new int[N];
    for (int i = 0; i < N; i++) {
         A[i] = rand() % 10;
         B[i] = rand() % 10;
```

}

```
}
    int* X, * Y, * Z;
    cudaMalloc(&X, vectorBytes);
    cudaMalloc(&Y, vectorBytes);
    cudaMalloc(&Z, vectorBytes);
    cudaMemcpy(X, A, vectorBytes, cudaMemcpyHostToDevice);
    cudaMemcpy(Y, B, vectorBytes, cudaMemcpyHostToDevice);
    add <<<1, N >>> (X, Y, Z, N);
    cudaMemcpy(C, Z, vectorBytes, cudaMemcpyDeviceToHost);
    cout << "Vector A:";</pre>
    for (int i = 0; i < N; i++) {
        cout << " " << A[i];
    }
    cout << endl;</pre>
    cout << "Vector B:";</pre>
    for (int i = 0; i < N; i++) {
        cout << " " << B[i];
    cout << endl;</pre>
    cout << "Addition:";</pre>
    for (int i = 0; i < N; i++) {
        cout << " " << C[i];
    }
    cout << endl;</pre>
    delete[] A;
    delete[] B;
    delete[] C;
    cudaFree(X);
    cudaFree(Y);
    cudaFree(Z);
    return 0;
    Writing add.cu
!nvcc add.cu -o add
!./add
    Enter the size of the vectors: 2
    Vector A: 3 7
    Vector B: 6 5
    Addition: 0 0
```

```
%%WriteTile matrix multi.cu
#include <iostream>
#include <cuda runtime.h>
using namespace std;
const int N = 2;
 _global__ void matrixMultiply(int* A, int* B, int* C) {
    int row = blockIdx.y * blockDim.y + threadIdx.y;
    int col = blockIdx.x * blockDim.x + threadIdx.x;
    if (row < N \&\& col < N) {
        int sum = 0;
        for (int i = 0; i < N; ++i) {
            sum += A[row * N + i] * B[i * N + col];
        C[row * N + col] = sum;
    }
}
int main() {
    int* A, * B, * C;
    size_t matrixBytes = N * N * sizeof(int);
    A = new int[N * N];
    B = new int[N * N];
    C = new int[N * N];
    auto input = [&](int* matrix) {
        cout << "Enter elements of Matrix (" << N << "x" << N << "):" << endl;</pre>
        for (int i = 0; i < N * N; ++i) cin >> matrix[i];
    };
    input(A);
    input(B);
    int* X, * Y, * Z;
    cudaMalloc(&X, matrixBytes);
    cudaMalloc(&Y, matrixBytes);
    cudaMalloc(&Z, matrixBytes);
    cudaMemcpy(X, A, matrixBytes, cudaMemcpyHostToDevice);
    cudaMemcpy(Y, B, matrixBytes, cudaMemcpyHostToDevice);
    matrixMultiply<<<1, dim3(N, N)>>>(X, Y, Z);
    cudaMemcpy(C, Z, matrixBytes, cudaMemcpyDeviceToHost);
    cout << "Output- Matrix size: " << N << "x" << N << endl;</pre>
    cout << "Input Matrix 1:" << endl;</pre>
    for (int i = 0; i < N; ++i) {
        for (int j = 0; j < N; ++j) cout << A[i * N + j] << " ";
        cout << endl;</pre>
```

```
}
    cout << "Input Matrix 2:" << endl;</pre>
     for (int i = 0; i < N; ++i) {
         for (int j = 0; j < N; ++j) cout << B[i * N + j] << " ";
         cout << endl;</pre>
    }
     cout << "Resultant matrix:" << endl;</pre>
     for (int i = 0; i < N; ++i) {
         for (int j = 0; j < N; ++j) cout << C[i * N + j] << " ";
         cout << endl;
    }
    cout << "Finished." << endl;</pre>
    delete[] A;
    delete[] B;
    delete[] C;
    cudaFree(X);
     cudaFree(Y);
     cudaFree(Z);
    return 0;
}
     Writing matrix multi.cu
!nvcc matrix multi.cu -o matrix multi
!./matrix multi
     Enter elements of Matrix (2x2):
%%writefile smma.cu
#include <iostream>
#include <vector>
#include <climits>
  global void min reduction kernel(int* arr, int size, int* result) {
     int tid = blockIdx.x * blockDim.x + threadIdx.x;
    if (tid < size) {</pre>
         atomicMin(result, arr[tid]);
    }
}
  global void max reduction kernel(int* arr, int size, int* result) {
     int tid = blockIdx.x * blockDim.x + threadIdx.x;
    if (tid < size) {
         atomicMax(result, arr[tid]);
    }
}
```

```
global void sum reduction kernel(int* arr, int size, int* result) {
    int tid = blockIdx.x * blockDim.x + threadIdx.x;
    if (tid < size) {
        atomicAdd(result, arr[tid]);
    }
}
int main() {
    std::vector<int> arr = {5, 2, 9, 1, 7, 6, 8, 3, 4};
    int size = arr.size();
    int* d arr;
    int* d result min, * d result max, * d result sum;
    int result min = INT MAX, result max = INT MIN, result sum = 0;
    cudaMalloc(&d arr, size * sizeof(int));
    cudaMemcpy(d arr, arr.data(), size * sizeof(int), cudaMemcpyHostToDevice);
    cudaMalloc(&d result min, sizeof(int));
    cudaMalloc(&d result max, sizeof(int));
    cudaMalloc(&d result sum, sizeof(int));
    cudaMemcpy(d result min, &result min, sizeof(int), cudaMemcpyHostToDevice);
    cudaMemcpy(d result max, &result max, sizeof(int), cudaMemcpyHostToDevice);
    cudaMemcpy(d result sum, &result sum, sizeof(int), cudaMemcpyHostToDevice);
    min reduction kernel <<< (size + 255) / 256, 256>>> (d arr, size, d result mir
    max reduction kernel <<< (size + 255) / 256, 256>>> (d arr, size, d result max
    sum reduction kernel<<<(size + 255) / 256, 256>>>(d arr, size, d result sum
    cudaMemcpy(&result min, d result min, sizeof(int), cudaMemcpyDeviceToHost);
    cudaMemcpy(&result max, d result max, sizeof(int), cudaMemcpyDeviceToHost);
    cudaMemcpy(&result sum, d result sum, sizeof(int), cudaMemcpyDeviceToHost);
    std::cout << "Minimum value: " << result min << std::endl;</pre>
    std::cout << "Maximum value: " << result max << std::endl;</pre>
    std::cout << "Sum: " << result sum << std::endl;</pre>
    std::cout << "Average: " << static cast<double>(result sum) / size << std::</pre>
    cudaFree(d arr);
    cudaFree(d result min);
    cudaFree(d result max);
    cudaFree(d result sum);
    return 0;
}
!nvcc smma.cu -o smma
!./smma
%writefile bfsdfs.cu
#include <iostream>
#include <vector>
#include >מוובוום>
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#INCLUAL YULUC
#include <stack>
global void bfs kernel(int* adjList, int* visited, int* queue, int* queueSi
    int tid = blockIdx.x * blockDim.x + threadIdx.x;
    if (tid < *queueSize) {</pre>
        int u = queue[tid];
        if (!visited[u]) {
            visited[u] = 1;
            for (int i = adjList[u]; i < adjList[u + 1]; ++i) {
                int v = adjList[i];
                if (!visited[v]) {
                     int idx = atomicAdd(queueSize, 1);
                    queue[idx] = v;
                }
            }
        }
    }
}
 _global__ void dfs_kernel(int* adjList, int* visited, int* stack, int* stackSi
    int tid = blockIdx.x * blockDim.x + threadIdx.x;
    if (tid < *stackSize) {</pre>
        int u = stack[tid];
        if (!visited[u]) {
            visited[u] = 1;
            for (int i = adjList[u]; i < adjList[u + 1]; ++i) {
                int v = adjList[i];
                if (!visited[v]) {
                     int idx = atomicAdd(stackSize, 1);
                     stack[idx] = v;
                }
            }
        }
    }
}
int main() {
    int n, m;
    std::cout << "Enter the number of vertices: ";</pre>
    std::cin >> n;
    std::cout << "Enter the number of edges: ";
    std::cin >> m;
    // Assuming graph is represented as an adjacency list
    std::vector<std::vector<int>> adjList(n + 1);
    std::cout << "Enter the edges (format: u v):\n";</pre>
    for (int i = 0; i < m; ++i) {
        int u, v;
        std::cin >> u >> v;
        adjList[u].push_back(v);
        adjList[v].push back(u); // Assuming an undirected graph
    }
    // Allocate memory on the GPII
```

```
// Account memory on the oro
int* d adjList, * d visited, * d queue, * d queueSize, * d stack, * d stack
cudaMalloc(&d adjList, (2 * m) * sizeof(int)); // Each edge is stored twice
cudaMalloc(&d_visited, n * sizeof(int));
cudaMalloc(&d queue, n * sizeof(int));
cudaMalloc(&d_queueSize, sizeof(int));
cudaMalloc(&d stack, n * sizeof(int));
cudaMalloc(&d stackSize, sizeof(int));
// Initialize data on the GPU
// Perform BFS traversal
int start;
std::cout << "Enter the starting vertex for BFS: ";</pre>
std::cin >> start;
cudaMemcpy(d queue, &start, sizeof(int), cudaMemcpyHostToDevice);
cudaMemcpy(d queueSize, &start, sizeof(int), cudaMemcpyHostToDevice);
int queueSize = 1;
while (queueSize > 0) {
    bfs kernel<<<(queueSize + 255) / 256, 256>>>(d adjList, d visited, d qu
    cudaMemcpy(&queueSize, d queueSize, sizeof(int), cudaMemcpyDeviceToHost
}
// Perform DFS traversal
std::cout << "Enter the starting vertex for DFS: ";
std::cin >> start;
cudaMemcpy(d visited, &start, sizeof(int), cudaMemcpyHostToDevice);
cudaMemcpy(d_stack, &start, sizeof(int), cudaMemcpyHostToDevice);
cudaMemcpy(d stackSize, &start, sizeof(int), cudaMemcpyHostToDevice);
int stackSize = 1;
while (stackSize > 0) {
    dfs kernel <<< (stackSize + 255) / 256, 256>>> (d adjList, d visited, d st
    cudaMemcpy(&stackSize, d_stackSize, sizeof(int), cudaMemcpyDeviceToHost
}
// Copy visited array back to host
int* h visited = new int[n];
cudaMemcpy(h_visited, d_visited, n * sizeof(int), cudaMemcpyDeviceToHost);
// Print BFS traversal result
std::cout << "BFS traversal starting from vertex " << start << ":\n";</pre>
for (int i = 0; i < n; ++i) {
    if (h visited[i]) {
        std::cout << i << " ";
    }
}
std::cout << std::endl;</pre>
// Print DFS traversal result
std::cout << "DFS traversal starting from vertex " << start << ":\n";</pre>
for (int i = 0; i < n; ++i) {
    if (h visited[i]) {
        std::cout << i << " ";
    }
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std::cout << std::endl;</pre>
   delete[] h visited;
    // Free memory on the GPU
    cudaFree(d adjList);
    cudaFree(d_visited);
    cudaFree(d queue);
    cudaFree(d_queueSize);
    cudaFree(d_stack);
    cudaFree(d stackSize);
    return 0;
}
!nvcc bfsdfs.cu -o bfsdfs
!./bfsdfs
%%writefile bubblesort.cu
#include <iostream>
#include <vector>
#include <chrono>
global void bubbleSortParallel(int* arr, int n) {
    int idx = blockIdx.x * blockDim.x + threadIdx.x;
    if (idx < n - 1) {
        if (arr[idx] > arr[idx + 1]) {
            int temp = arr[idx];
            arr[idx] = arr[idx + 1];
            arr[idx + 1] = temp;
        }
    }
}
void bubbleSortSerial(std::vector<int>& arr) {
    int n = arr.size();
    bool swapped = true;
   while (swapped) {
        swapped = false;
        for (int i = 0; i < n - 1; i++) {
            if (arr[i] > arr[i + 1]) {
                std::swap(arr[i], arr[i + 1]);
                swapped = true;
            }
        }
    }
}
int main() {
    int n = 10000;
    int block size = 256;
    int num_blocks = (n + block_size - 1) / block_size;
```

```
sta::vector<int> arr(n);
    // Initialize array with random values
    for (int i = 0; i < n; i++) {
        arr[i] = rand() % 10000;
    }
    // Measure serial Bubble Sort performance
    auto start = std::chrono::high resolution clock::now();
    bubbleSortSerial(arr);
    auto stop = std::chrono::high resolution clock::now();
    auto durationSerial = std::chrono::duration cast<std::chrono::milliseconds>
    std::cout << "Serial Bubble Sort took " << durationSerial.count() << " mill</pre>
    // Reset array for parallel sort
    for (int i = 0; i < n; i++) {
        arr[i] = rand() % 10000;
    }
    int* d arr;
    cudaMalloc(&d arr, n * sizeof(int));
    cudaMemcpy(d_arr, arr.data(), n * sizeof(int), cudaMemcpyHostToDevice);
    // Measure parallel Bubble Sort performance
    start = std::chrono::high resolution clock::now();
    for (int i = 0; i < n; i++) {
        bubbleSortParallel<<<num blocks, block size>>>(d arr, n);
        cudaDeviceSynchronize();
    }
    stop = std::chrono::high resolution clock::now();
    auto durationParallel = std::chrono::duration cast<std::chrono::milliseconc
    std::cout << "Parallel Bubble Sort took " << durationParallel.count() << "</pre>
    cudaMemcpy(arr.data(), d arr, n * sizeof(int), cudaMemcpyDeviceToHost);
    cudaFree(d arr);
    return 0;
}
!nvcc bubblesort.cu -o bubblesort
!./bubblesort
%%writefile mergesort.cu
#include <iostream>
#include <vector>
#include <chrono>
// Serial merge sort implementation
void merge(int* arr, int l, int m, int r) {
    // Merge logic
}
```

```
void mergeSort(int* arr, int l, int r) {
    // Merge sort logic
}
// Parallel merge sort implementation
global void mergeSortParallel(int* arr, int l, int r) {
    // Merge sort logic
}
int main() {
    int n = 10000;
    int block size = 256;
    int num blocks = (n + block size - 1) / block size;
    std::vector<int> arr_serial(n);
    std::vector<int> arr_parallel(n);
    // Initialize arrays with random values
    // Copy values from arr_serial to arr_parallel for comparison
    // Serial merge sort
    auto start serial = std::chrono::high resolution clock::now();
    mergeSort(arr serial.data(), 0, n - 1);
    auto end serial = std::chrono::high resolution clock::now();
    // Parallel merge sort
    int* d arr;
    cudaMalloc(&d_arr, n * sizeof(int));
    cudaMemcpy(d_arr, arr_parallel.data(), n * sizeof(int), cudaMemcpyHostToDevi
    auto start parallel = std::chrono::high resolution clock::now();
    mergeSortParallel<<<num blocks, block size>>>(d arr, 0, n - 1);
    cudaDeviceSynchronize();
    auto end parallel = std::chrono::high resolution clock::now();
    cudaMemcpy(arr_parallel.data(), d_arr, n * sizeof(int), cudaMemcpyDeviceToHo
    cudaFree(d arr);
    // Print timing information
    std::chrono::duration<double, std::milli> duration_serial = end_serial - sta
    std::cout << "Serial Merge Sort took " << duration_serial.count() << " milli</pre>
    std::chrono::duration<double, std::milli> duration parallel = end parallel -
    std::cout << "Parallel Merge Sort took " << duration parallel.count() << " m</pre>
    return 0;
}
!nvcc mergesort.cu -o mergesort
!./mergesort
Start coding or generate with AI.
```