Project V

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I. Introduction

The particular project has two parts where the first part involves to test the array multiplication, SIMD and non-SIMD. The second part involves to test the array multiplication and reduction, with SIMD and non-SIMD.

II. System Configurations & Load

This project was implemented using the flip server at OSU on-campus computer labs which has about 24 CPU cores. Fig 1 below shows the CPU average load for 1, 5 and 15 minutes to be 1.46, 1.35 and 1.45 respectively.

```
flip2 ~/workspace/Project 5 45%
flip2 ~/workspace/Project 5 45% uptime
21:50:08 up 63 days, 6:51, 62 users, load average: 1.46, 1.35, 1.45
```

Fig 1. System configurations - System Load

III. Implementation Specifications

The implementation code is mainly separated into SIMD and non-SIMD code respectively. The output performance calculated for the particular can be found in the Output.txt file. Moreover, the code generates its results using the test.sh script for the non-SIMD code. SIMD was tested manually. Both these codes were tested under varying array size from 10K to 10M. Under the SIMD folder, the code for SIMD is contained within the arraymult.cpp, simd.p5.o, simd.p5.h with makefile, and the non-SIMD folder contains the main implementation files under the arraymult.cpp.

IV. Performance and Speedup

TABLE I. MULTIPLICATION AVERAGE PERFORMANCE

Multiplication- Average	ge Performance			
	10000	100000	1000000	10000000
Non-SIMD	1.06	1.13	1.18	1.42
SIMD	5.04	7.31	6.96	7.07
Speedup	4.754716981	6.469026549	5.898305085	4.978873239

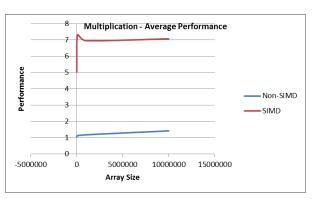


Fig 2. Multiplication - Average Performance

The Table I above shows the performance for the Non-SIMD and SIMD, where the performance is relatively constant for Non-SIMD. However, the performance for SIMD considerably increases until a particular array size, in the particular scenario an array size of 10⁶ shows a considerable increase. However, the performance slightly decreases and remains almost constant for an array size of 10⁷. The particular has also been replicated in the graph. Note, the performance here is measured as MegaNumTries/second.

The graph for speedup with respect to ratio of SIMD and Non-SIMD for the particular is shown in the image below. Here the speedup considerably increases

as for array size lower than 10⁵, however the speedup is slowly affected for all the later array sizes.

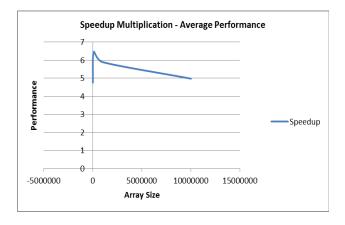


Fig 3. Speedup Multiplication

TABLE II. REDUCTION AVERAGE PERFORMANCE

Reduction - Average Performance				
	10000	100000	1000000	10000000
Non-SIMD	1.05	1.13	1.13	2.17
SIMD	5.89	10.4	9.72	9.97
Speedup	5.60952381	9.203539823	8.601769912	4.594470046

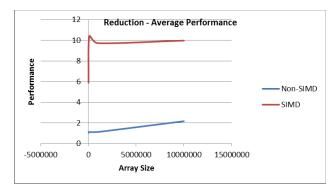


Fig 4. Reduction – Average Performance

The Table II above shows the reduction average performance for increasing array size. Here the performance behavior for SIMD is similar to that of multiplication. However, the non-SIMD array size shows a considerable increase with the increased number of arraysize.

The speedup for the particular has been plotted in the figure below. Here, a considerable good speedup is observed for all the lower arraysize, however with the

increase in the arraysize the speedup considerably decreases.

However, for both multiplication and reduction, the speedup observed is around 5.0.

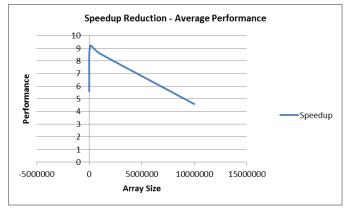


Fig 5. Speedup Reduction

TABLE III. MULTIPLICATION PEAK PERFORMANCE

Multiplication - Peak Performance				
	10000	100000	1000000	10000000
Non-SIMD	111.95	120.95	120.44	229.28
SIMD	616.46	968.9	850.55	750.57

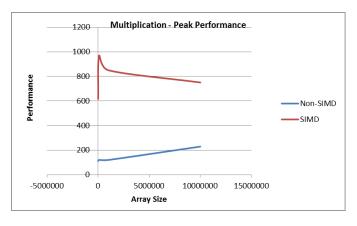


Fig 6. Multiplication - Peak Performance

Table III above and the graph represents the Peak performance which is observed at the array size of 10⁵ and 10⁶ respectively of 120.95 for Non-SIMD and 968.9 for SIMD. Note, the performance is measured here MegaNUMs/Sec. The graphical representation for the peak performance represents the similar performance observed with average performance.

However, the performance of SIMD continues to degrade, whereas Non-SIMD steadily increases.

TABLE IV. REDUCTION PEAK PERFORMANCE

Reduction - Peak Performance				
	10000	100000	1000000	10000000
Non-SIMD	111.95	120.95	120.44	229.28
SIMD	624.12	1052.42	974.66	1000.1

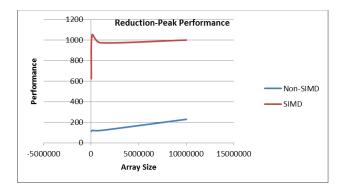


Fig 7. Reduction – Peak Performance

Table IV above and the graph represents the Peak performance where the performance for Non-SIMD becomes constant for Non-SIMD, however the performance is observed to slowly increase for SIMD. Note, the performance is measured here MegaNUMs/Sec as well. The graphical representation for the peak performance represents the similar performance observed with average performance in reduction.