



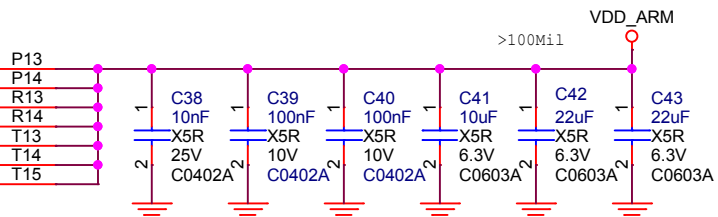
U3N

ARM

VDD\_CORE  
VDD\_CORE  
VDD\_CORE  
VDD\_CORE  
VDD\_CORE  
VDD\_CORE  
VDD\_CORE

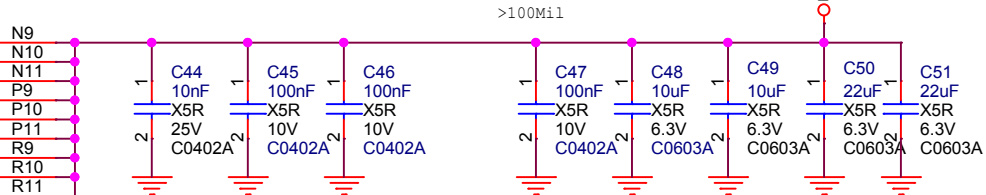
GPU/Logic

VDD\_LOGIC  
VDD\_LOGIC  
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VDD\_LOGIC  
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VDD\_LOGIC



PCB LAYOUT Note: All capacitances should be close to SOC,  
and one of 22uF capacitance should be placed right under the pin

VDD\_ARM



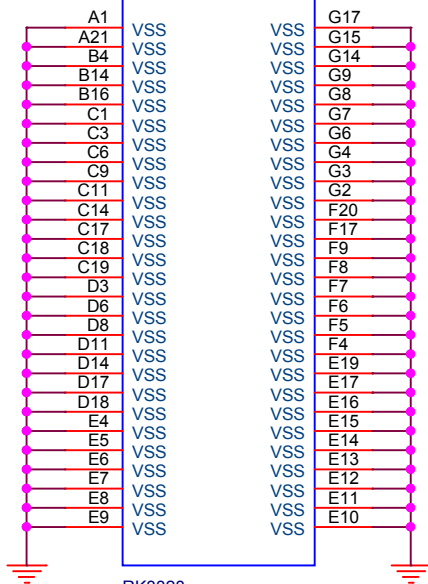
PCB LAYOUT Note: All capacitances should be close to SOC,  
and one of 22uF capacitance should be placed right under the pin

VDD\_LOG

RK3328

TFBGA395L-14X14mmA

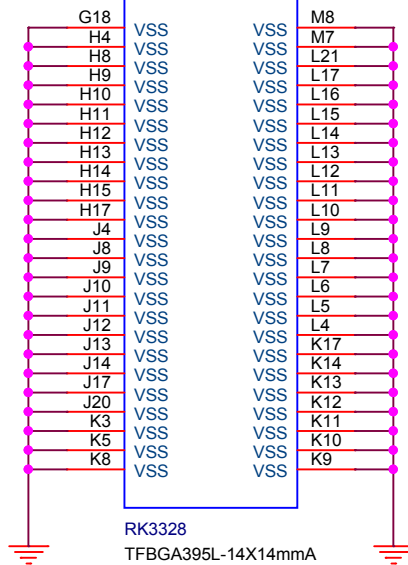
U3O



RK3328

TFBGA395L-14X14mmA

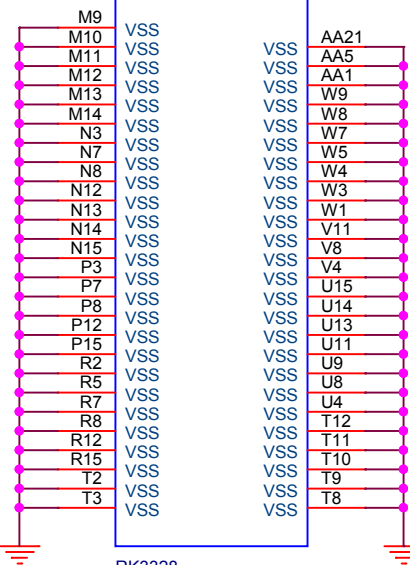
U3P



RK3328

TFBGA395L-14X14mmA

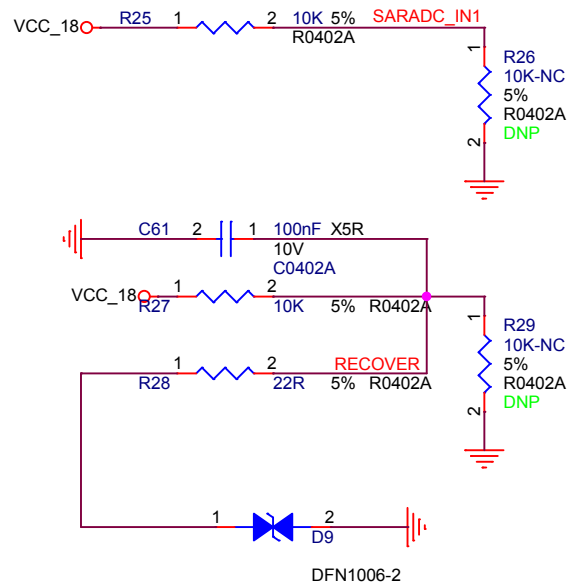
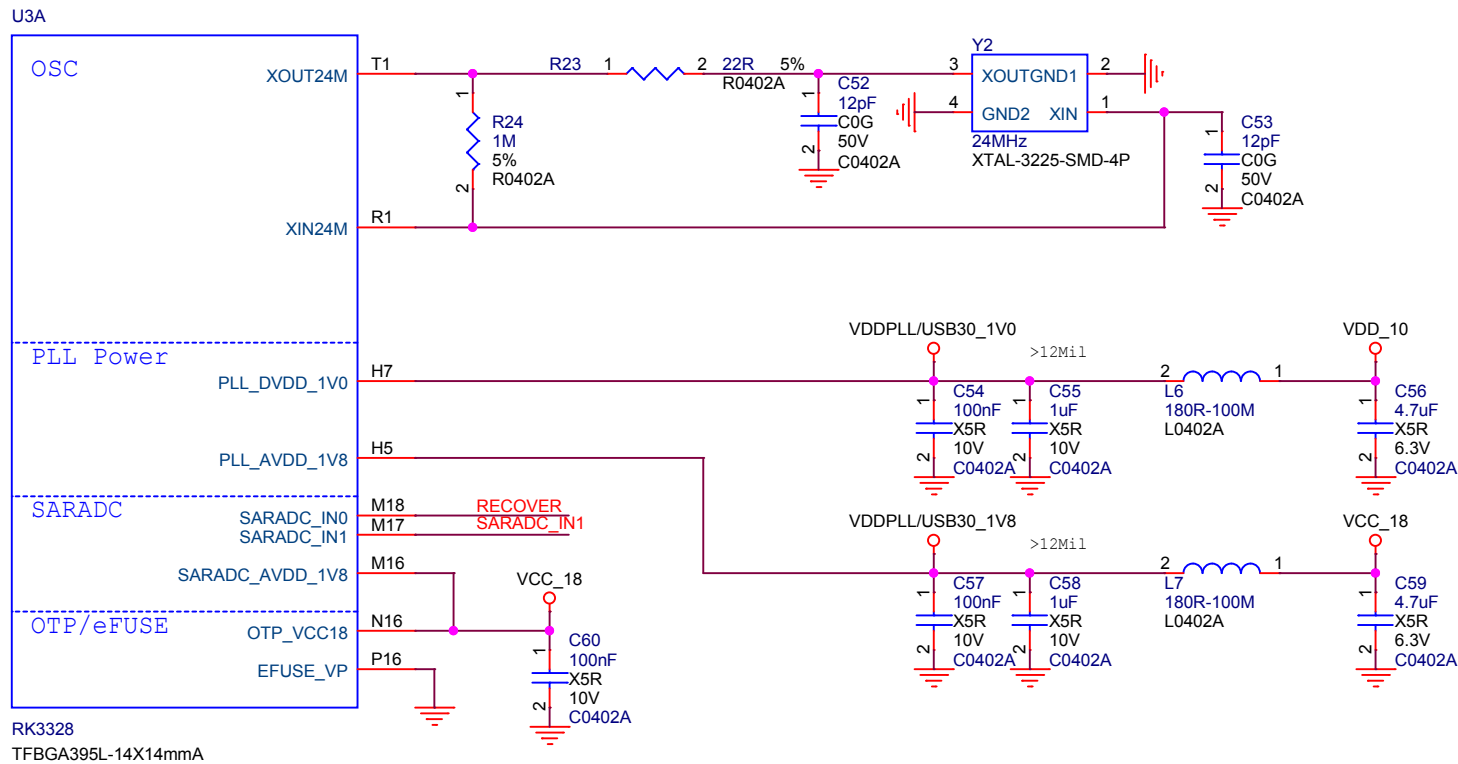
U3Q



RK3328

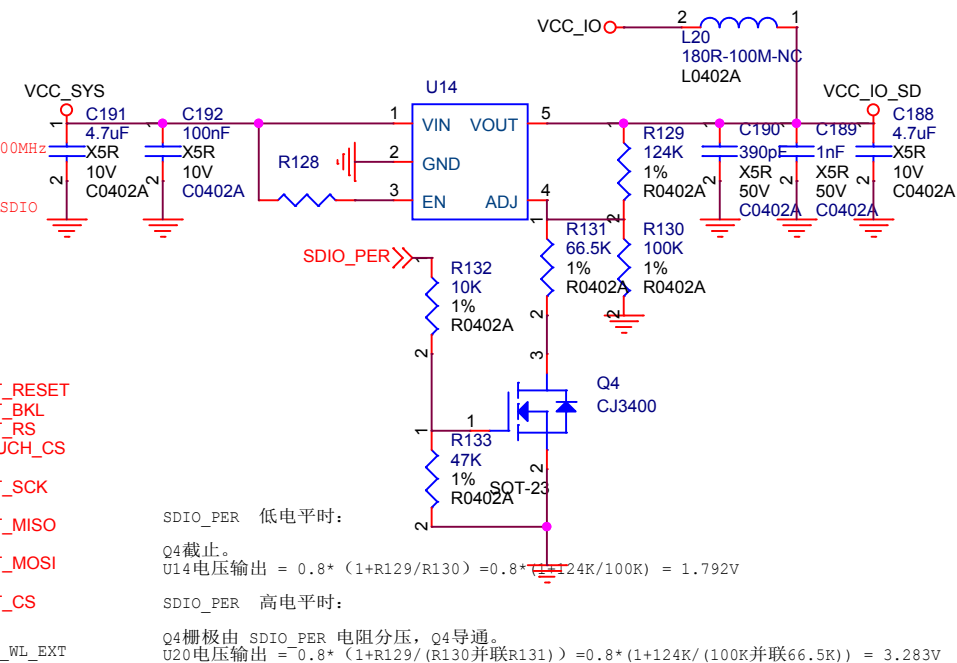
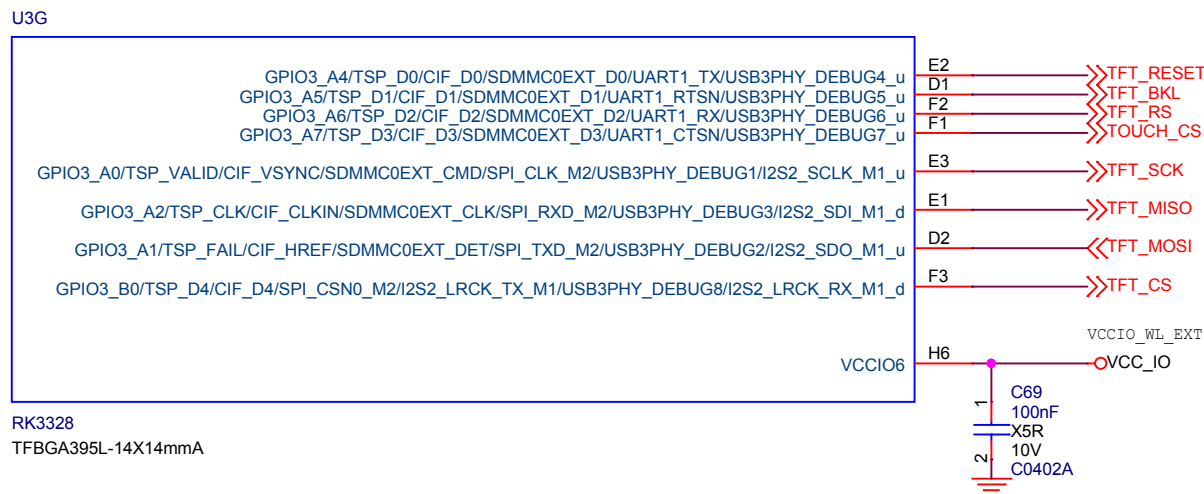
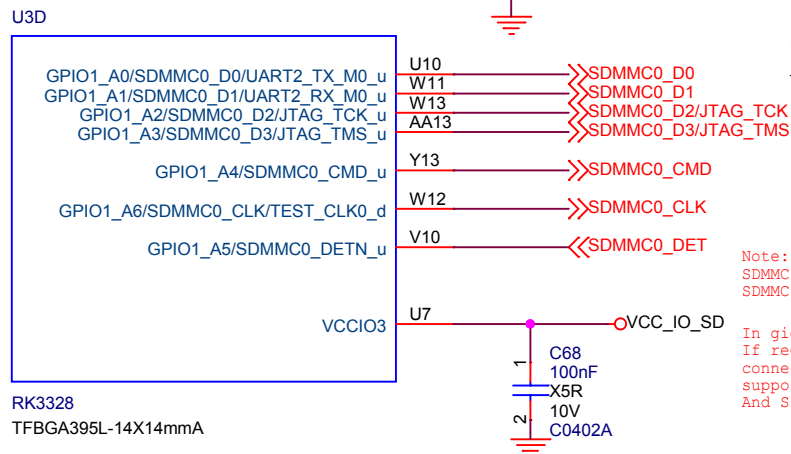
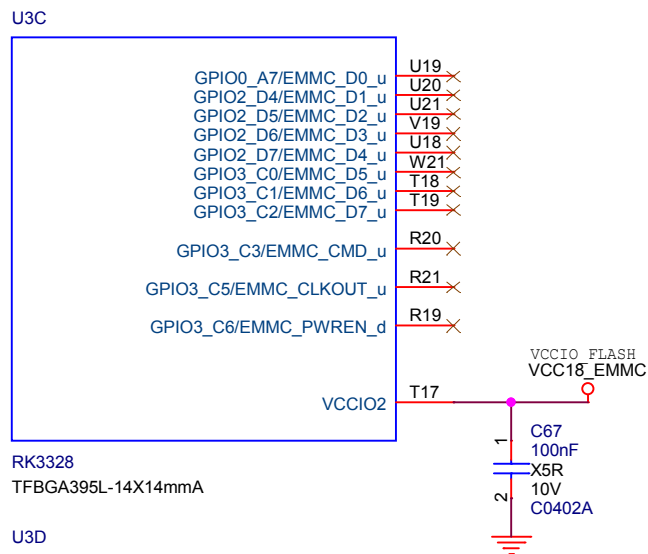
TFBGA395L-14X14mmA

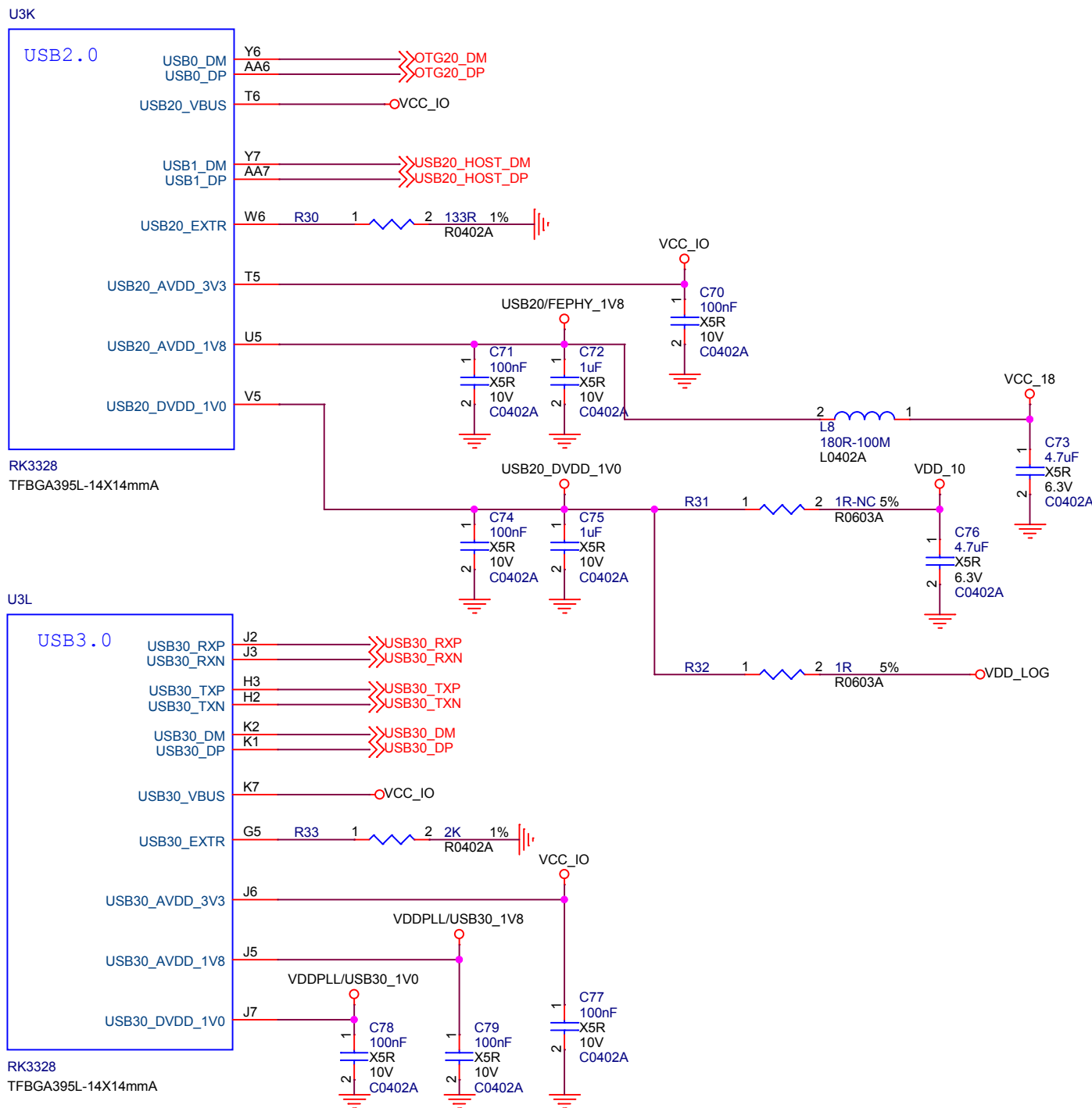
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				Github : <a href="https://github.com/makerbase-mks">https://github.com/makerbase-mks</a>	
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					Rev V1.0.002




SARADC_IN0	SARADC_IN1	Up Resistance	Down Resistance	
Recover		DNP	10K	
BOM1		110K	10K	
BOM2		100K	20K	
BOM3		100K	33K	
BOM4		36K	18K	
BOM5		51K	36K	
BOM6		51K	51K	
BOM7		36K	51K	
BOM8		18K	36K	
BOM9		33K	100K	
BOM10		20K	100K	
BOM11		10K	110K	
BOM12		10K	DNP	

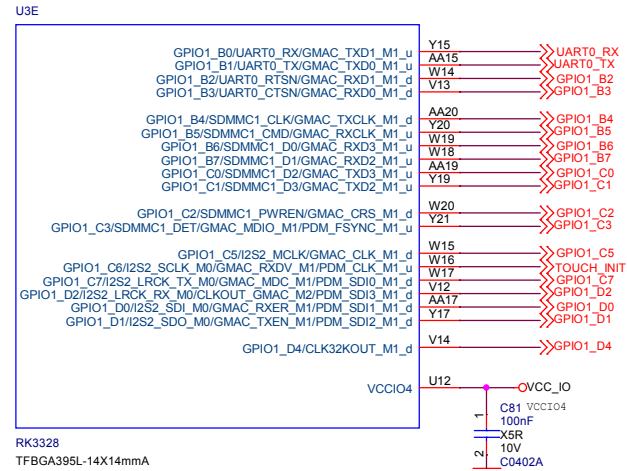
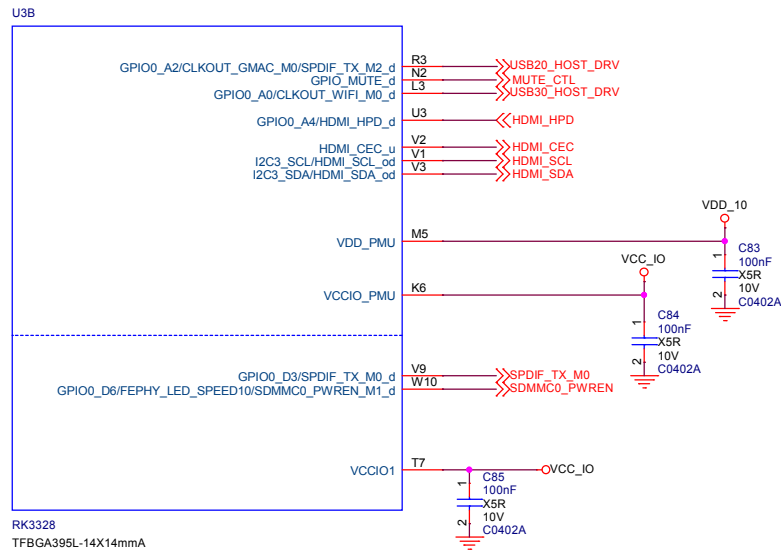
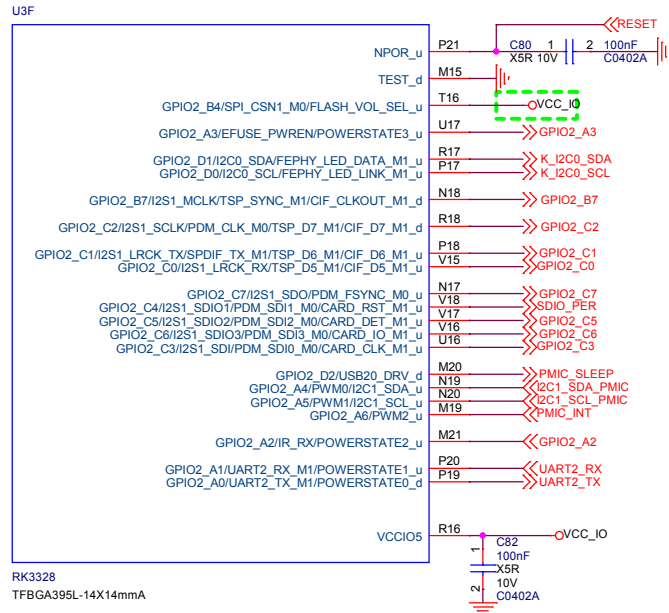




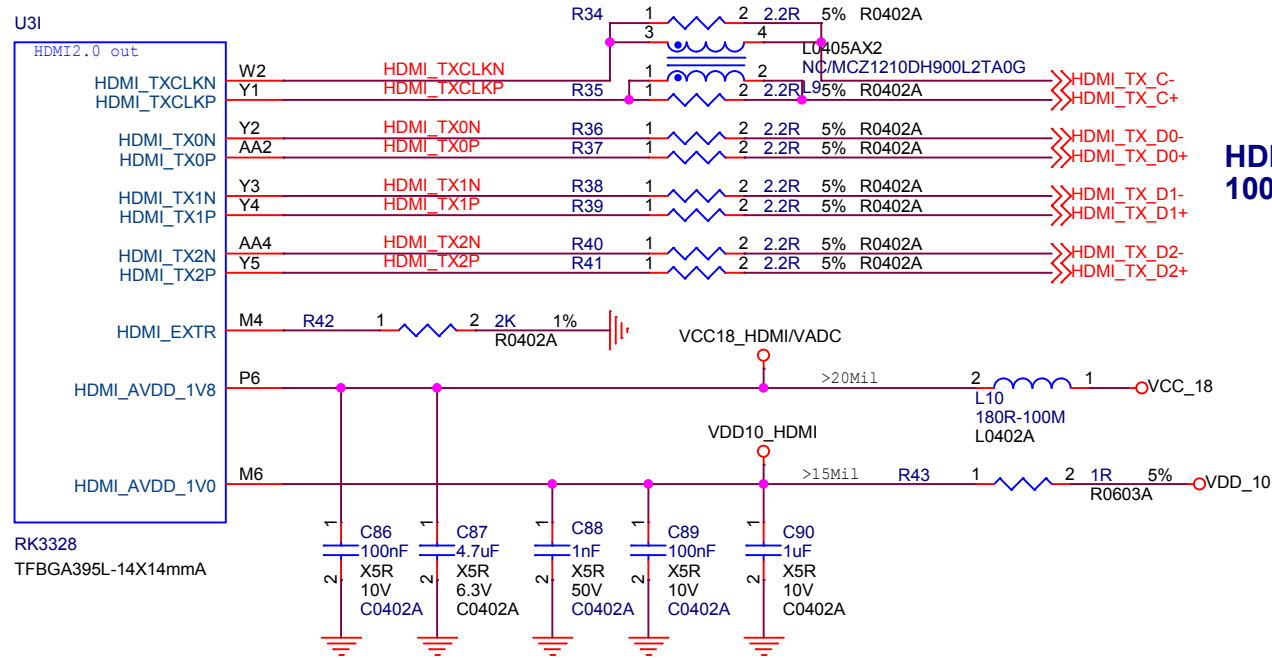


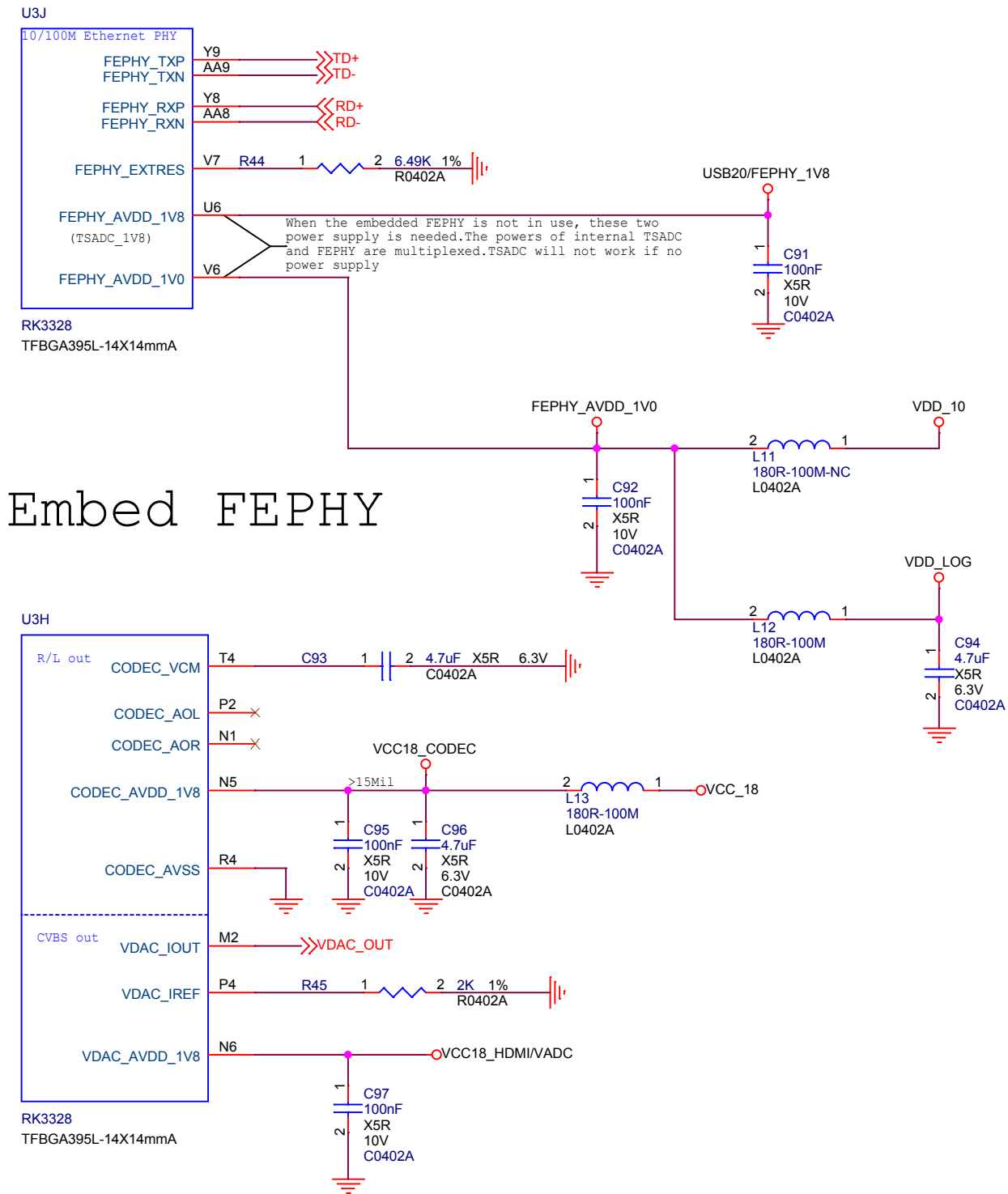
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A4	08_RK3328 USB2 PHY/USB3 PHY		V1.0 002
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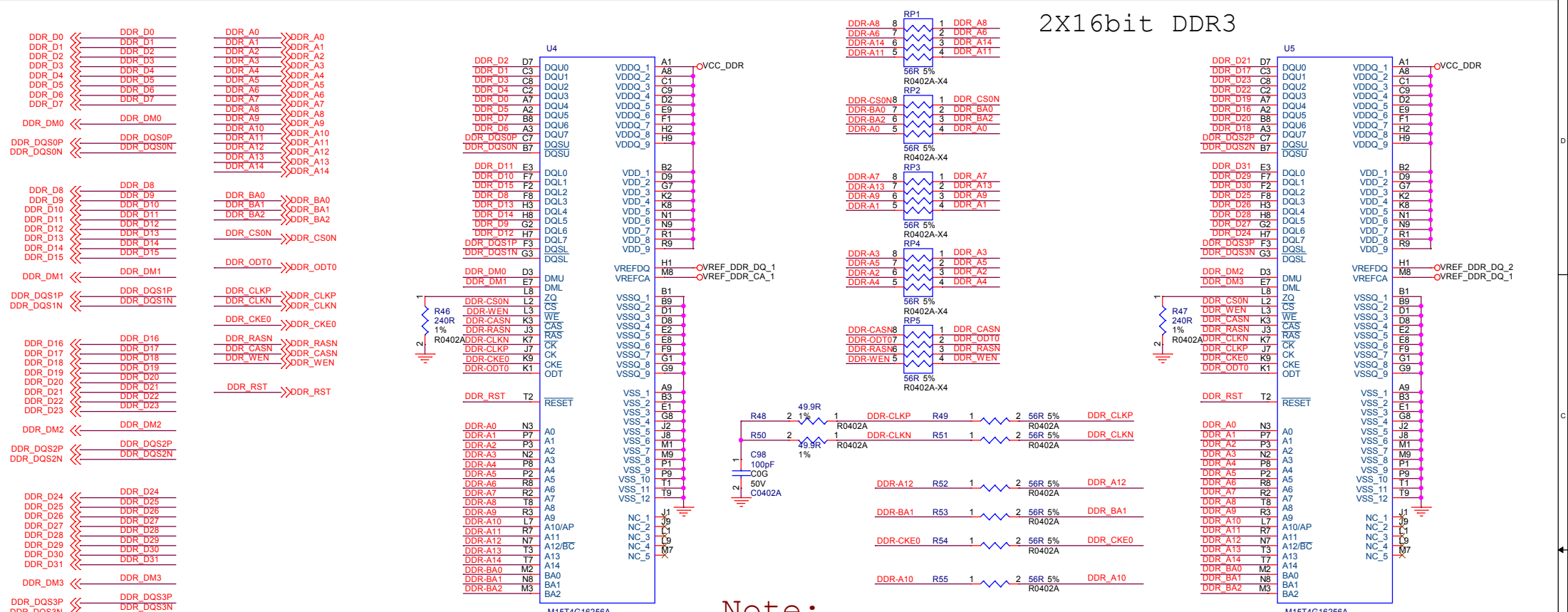
## Close to RK3328



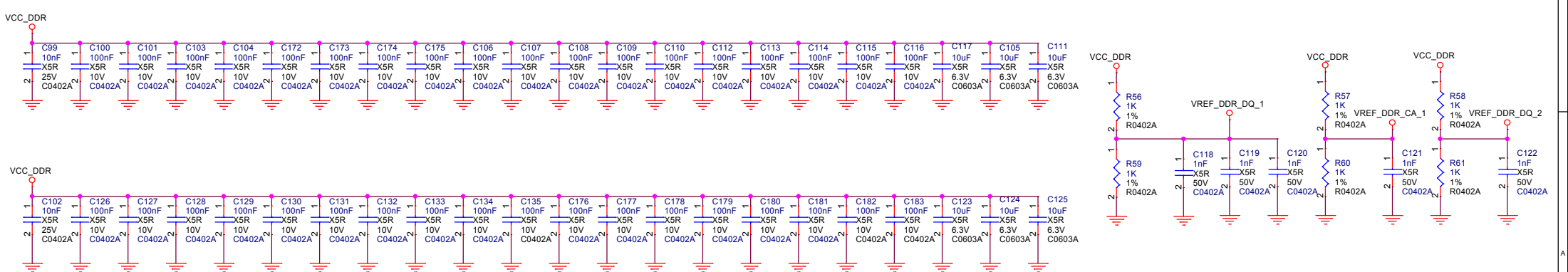


# Embed FEPHY

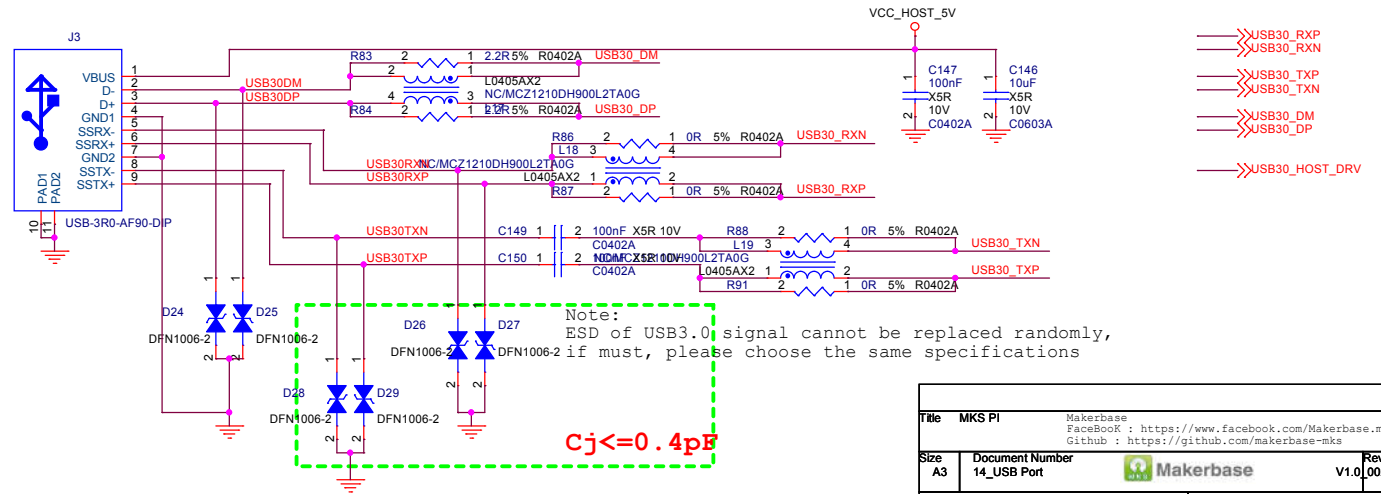
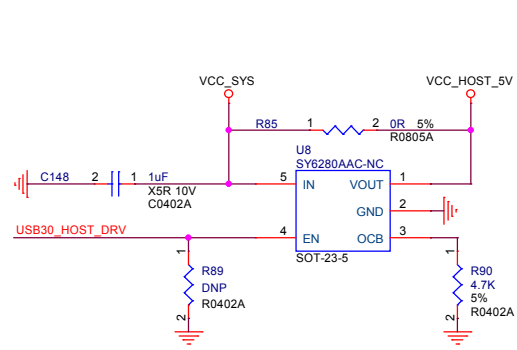
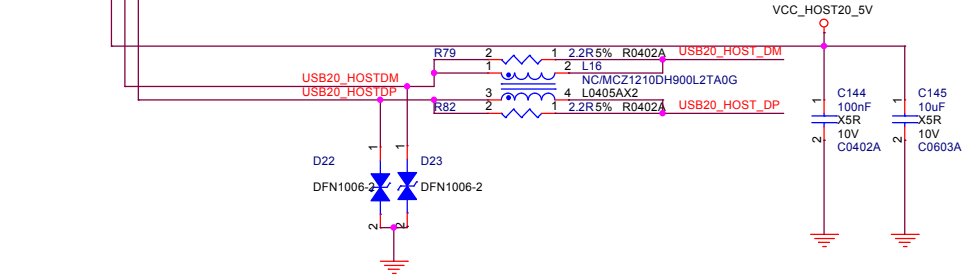
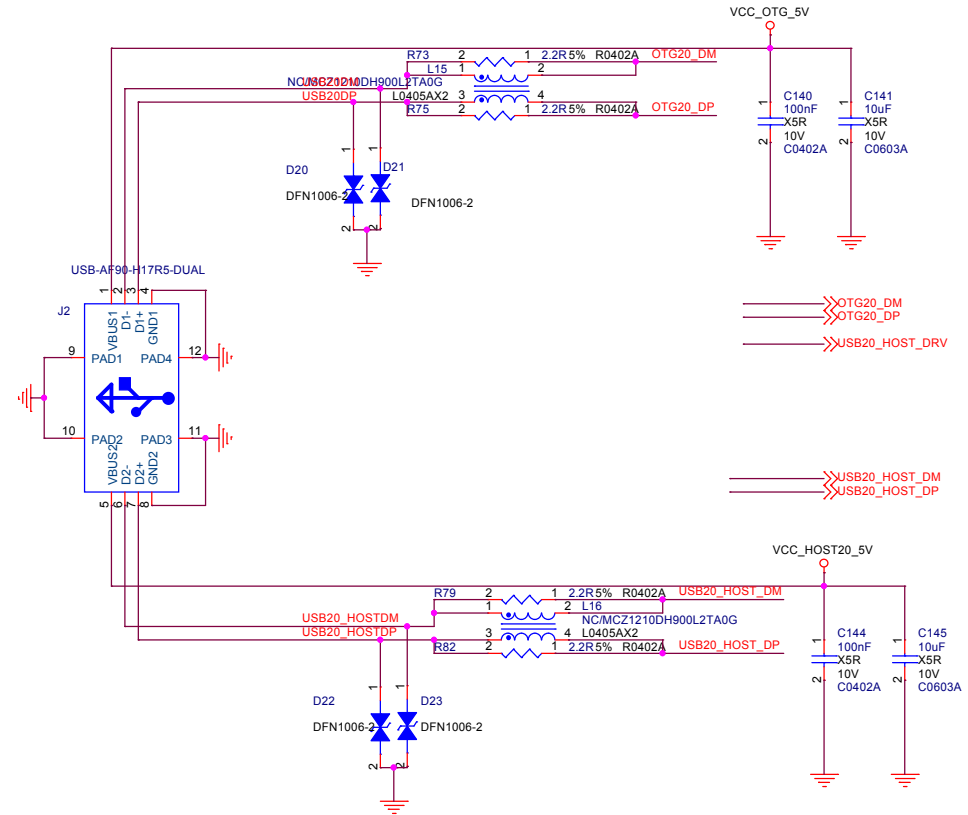
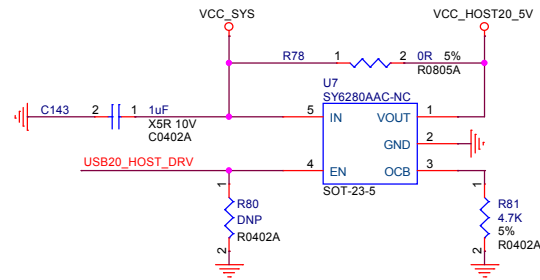
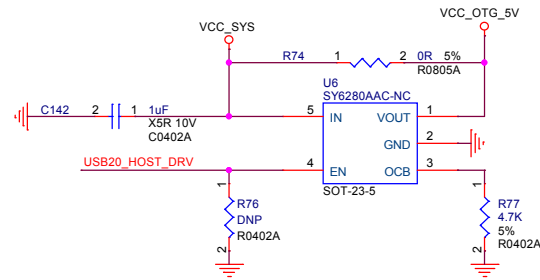
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Size	Document Number	 <b>Makerbase</b>	Rev	
A4	11_RK3328 FEPHY/AV		V1.0_002	
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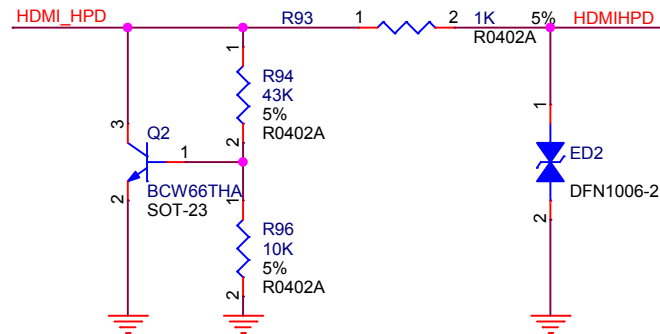
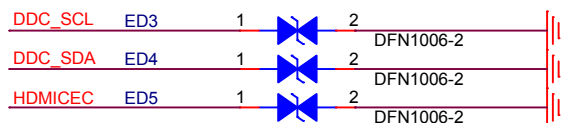
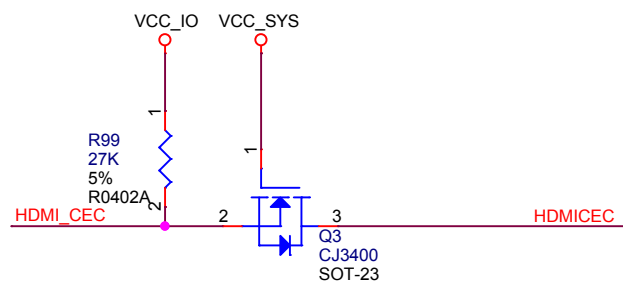
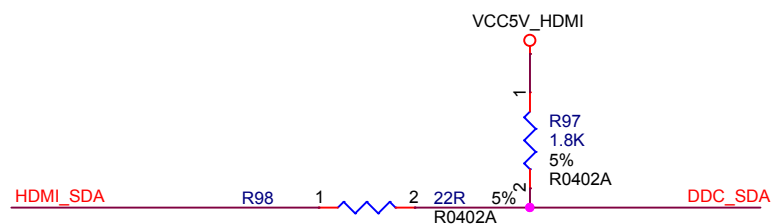
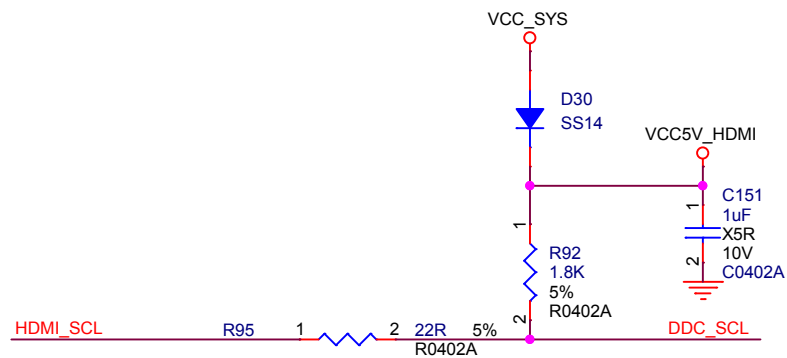


2X16bit DDR3

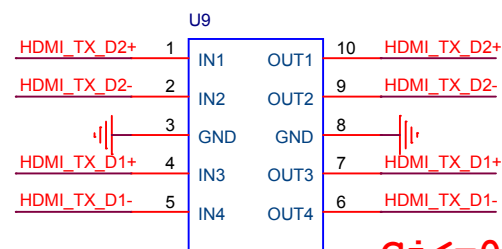




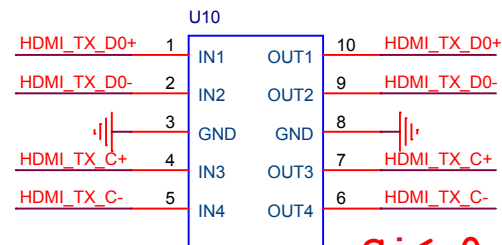




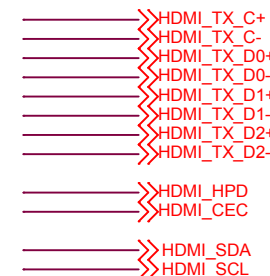
HDMI OUT

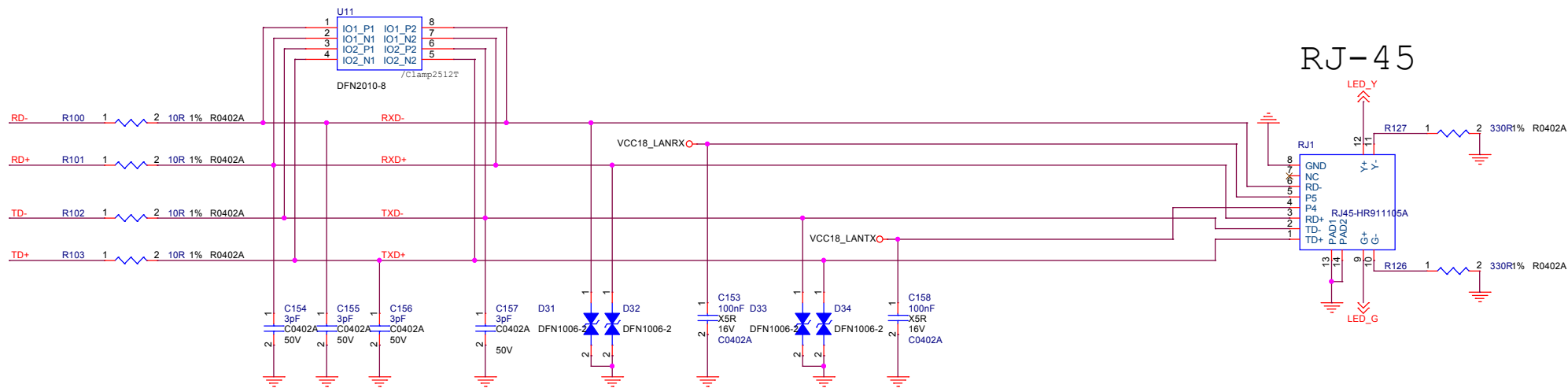


$C_j \leq 0.4 \text{ pF}$



$C_j \leq 0.4 \text{ pF}$





RJ-45

