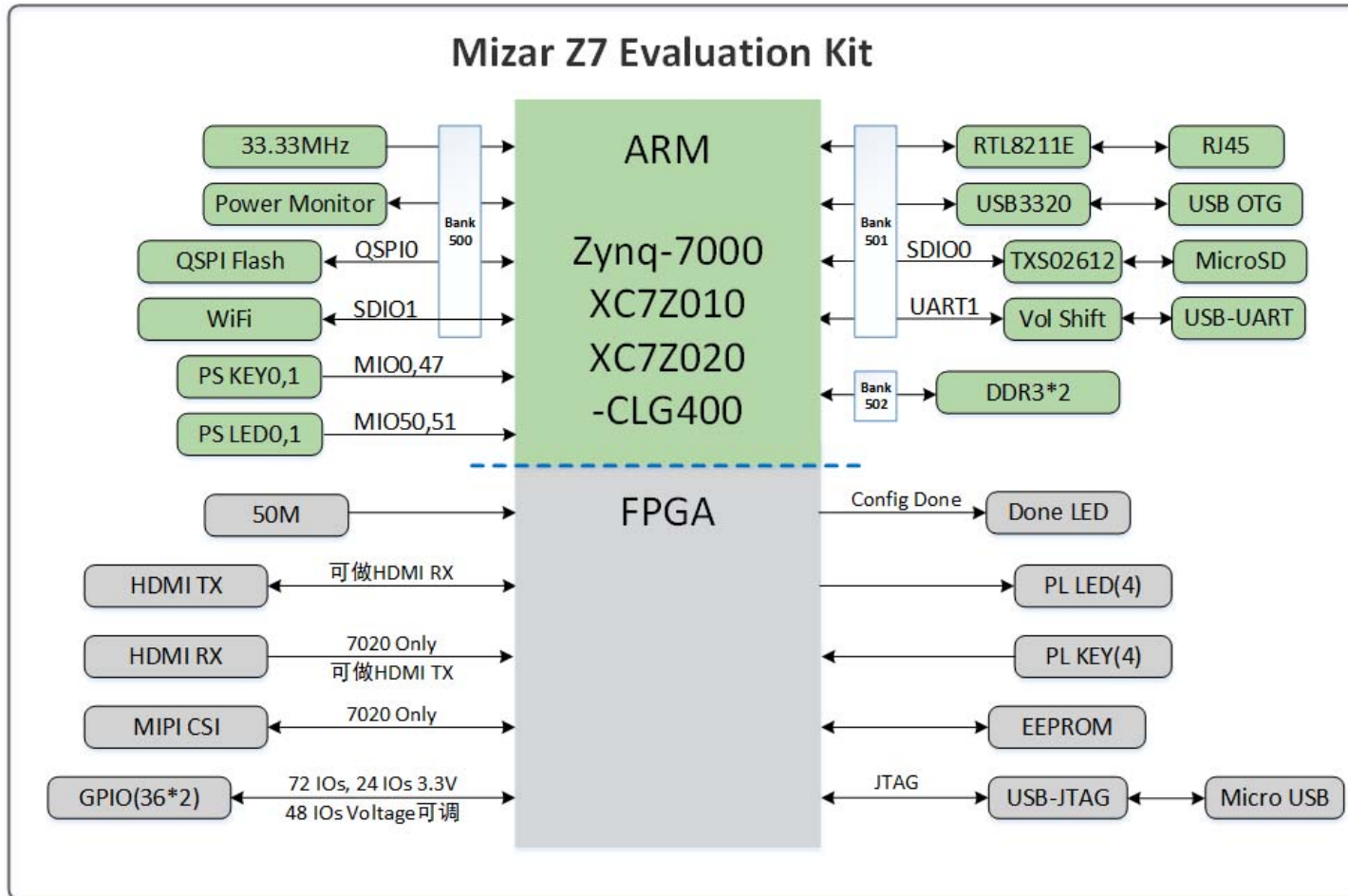


MIZAR Z7 Block Diagram

REV	DATE	PAGES	DESCRIPTION
1.0	01/05/2020	All	Rev 1.0 Release



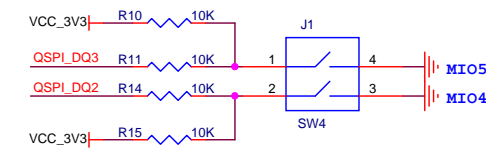
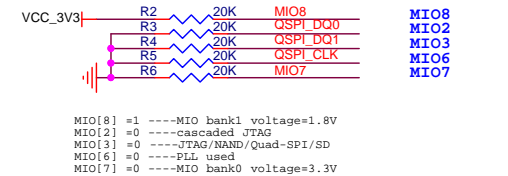
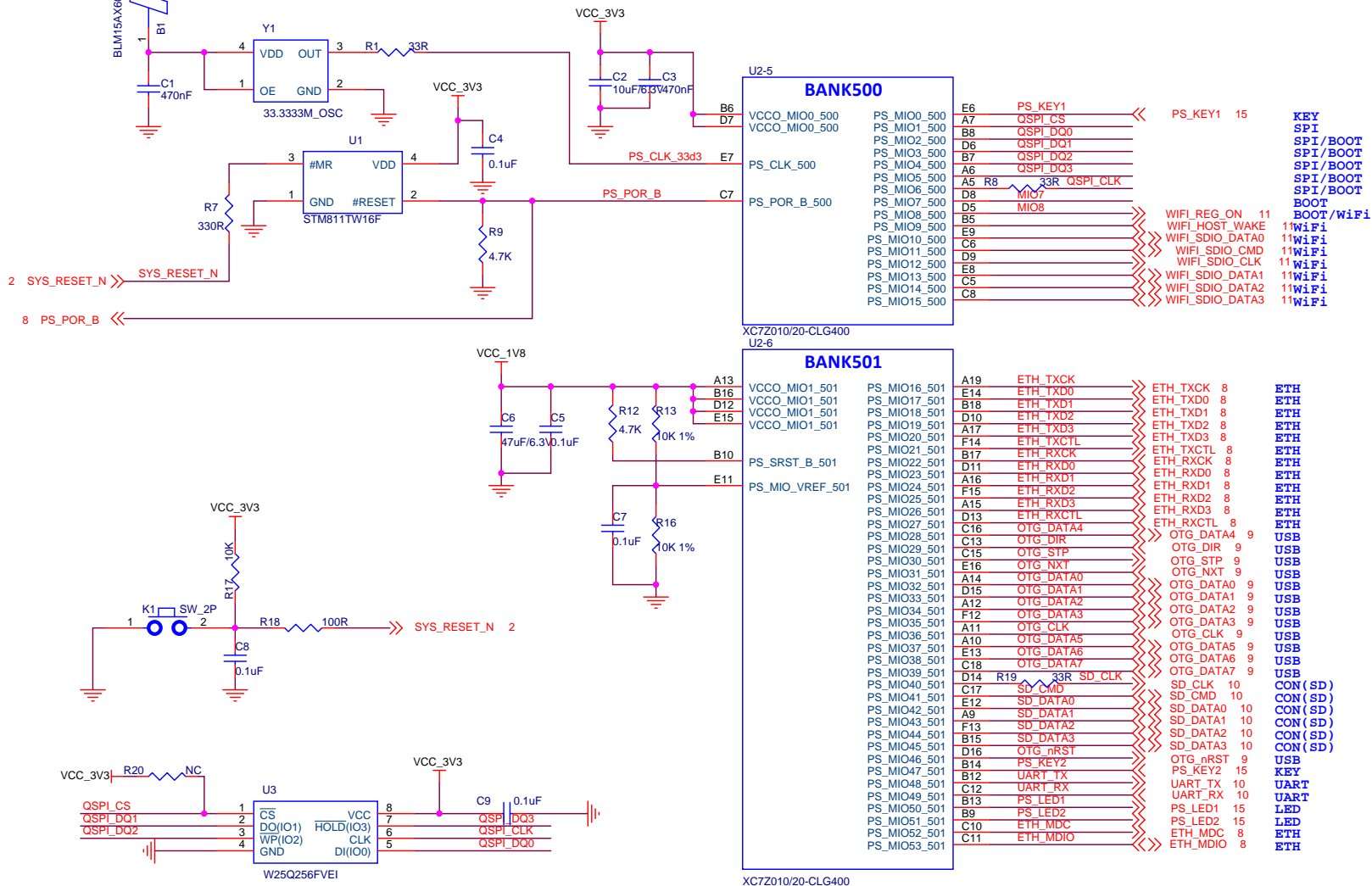
PAGE	DESCRIPTION
1	Title, Notes, Block Diagram, Revision History
2	PS BANK500&501
3	PS BANK502
4	PL BANK0&13
5	PL BANK34&35
6	ZYNQ Power
7	DDR3 RAM
8	PS ETH
9	PS USB OTG
10	PS UART SD
11	WiFi-BT
12	HDMI-TX
13	HDMI-RX
14	MIPI EEPROM CLOCK
15	KEY LED
16	USB JTAG
17	GPIO EXPANSION
18	Power
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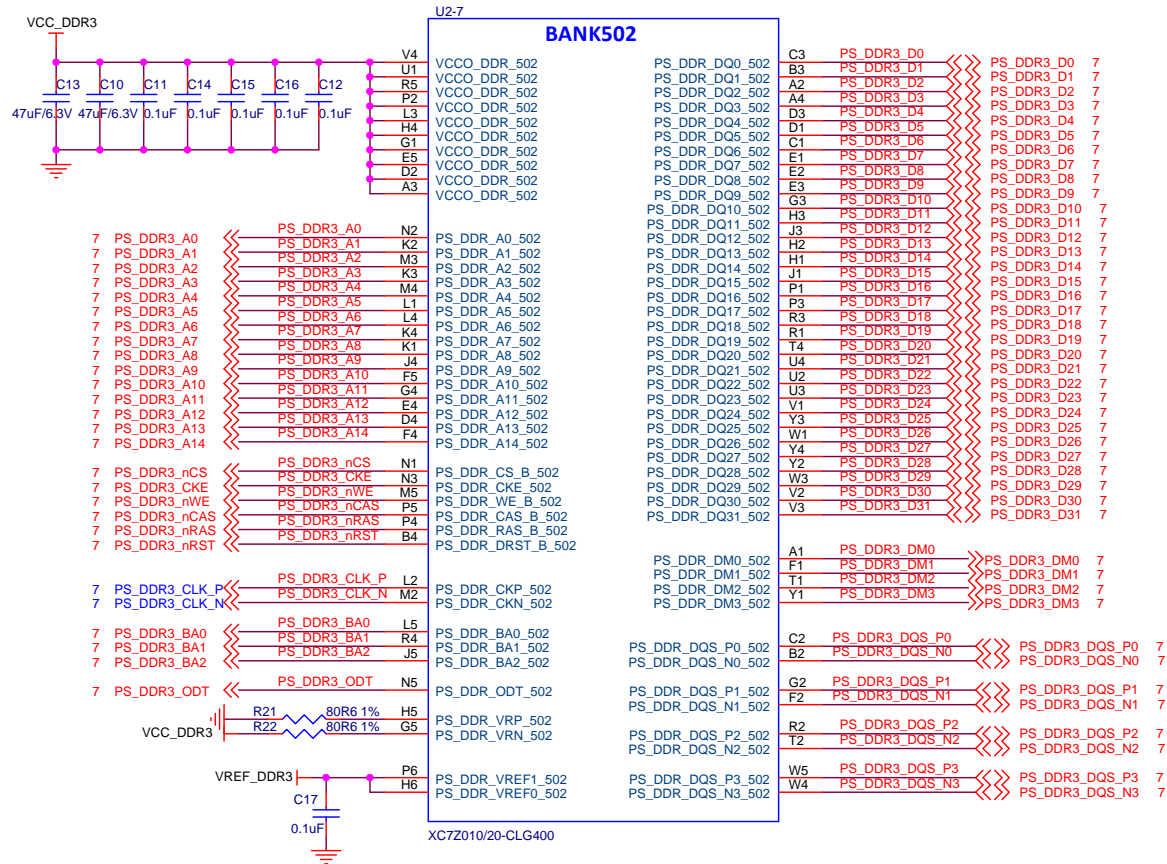
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Title Mizar Z7 Board			
Size B	Document Number	Rev 1.0	
Block Diagram			
Date:	Tuesday, May 05, 2020	Sheet	1 of 18

PS Bank 500 & 501

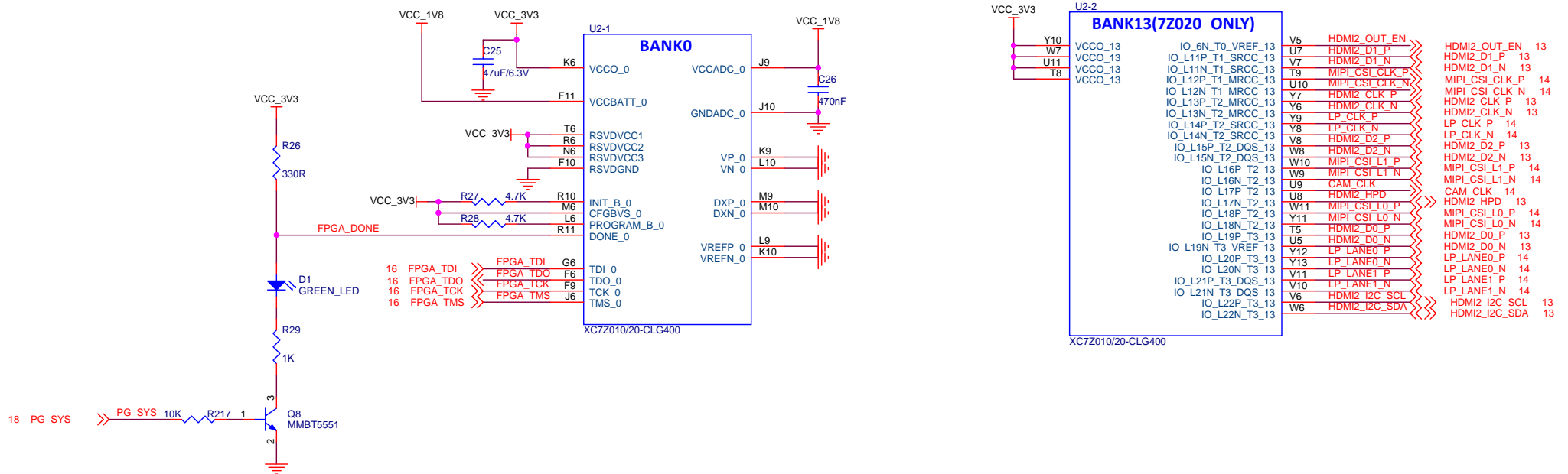


Boot Mode	MIO[5] (QSPI_DQ3)	MIO[4] (QSPI_DQ2)
JTAG	0	0
NAND	0	1
QSPI	1	0
SD Card	1	1

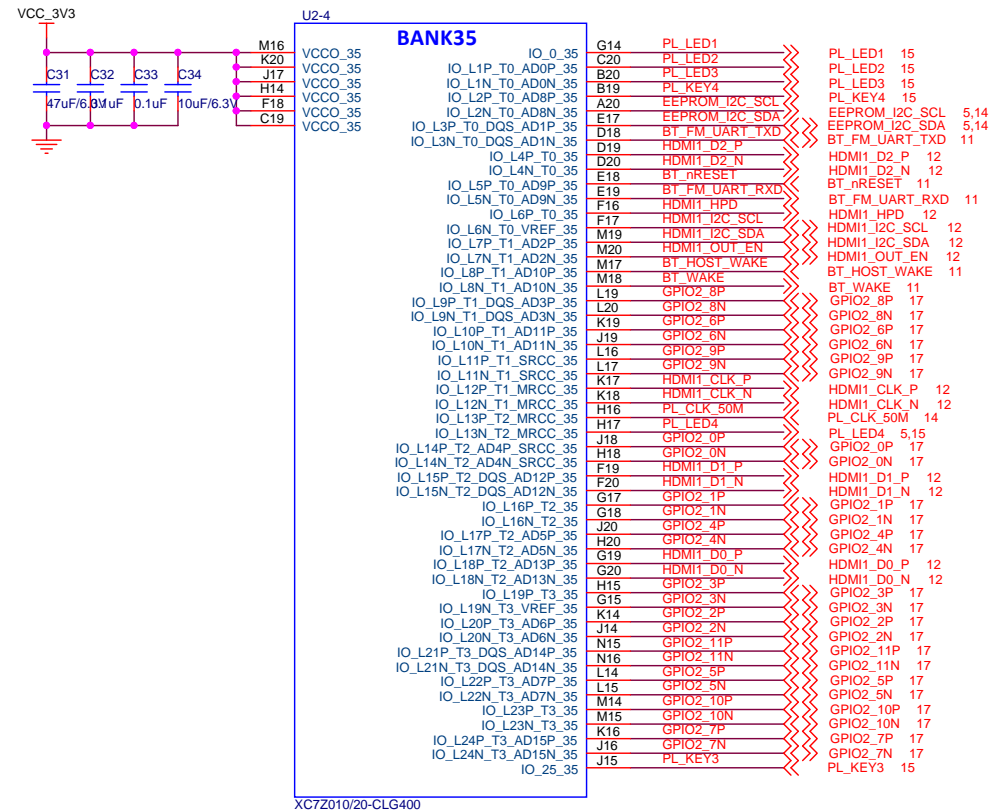
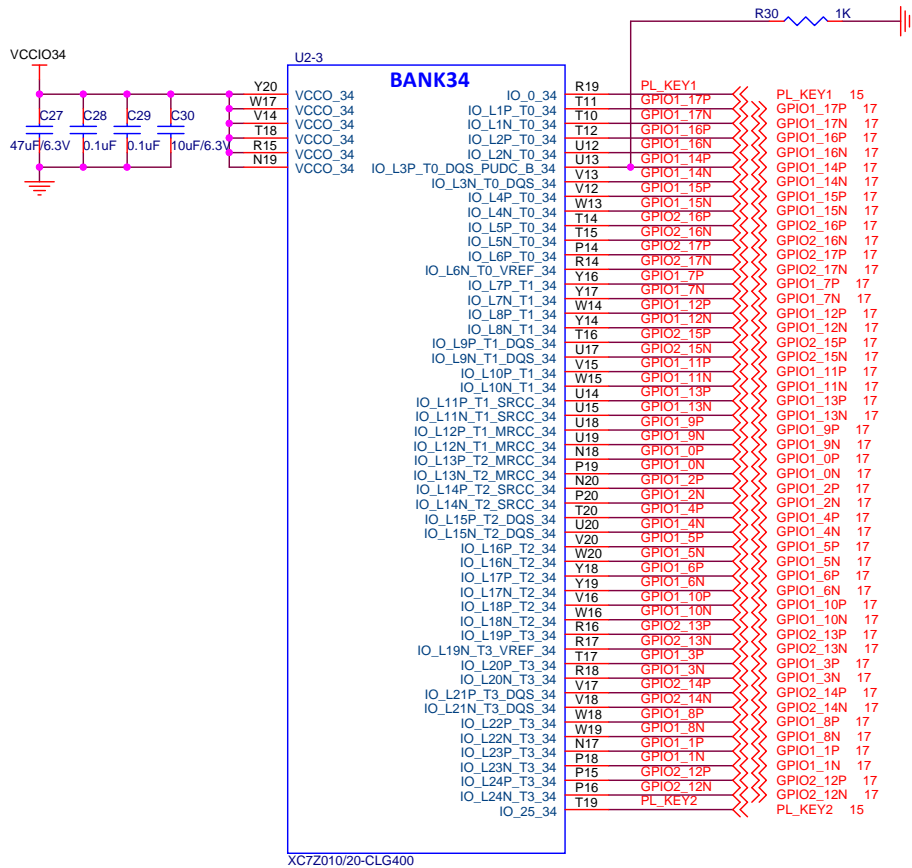
PS DDR Interface



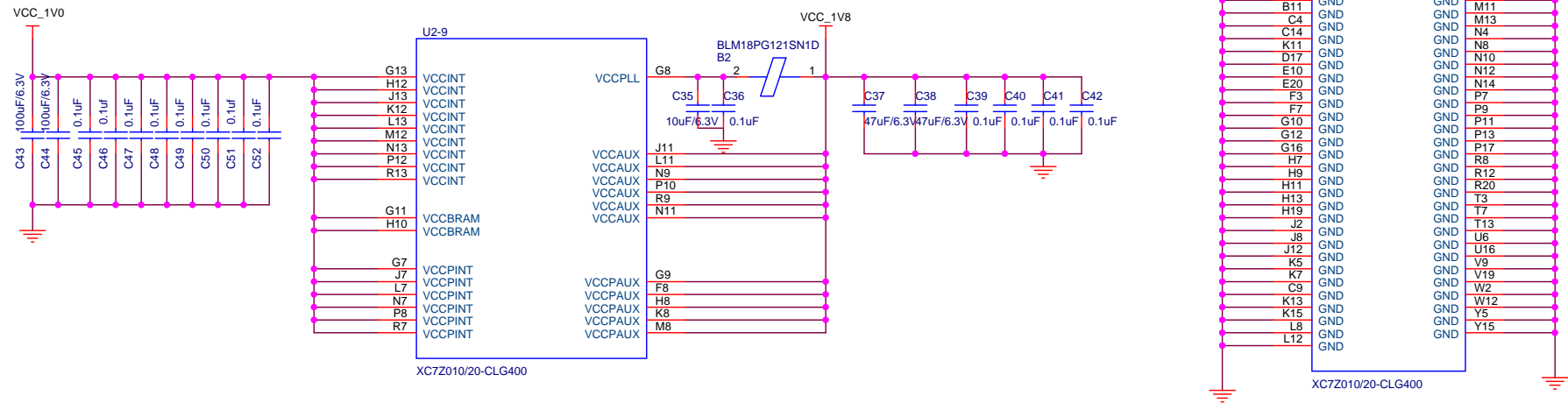
ZYNQ CONFIG



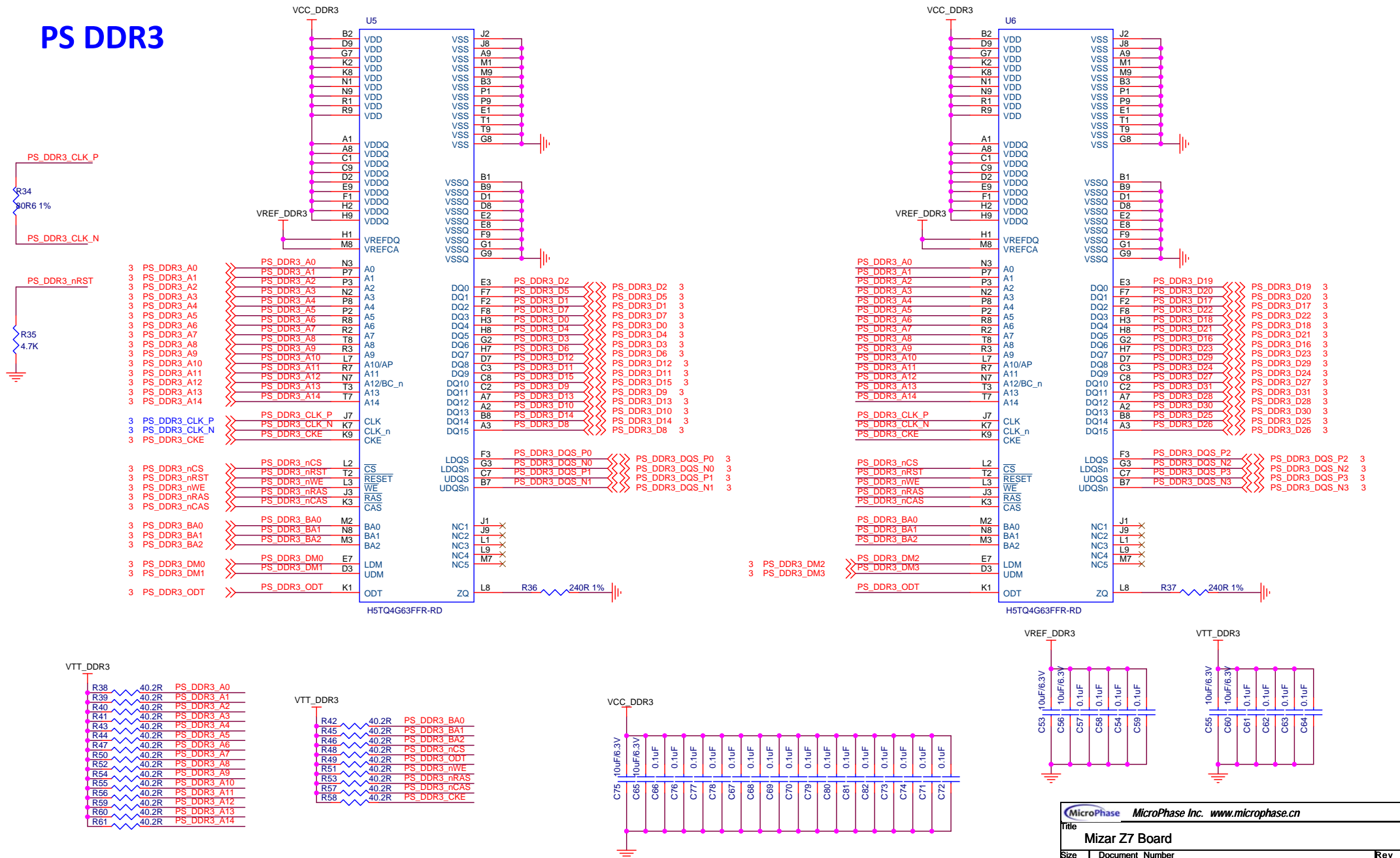
PL Bank 34 & 35



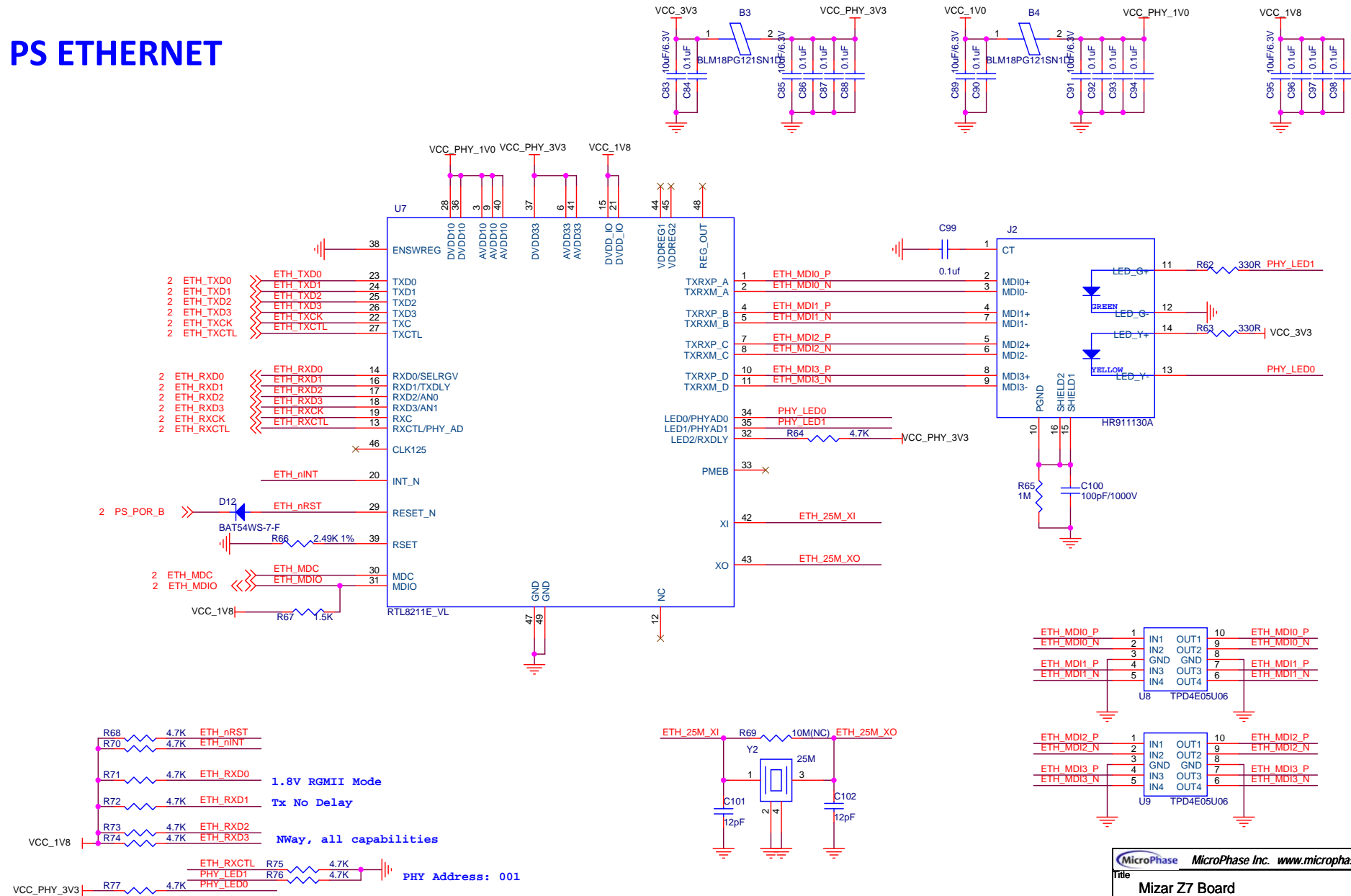
ZYNQ POWER



PS DDR3



PS ETHERNET



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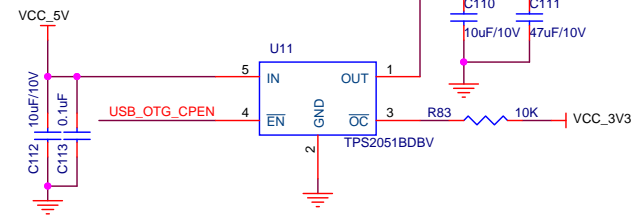
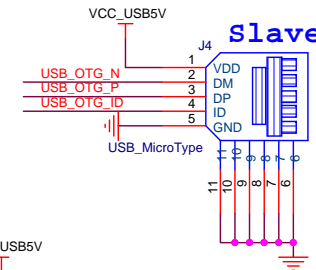
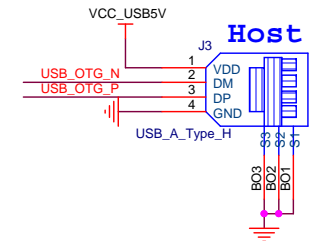
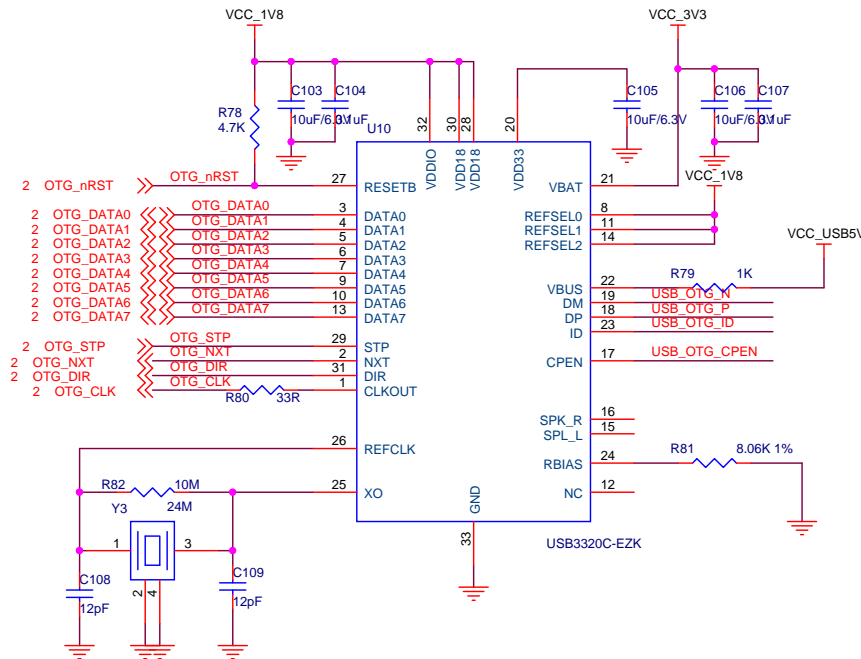
Mizar Z7 Board

Size B Document Number PS ETHERNET

Rev 1.0

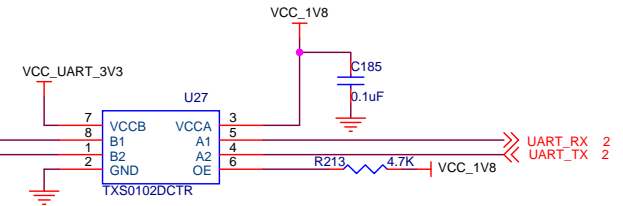
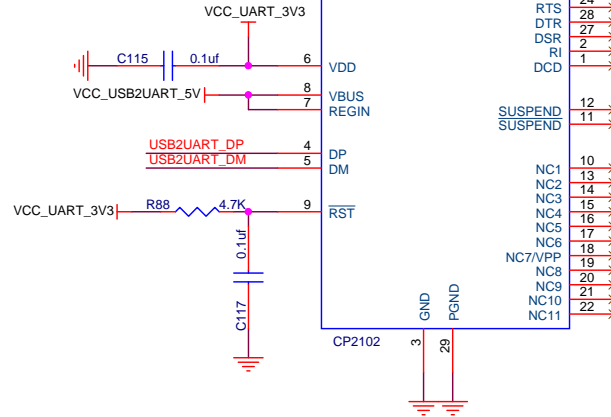
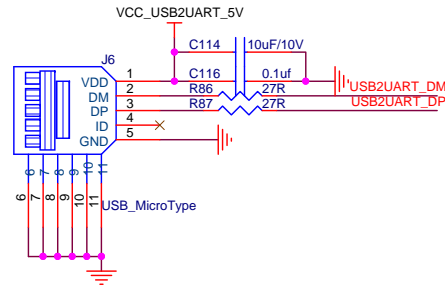
Date: Tuesday, May 05, 2020 Sheet 8 of 18

PS USB OTG

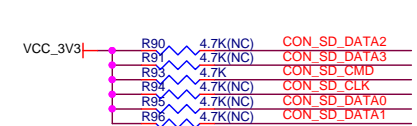
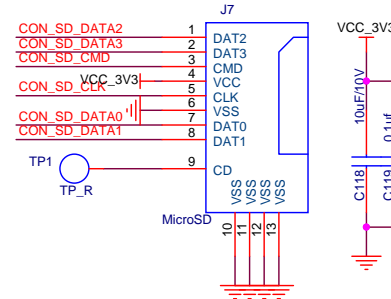
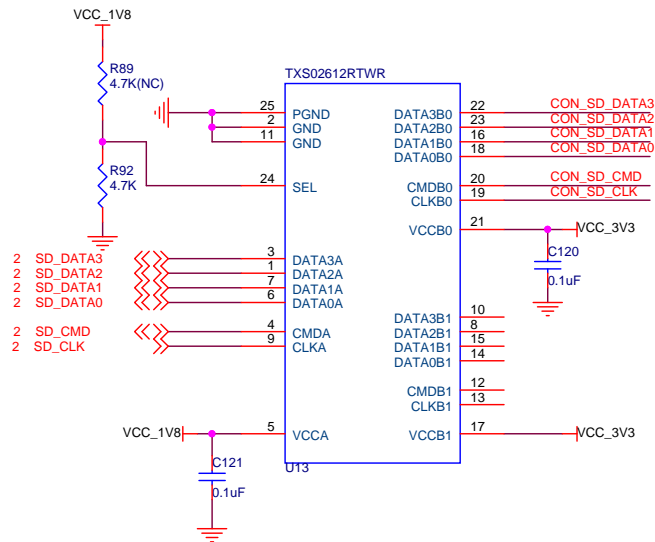


Configuration Pins			Description
REFSEL[2]	REFSEL[1]	REFSEL[0]	Reference Frequency
0	0	0	52 MHz
0	0	1	38.4 MHz
0	1	0	12 MHz
0	1	1	27 MHz
1	0	0	13 MHz
1	0	1	19.2 MHz
1	1	0	26 MHz
1	1	1	24 MHz

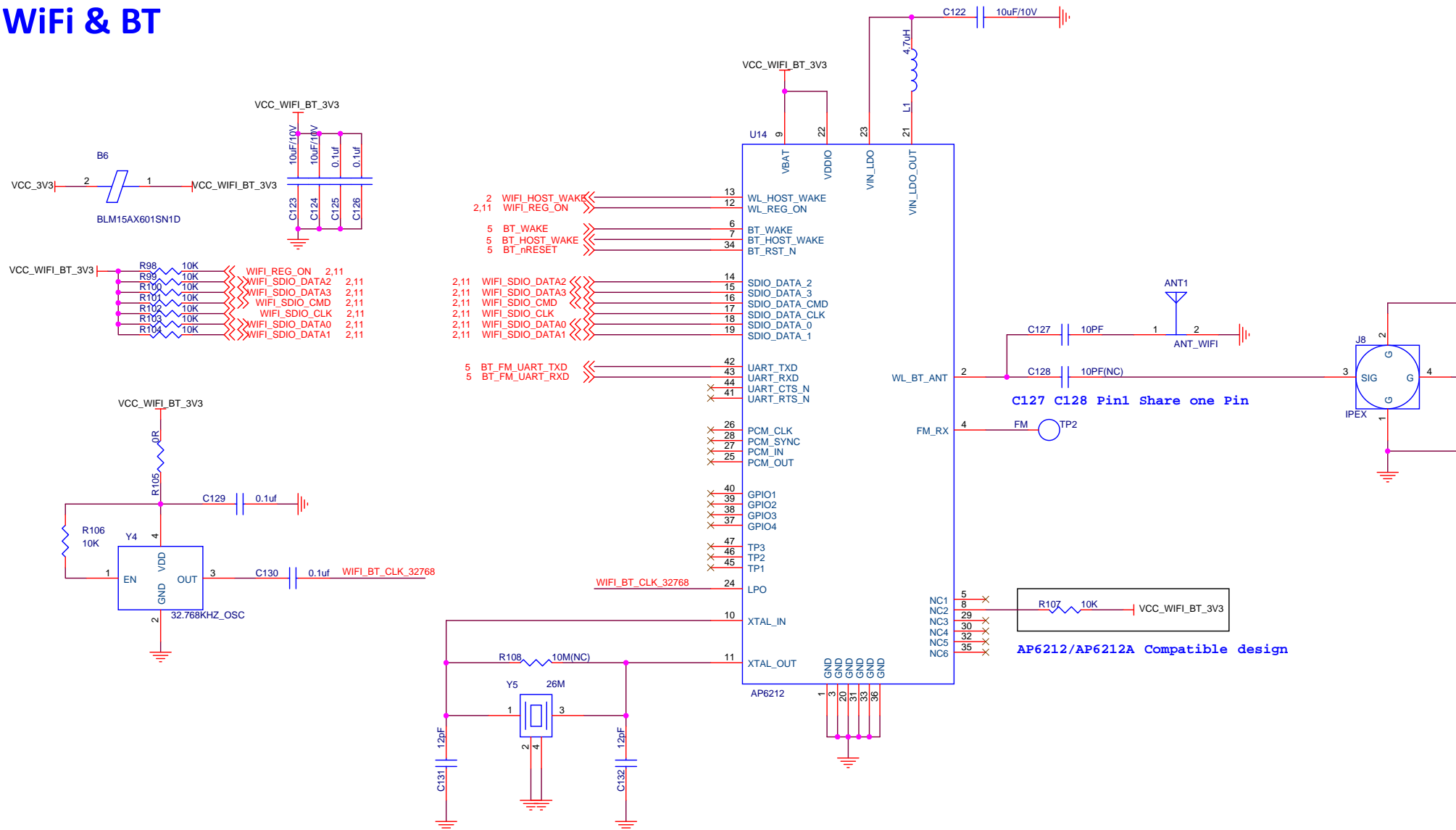
PS USB-UART



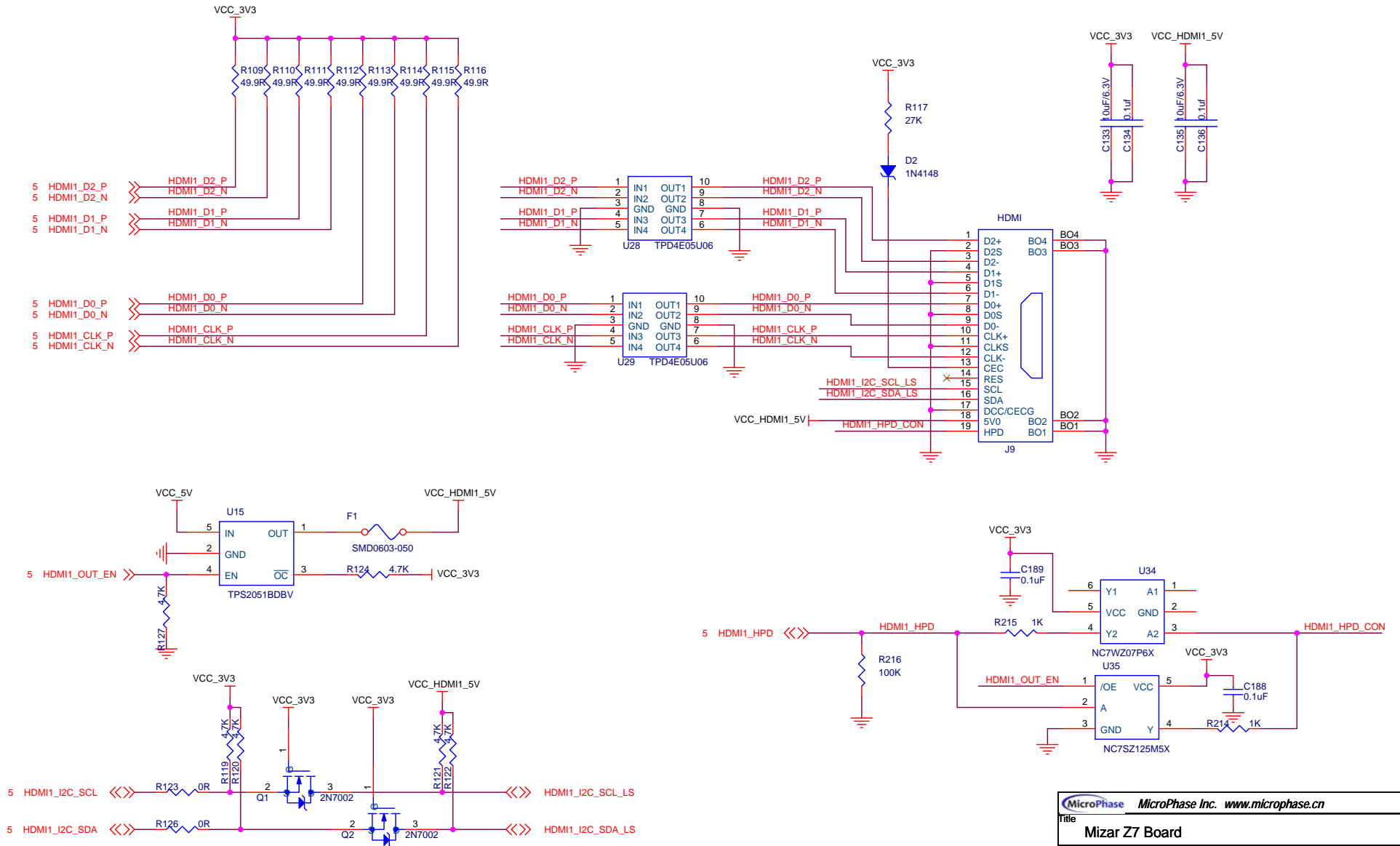
PS SD Card



WiFi & BT



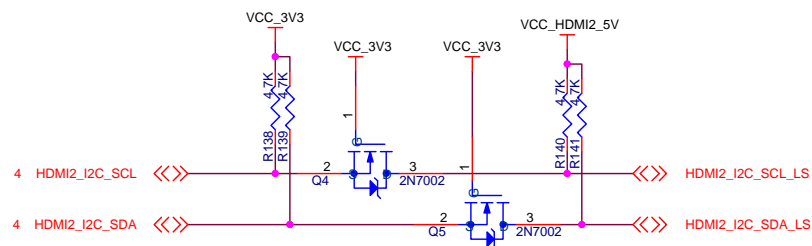
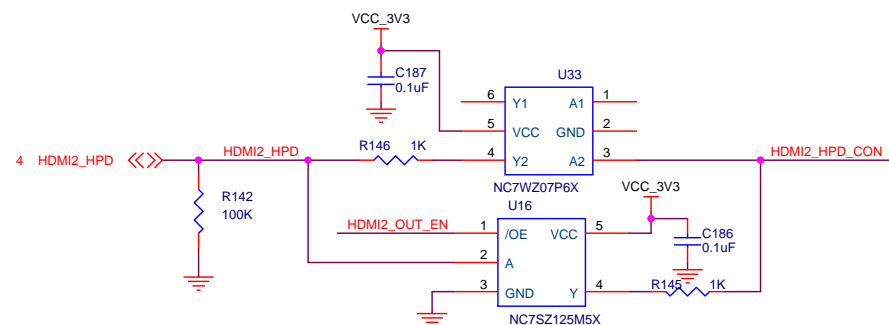
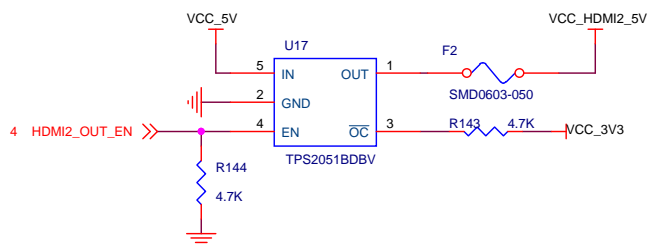
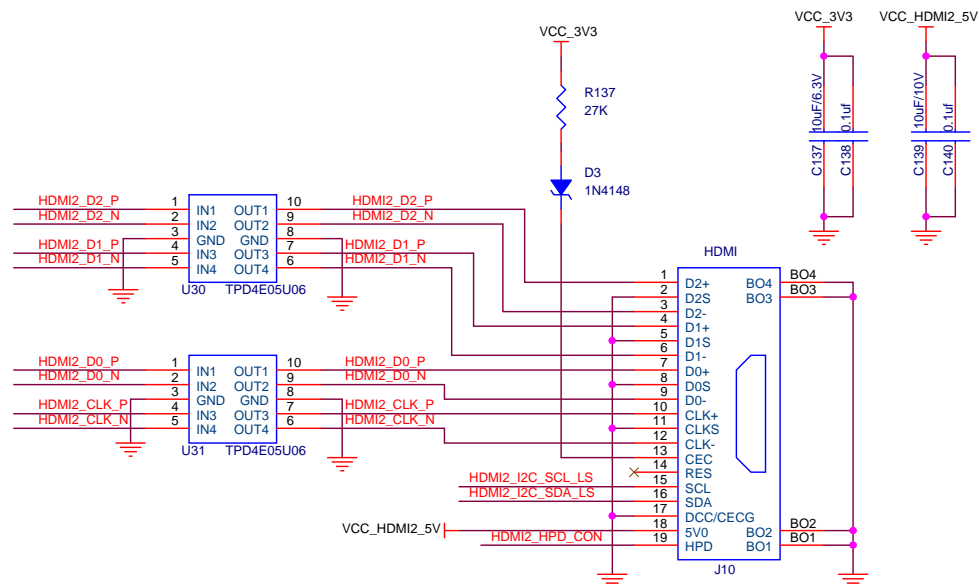
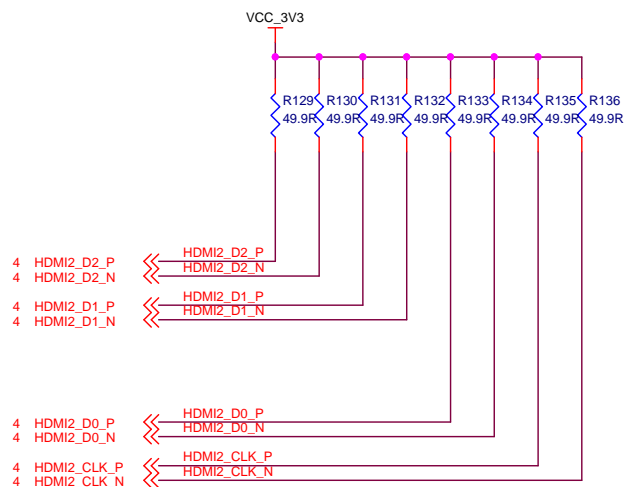
PL HDMI-TX



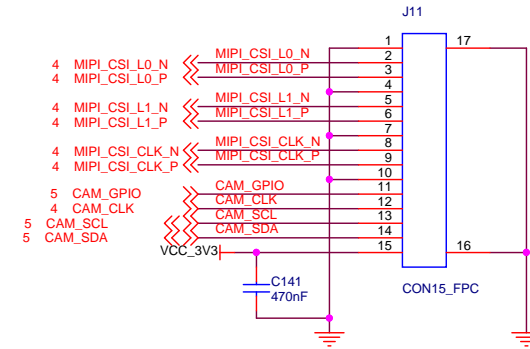
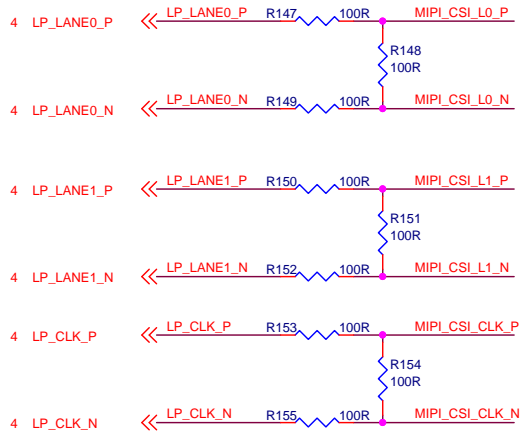
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Title		
Mizar Z7 Board		
Size	Document Number	Rev
B	HDMI TX	1.0
Date:	Tuesday, May 05, 2020	Sheet 12 of 18

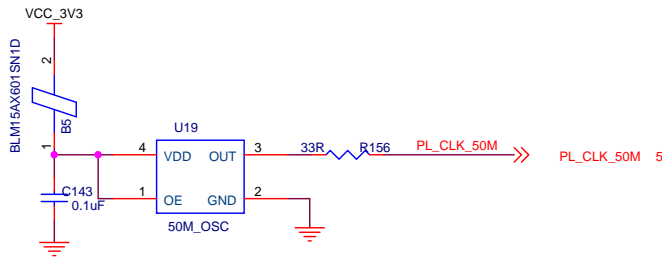
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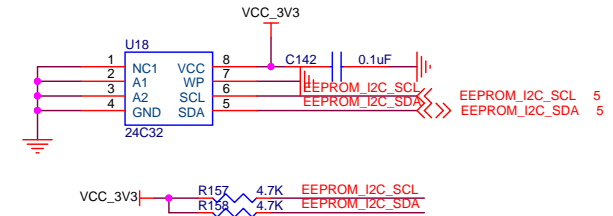
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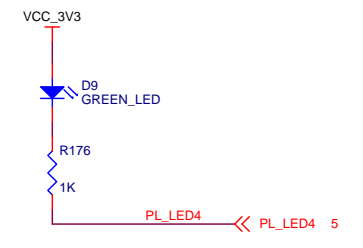
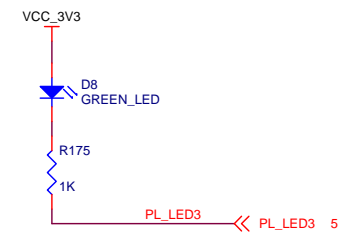
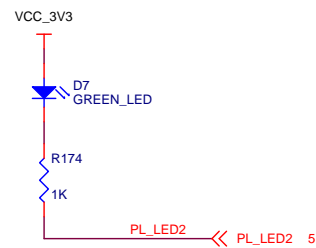
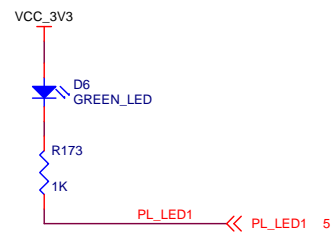
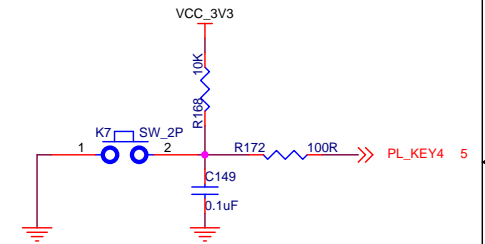
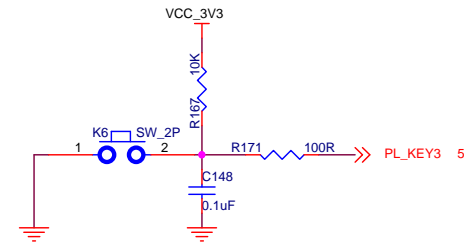
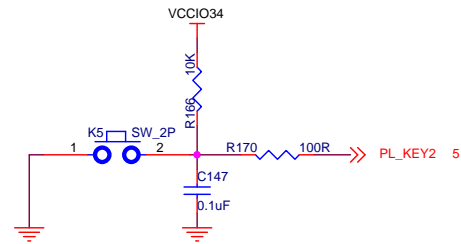
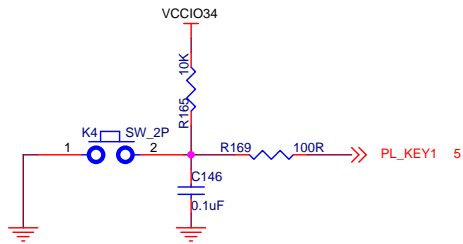
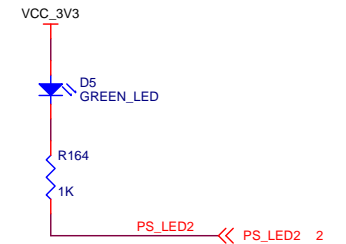
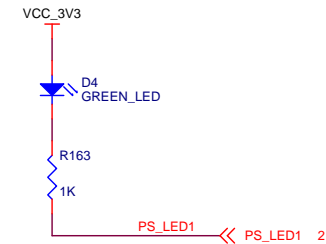
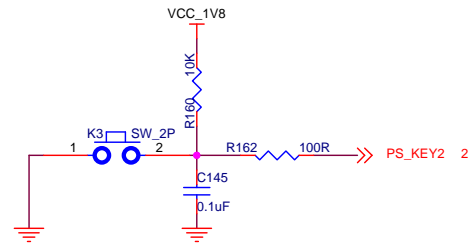
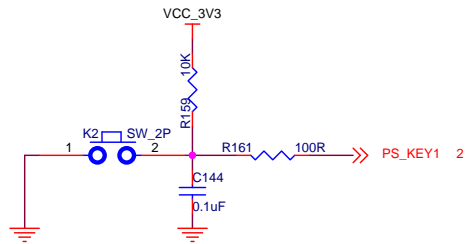
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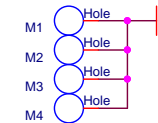
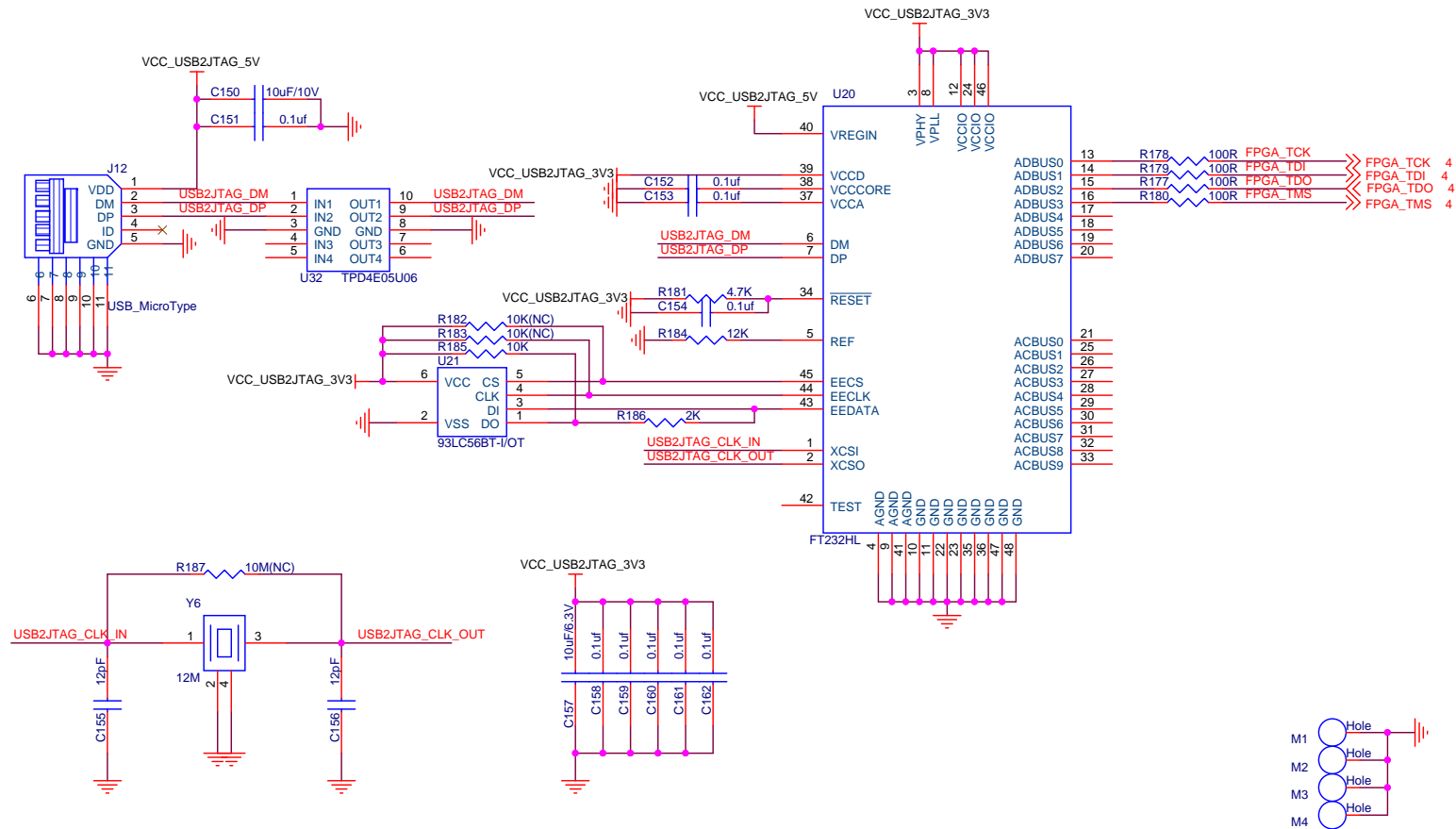
EEPROM



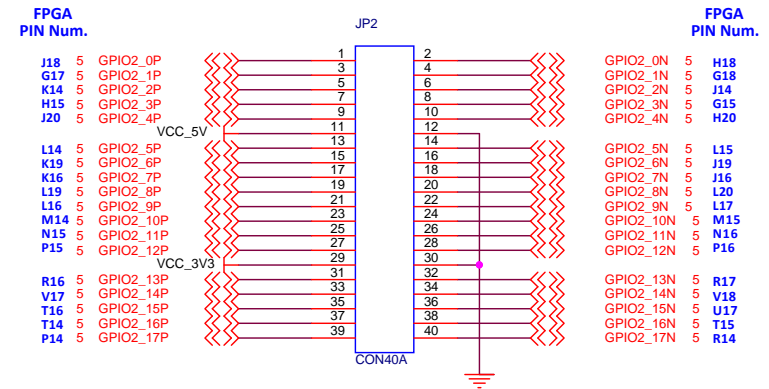
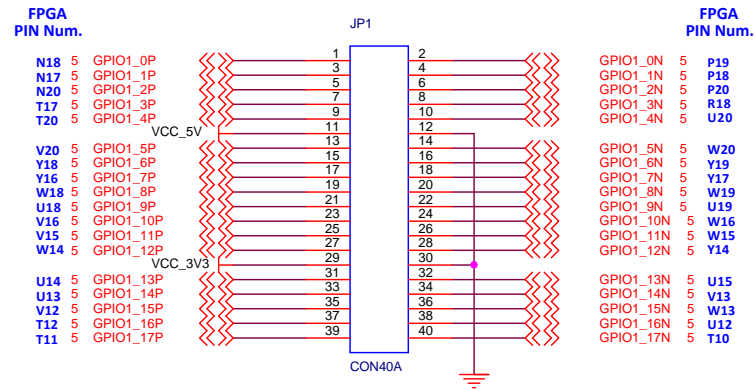
KEY & LED



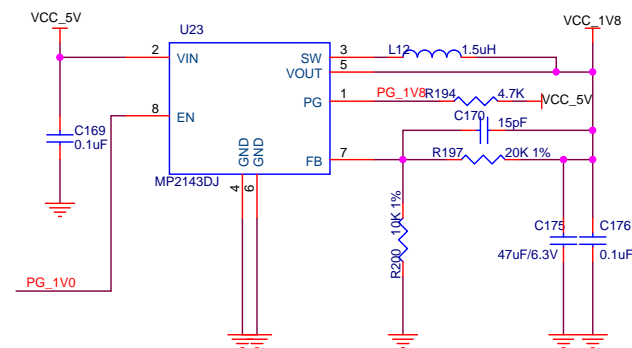
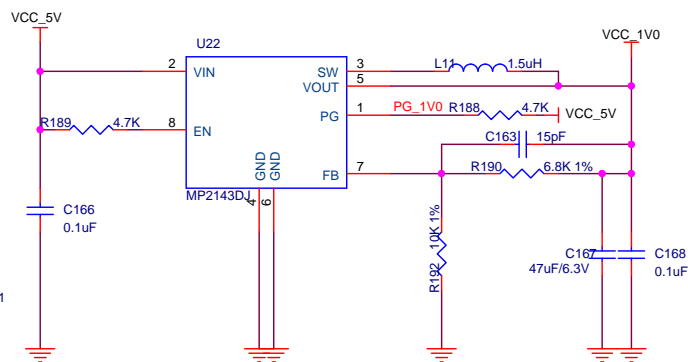
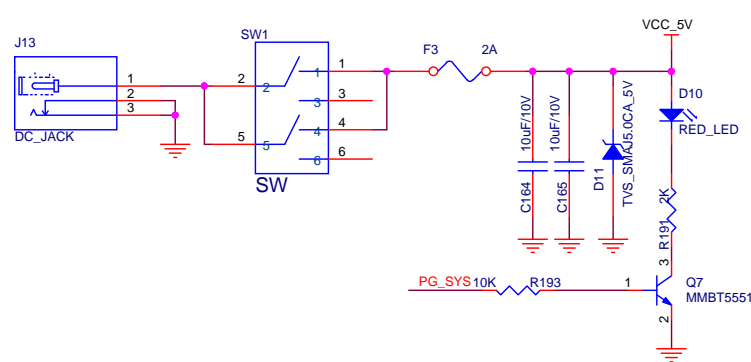
USB JTAG



GPIO Interface

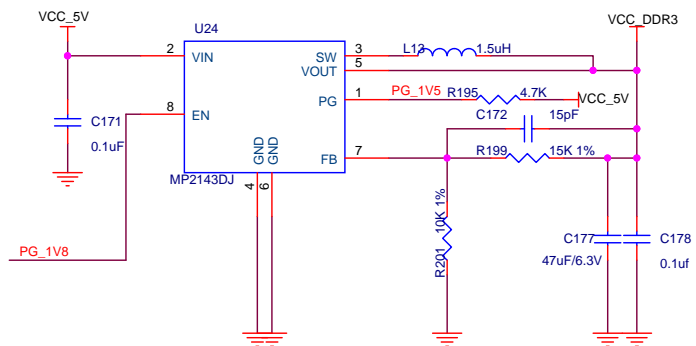


POWER

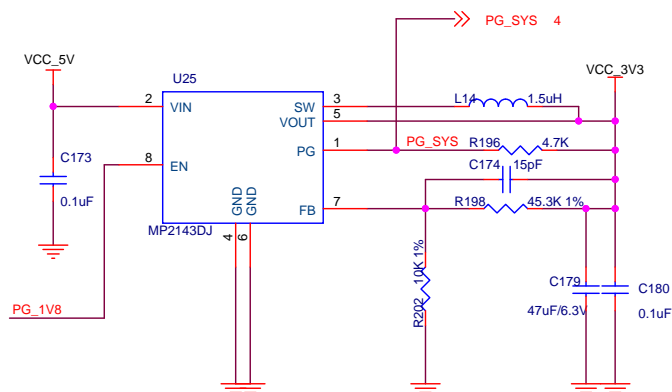


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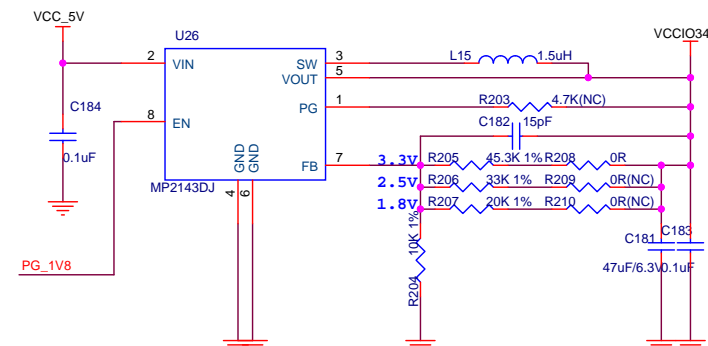
Vref = 0.6V VOUT = 1.8V



Vref = 0.6V VOUT = 1.5V



Vref = 0.6V VOUT = 3.3V



Vref = 0.6V VOUT = ADJ