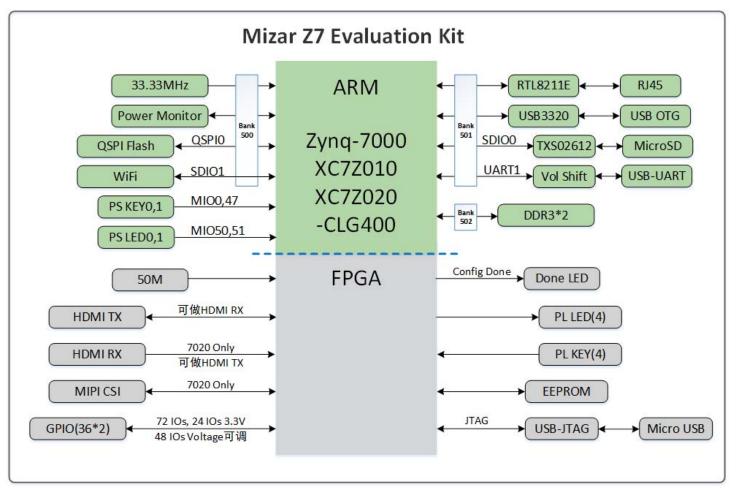
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MIZAR Z7 Block Diagram

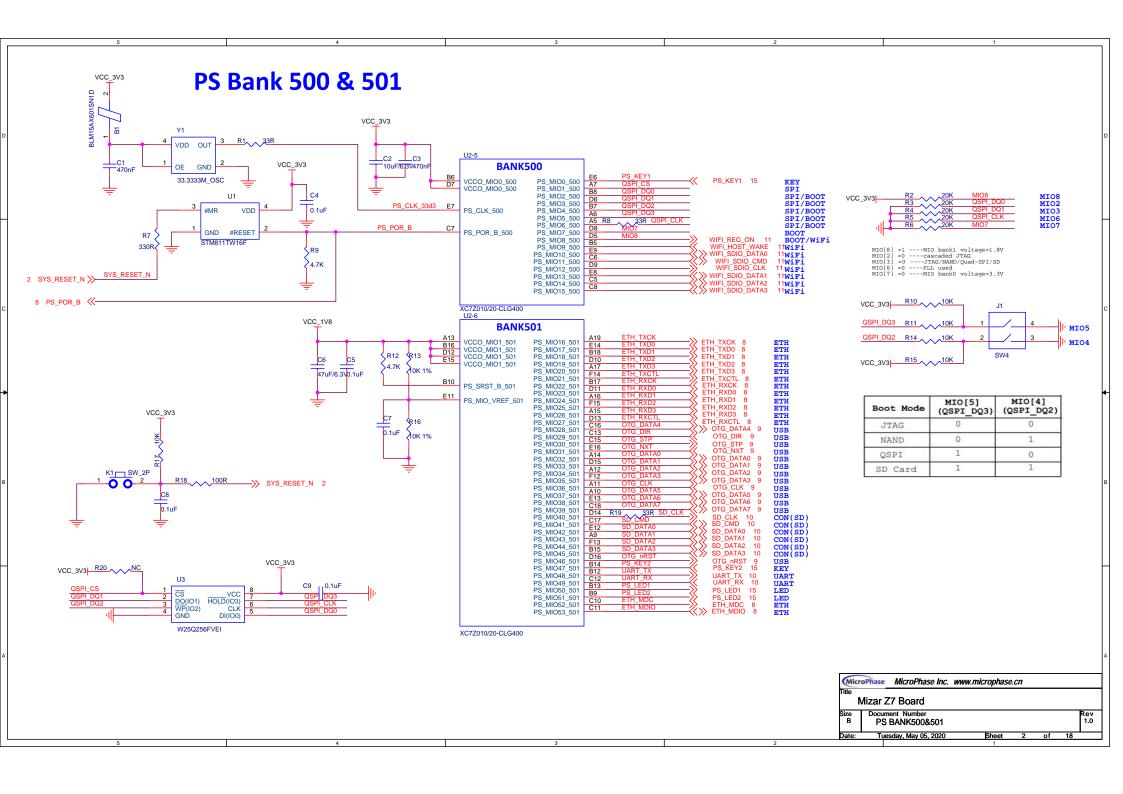


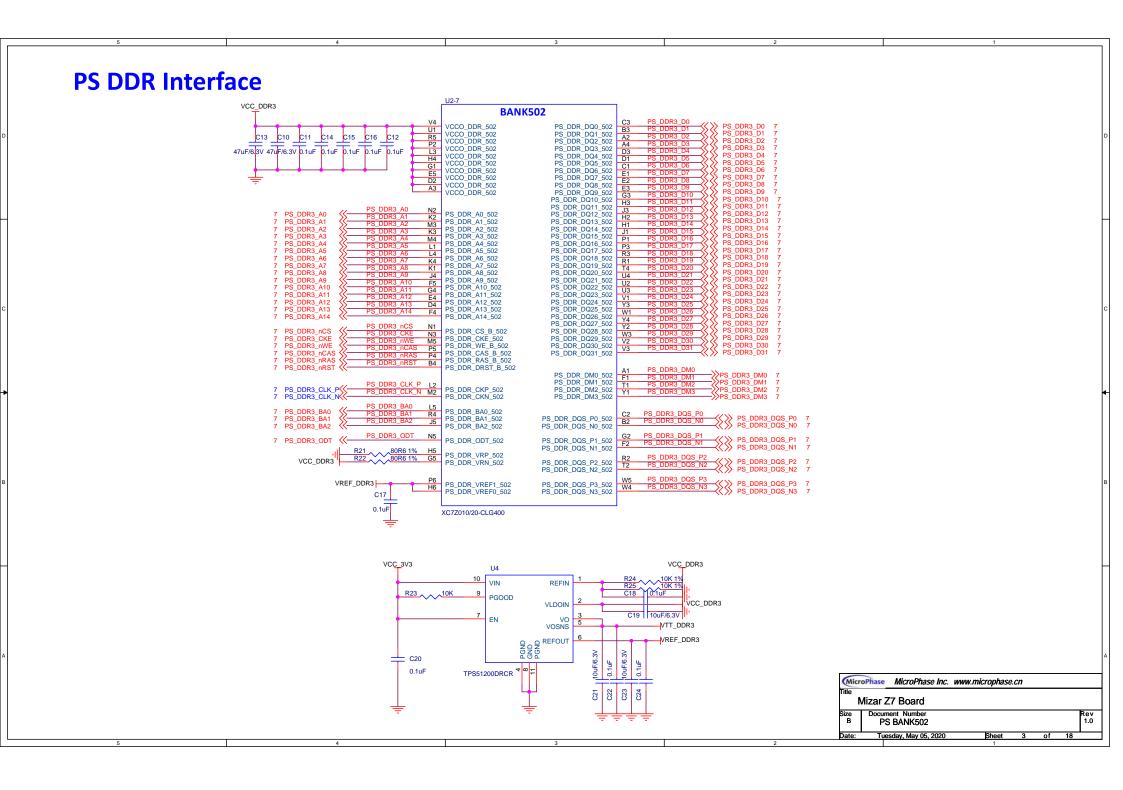
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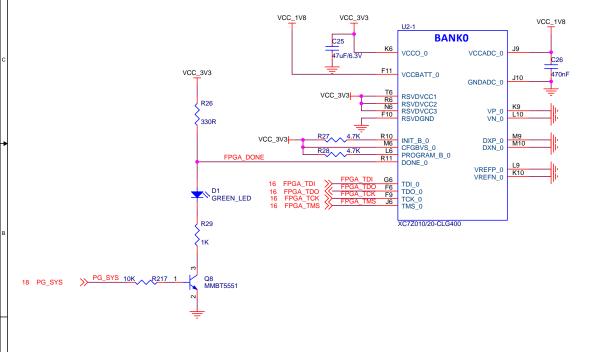
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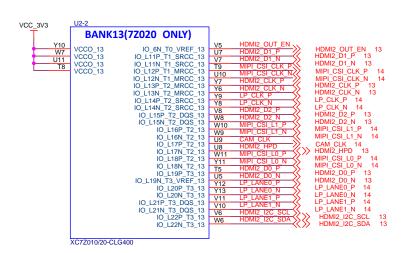
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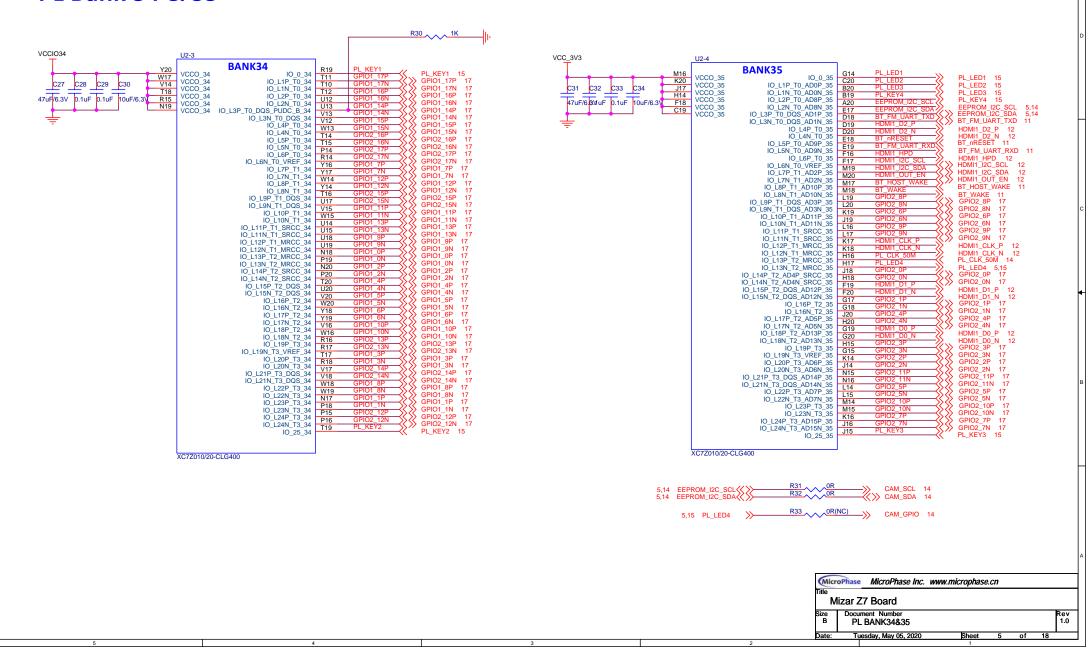


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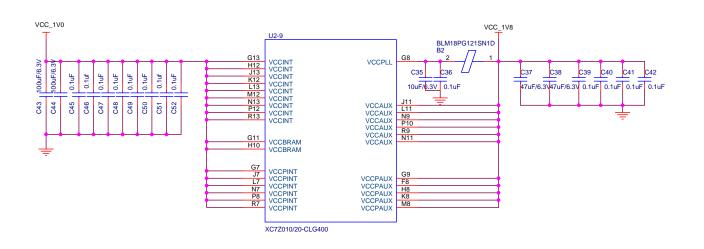


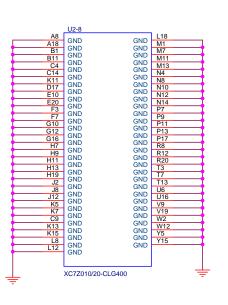


PL Bank 34 & 35

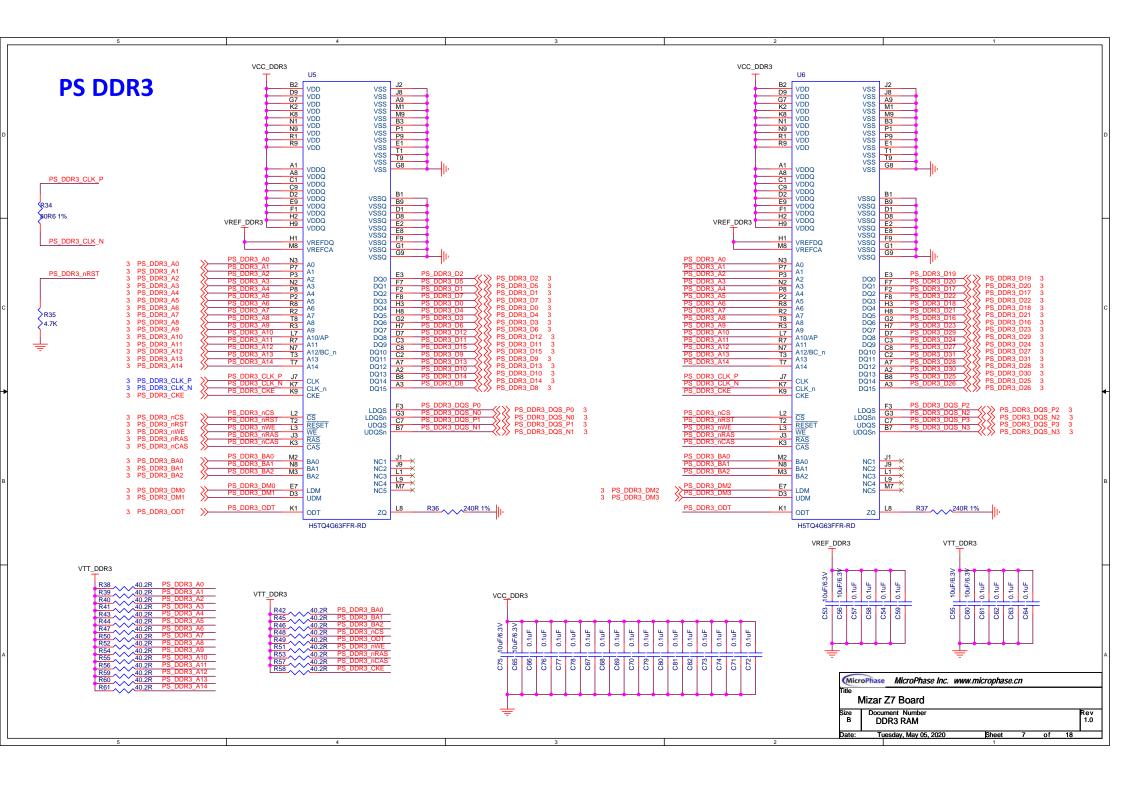


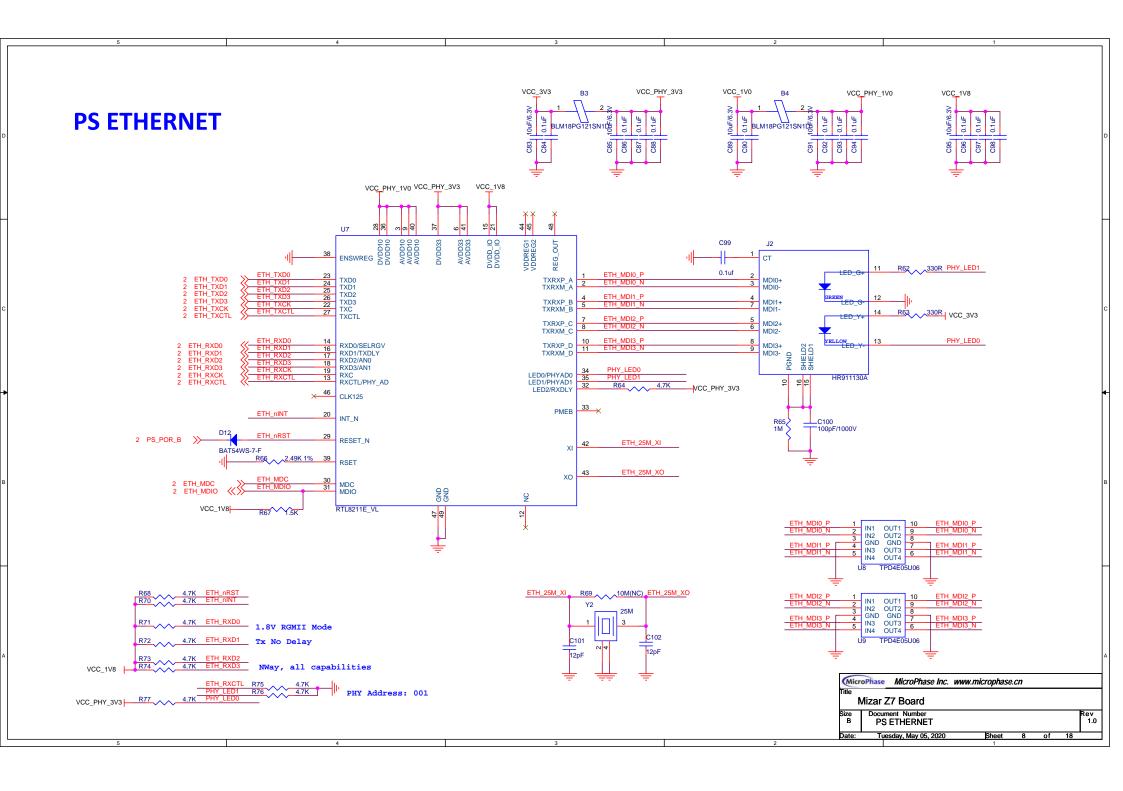
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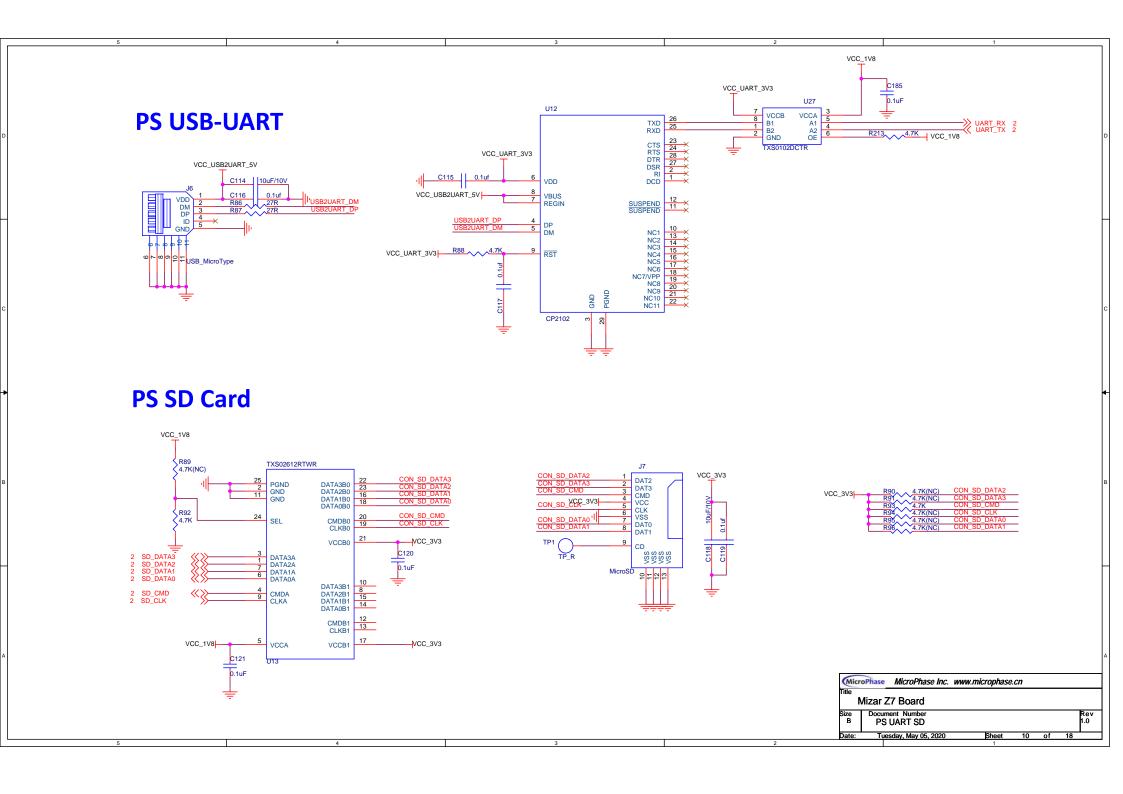


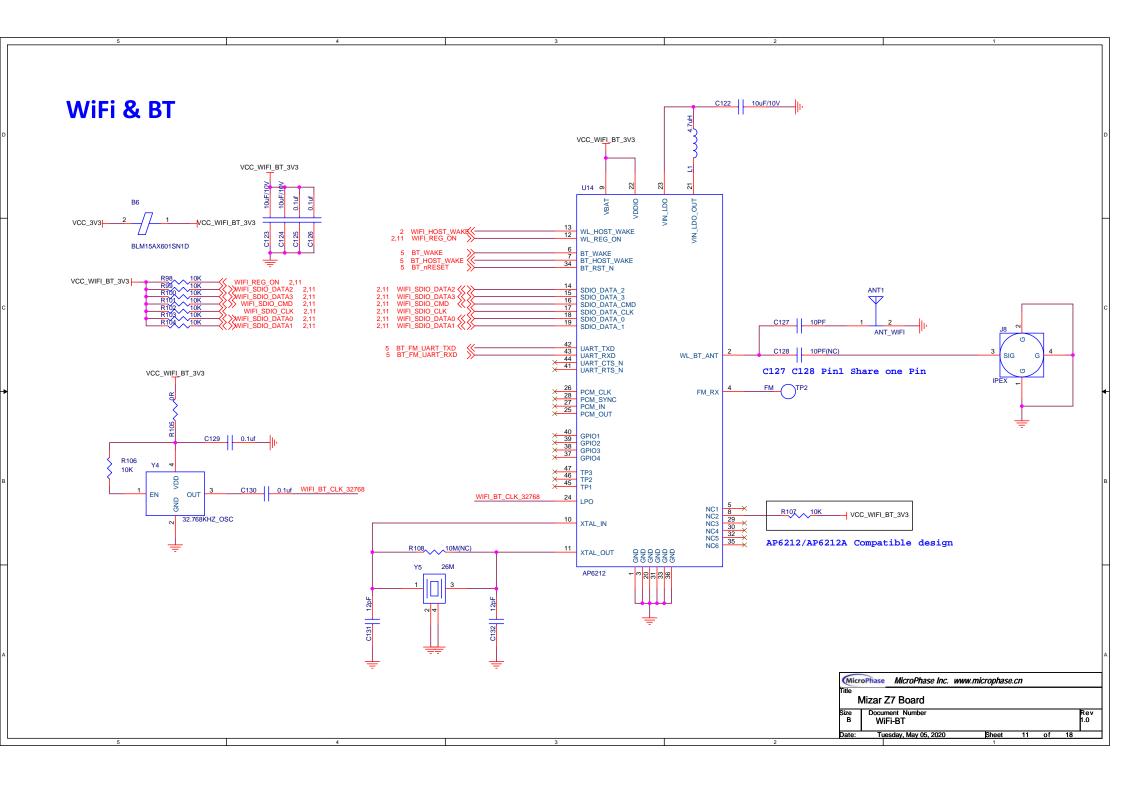
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Mizar Z7 Board
Size Document Number ZYNQ Power
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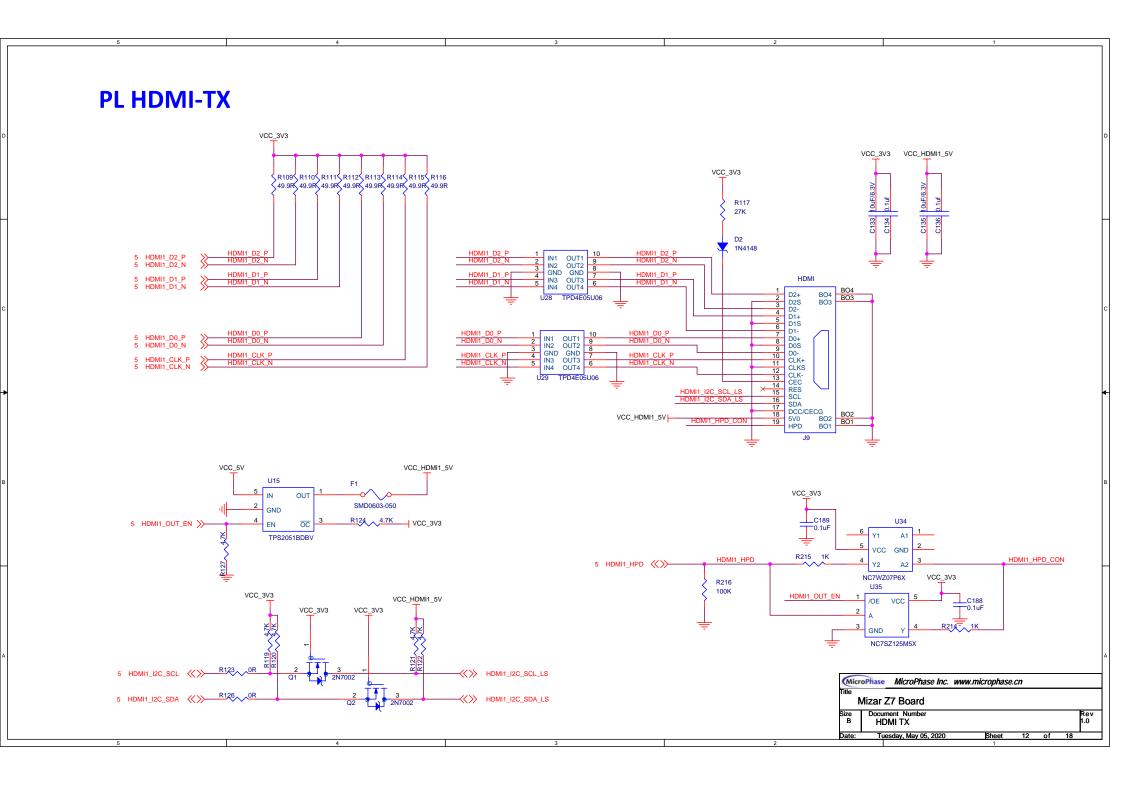


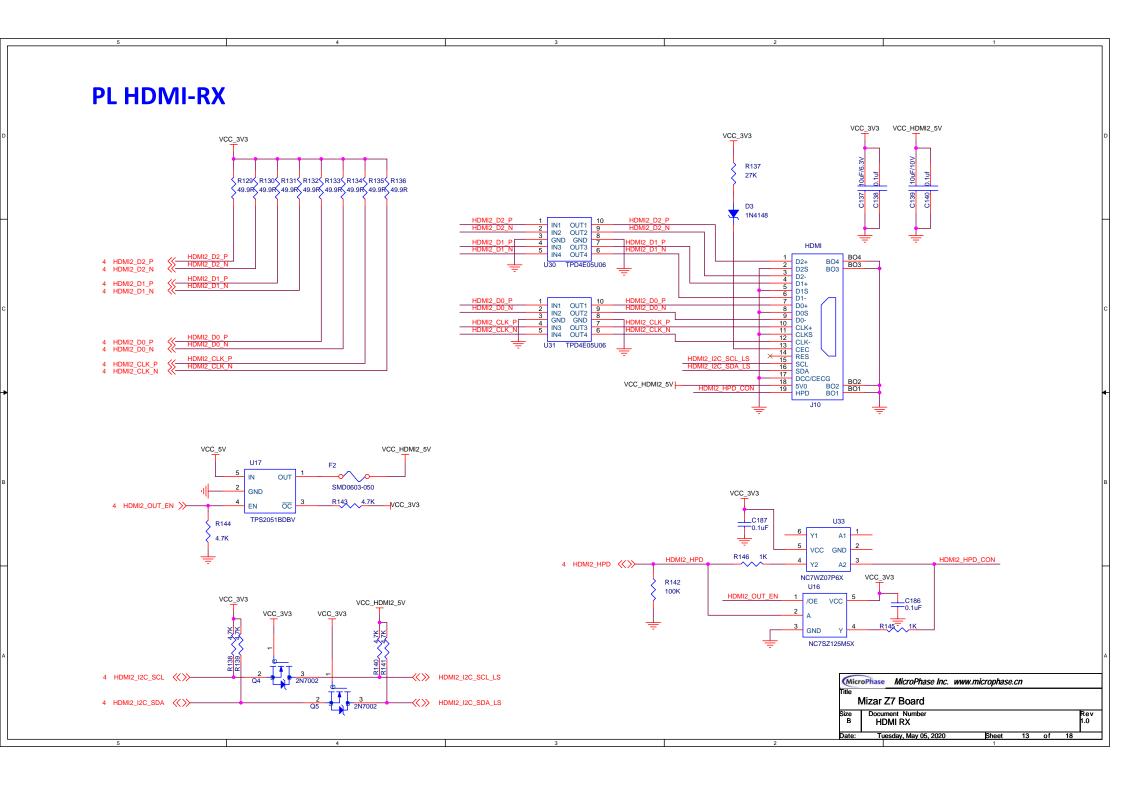


PS USB OTG VCC_1V8 VCC_3V3 C105 C103 C104 C106 C107 10uF/6.0MuF_{U10} VCC_USB5V 10uF/6.**3**MuF 10uF/6.3V Host RESETB O VDD18 VDD18 VDD DM DP 2 OTG_nRST >> OTG_nRST VCC_1V8 VBAT 2 OTG_DATA0 2 OTG_DATA1 2 OTG_DATA2 GND REFSEL0 REFSEL1 REFSEL2 DATA0 USB_A_Type_H VCC_USB5V 5 DATA2 2 OTG_DATA2 2 OTG_DATA4 2 OTG_DATA5 2 OTG_DATA6 2 OTG_DATA7 7 DATA3 DATA4 R79 9 DATA5 VBUS DM USB_OTG_N 10 DATA5 DATA6 DATA7 ID 2 OTG_STP 2 OTG_NXT 2 OTG_DIR STP USB_OTG_CPEN NXT CPEN VCC_USB5V SPK_R 16 15 Slave SPL_L 26 REFCLK 8.06K 1% VDD 24 DM DP ID RBIAS NC 24M USB3320C-EZK J5 C108 CON2 12pF VCC_USB5V C110 C111 VCC_5V 10uF/10V 47uF/10V C112 10uF/10V OUT GND R83 VCC_3V3 ĒΝ OC **Configuration Pins** Description TPS2051BDBV REFSEL[0] REFSEL[2] REFSEL[1] Reference Frequency 52 MHz 0 0 0 0 38.4 MHz 0 1 0 12 MHz 0 1 1 27 MHz 0 0 13 MHz 1 1 0 19.2 MHz MicroPhase Inc. www.microphase.cn 1 0 26 MHz Mizar Z7 Board 24 MHz Document Number PS USB OTG Rev 1.0 Tuesday, May 05, 2020 Sheet

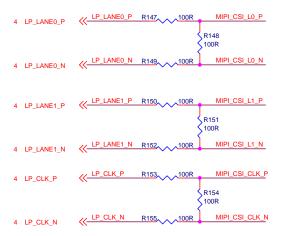






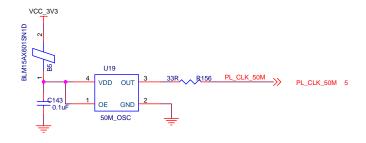


MIPI CSI

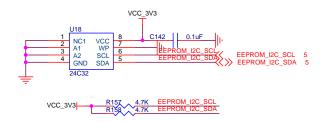


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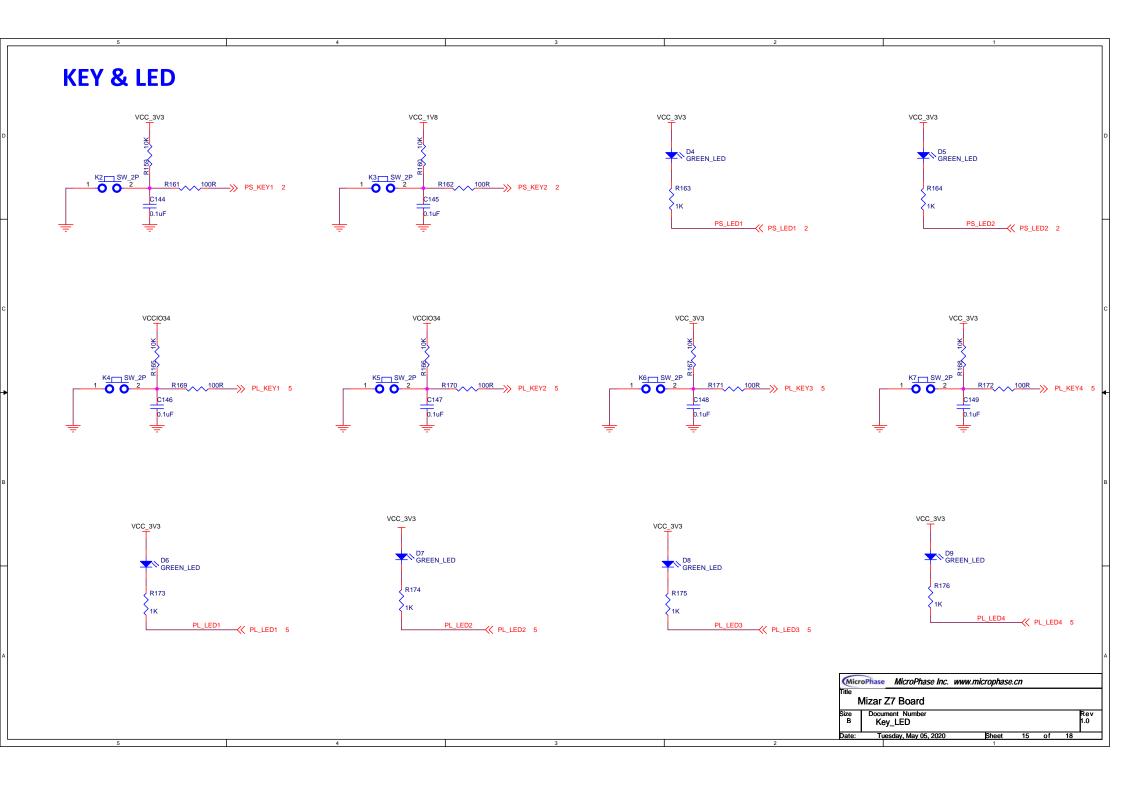
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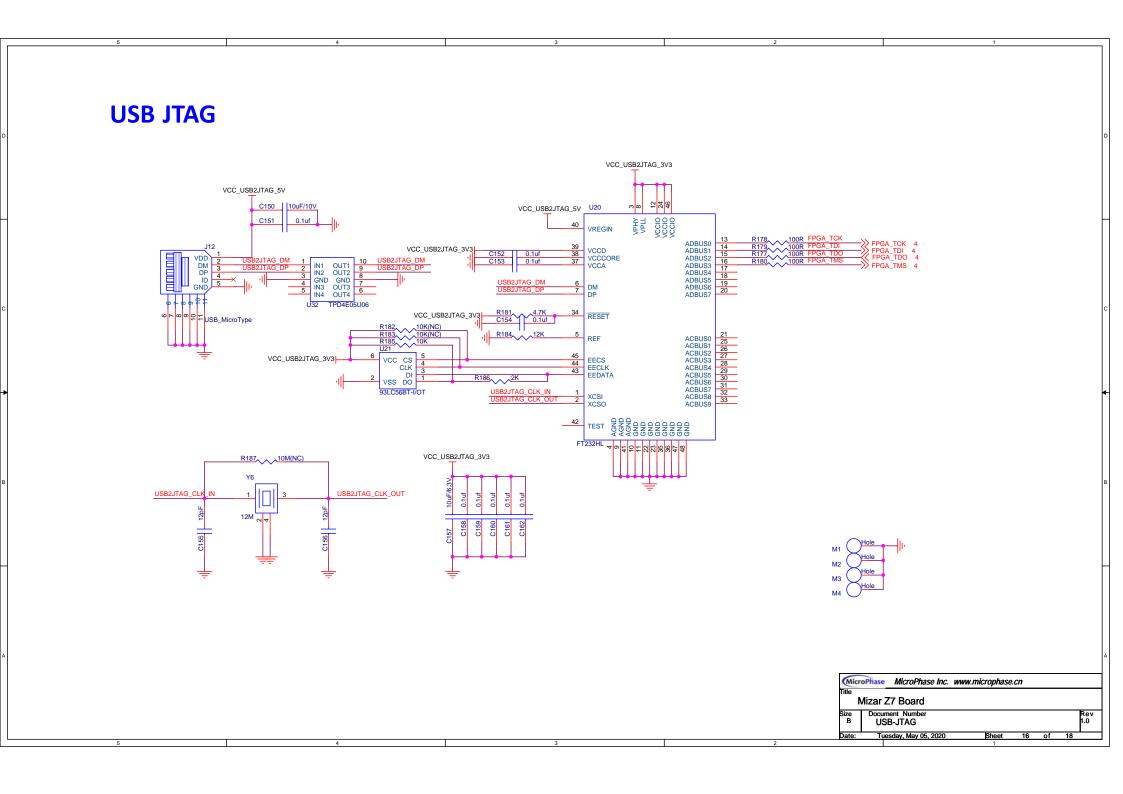


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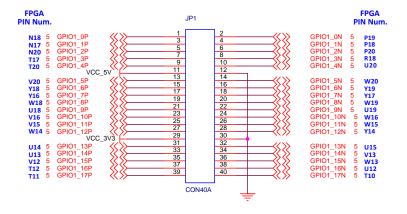


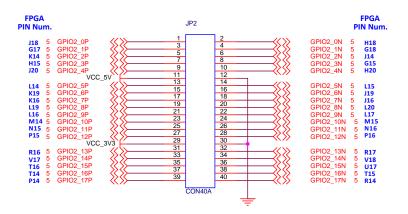
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GPIO Interface





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Mizar Z7 Board

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